Tarzan™ MQ7250ASIP/IBA
Non-isolated 8~14VDC input, 0.75~5.5V output, 10A DC-DC Converter

FEATURES

- Wide operating voltage: 8V ~ 14V
- Output Current up to 10A
- Output voltage ripple: 20mV$_{PP}$
- High Efficiency 95%
- Overcurrent /shortcircuit protection
- Over-temperature protection
- Remote on/off control-positive or negative logic
- High reliability: designed to meet 5 million hour MTBF
- Output voltage remote sense compensation ("s" suffix)
- Minimal space on PCB:
  - 50.8 mm x 12.7 mm x 8.0 mm or
  - 2.00 in x 0.50 in x 0.31 in
- No derating to +60℃, natural convection
- UL/IEC/EN60950 compliant
- RoHS Compliant available

APPLICATIONS

- Workstations, servers
- Desktop computers
- DSP applications
- Distributed power architectures
- Telecommunications equipment
- Data communications equipment
- Wireless communications equipment

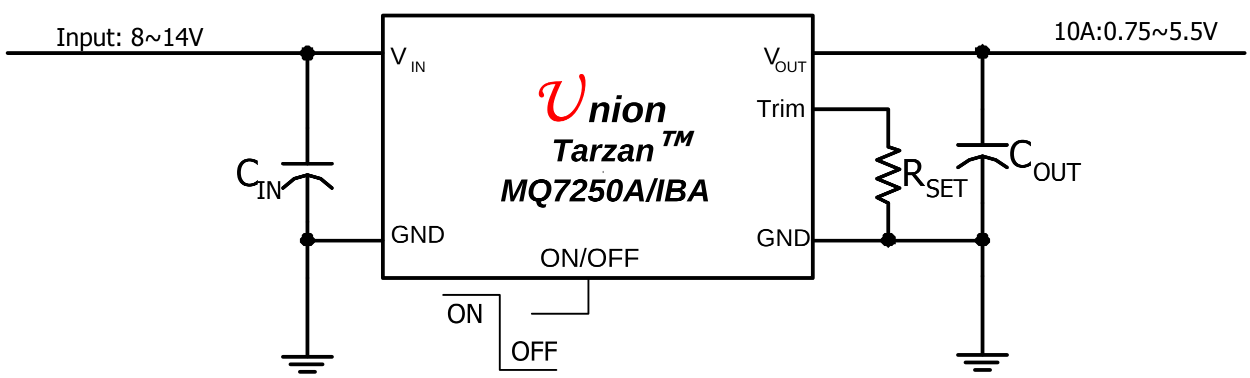
OPTIONS

- Output voltage remote sense
- Positive or Negative remote ON/OFF control logic
- Right Angle

Description

The ***Tarzan™*** MQ7250ASIP/IBA Series Power Modules are non-isolated dc-dc converters that operate over a wide input voltage range of 8Vdc to 14Vdc and provide a precisely (2%) regulated dc output with industry standard pin configuration. Such a module is suitable to application with 8V or 14V power supply bus. The modules have a maximum output current rating of 10A at a typical full-load efficiency over 95%. Standard features include remote on/off with positive logic and output voltage adjustment, over-current protection, over-temperature protection. Option features include output voltage remote sense compensation.

************************************* ***Typical Application Circuit*** ********************************

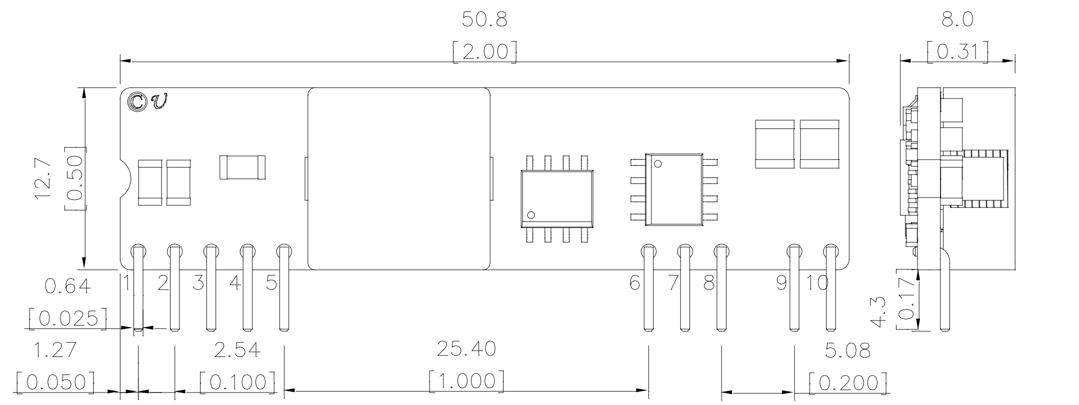


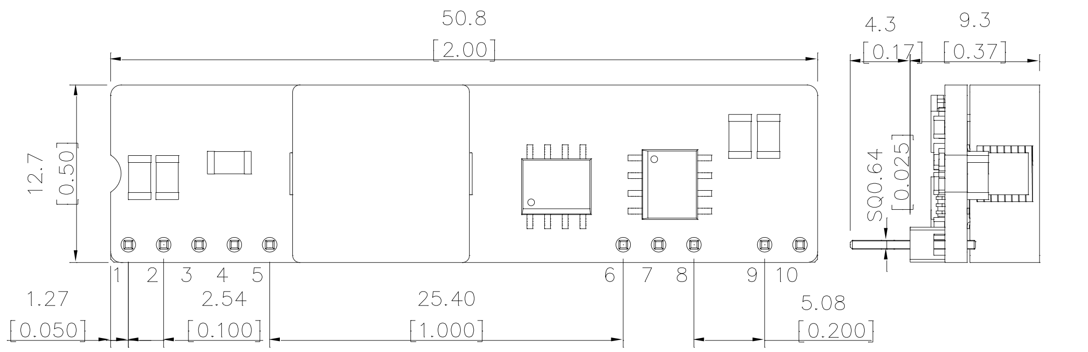
Performance Specifications (at TA=+25℃)

Model	Input V_{IN} Range (V)	Output				Efficiency (%)
		I_{OUT} (A)	Trim Range (V)	Regulation		
				Line (%)	Load (%)	
MQ7250ASIP/IBA	8~14	10	0.75V~5.5V	0.5	0.5	93

Mechanical Specifications

Dimensions are in mm (inches)
Tolerances: x.x mm±0.5mm (x.xx in ±0.02 in);
x.xx mm±0.25m m (x.xxx in ±0.01 in)

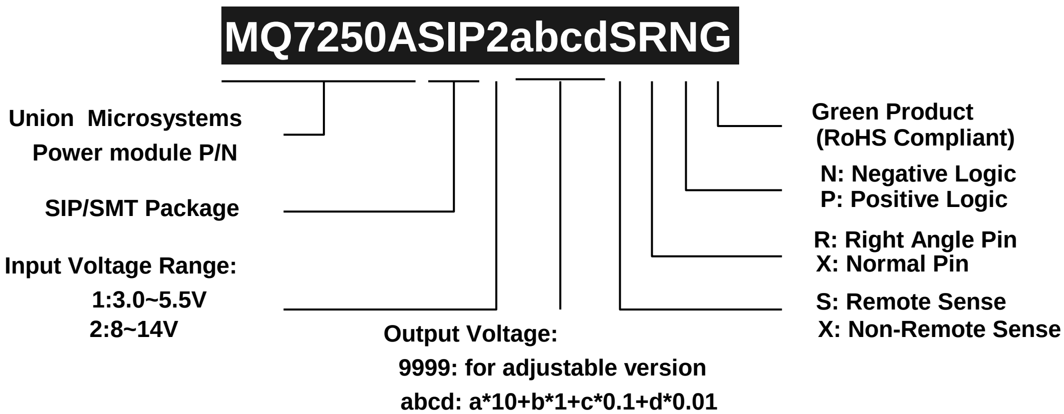




"-R" option

PIN	Description
1	V_O
2	V_O
3	Sense ("S" suffix)
4	V_O
5	GND
6	GND
7	V_{IN}
8	V_{IN}
9	TRIM
10	ENABLE

Ordering Information



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For examples:

MQ7250ASIP29999SXNG means MQ7250A in SIP Pin-out, input voltage 8~14V, output voltage 0.75~5.0, and with remote sense pin equipped, normal pin direction, negative logic mode and green product.

MQ7250ASIP20150XRPG means MQ7250A in SIP Pin-out, input voltage 8~14V, output voltage 1.5V, and without remote sense pin equipped, right angle pin direction, positive logic mode and green product.

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Absolute Maximum Ratings

Note: These are stress ratings. Exposure of devices to any of these conditions may adversely affect long-term reliability. Proper operation under conditions other than those listed in the Performance Specifications Table is not implied.

Parameter	Symbol	Min	Max	Unit
Input Voltage	V_{IN}	-0.3	6	V
Storage Temperature	T_{STG}	-40	125	℃

MQ7250ASIP/IBA Electrical Specifications: *(T_A=+25°C)*

Parameter	Condition	Symbol	Min	Typ	Max	Unit
Input Voltage Range		V_{IN}	8		14	V
Output Current		Io	0		10	A
Output Voltage Set point	100% load	Δ Vo	-2		+2	%
Temperature Regulation	T_A= $T_{A.MIN}$ To $T_{A.MAX}$	-		0.4		%$V_{O.SET}$
Remote Sense Range					0.5	V
Line Regulation	See each output's corresponding character figure					
Load Regulation						
Output Ripple and Noise Voltage	Io=6A,0~20MHz（***Detail Please see corresponding figure***）					
Transient Response						

General Specifications

Parameter	Condition	Symbol	Min	Typ	Max	Unit
Maximum Capacitive Load	10A resistive load + Aluminum capacitor			6600		μF
	10A resistive load +Sanyo POSCAP			2000		
Overcurrent Protection			15		20	A
Output short-circuit current (average)	All				3	A
Under Voltage Lockout Trip Level	Rising and falling V_{IN}, 3% hysteresis		7.8	8	8.2	V
Positive Logic						
Logic High (Module ON)		V_{IH}			$V_{IN.MAX}$	V
Logic Low (Module OFF)		V_{IL}	-0.2		0.3	V
Negative Logic						
Logic High (Module OFF)		V_{IH}	2		$V_{IN.MAX}$	V
Logic Low (Module ON)		V_{IL}	-0.2		0.3	V
Start-up Time	10A resistive load, no external output capacitors			3	5	mS
Switching Frequency		Fo		300		kHz
Operating Temperature	Natural convection		-40		85	℃
Vibration	3 Axes, 5 Min Each	10~55Hz，0.35mm，5g				
	3 Axes, 6 Times Each	Peak Deviation 300g，Settling Time 6mS				
MTBF		5,000,000				Hour

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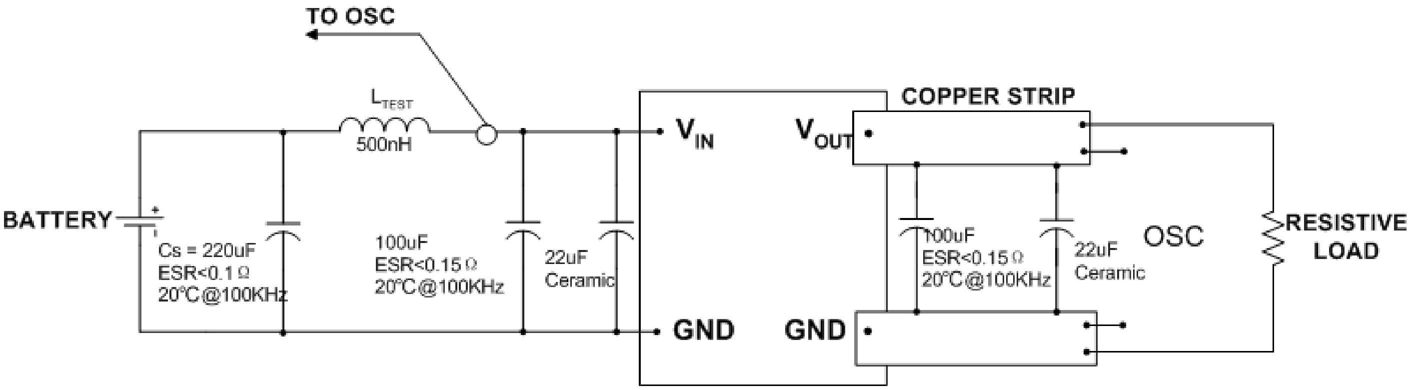
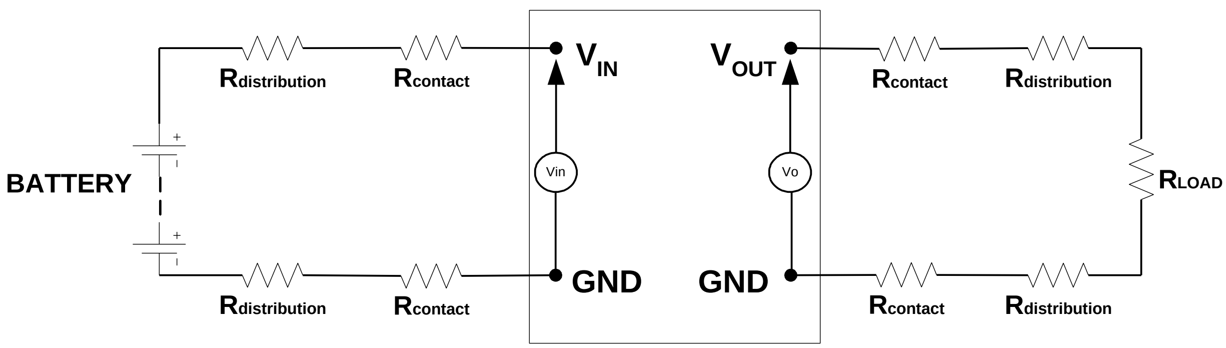
Test Configurations



Test setup for input noise, output noise and ripple

Note:

Output noise is measured with 0.1μF ceramic capacitor connected at the output. OSC measurement should be made using a BNC socket. Position the load between 50mm and 75mm（2in. and 3in）from the tested module.



Test setup for efficiency

Note:

All voltage measurements must be taken at the module's terminals, as shown above. If sockets are needed, Kelvin connections are required at the module terminals to avoid measurement errors due to socket contact resistance.

Technical Notes

Input Voltage Range

The MQ7250ASIP/IBA Series can be used in a wide variety of applications, esp. most of unregulated 12V intermediate power supply bus system. Its wide input voltage ranges can tolerate worst voltage drop from cheap isolated Brick-type Bus-converter, so it reduces total system cost on power supply.

Return Current Paths

The MQ7250ASIP/IBA Series are non-isolated DC/DC converters. Their two Common pins (pins 5 and 6) are connected to each other internally. To the extent possible with the intent of minimizing ground loops, input return current should be directed through pin 6 (also referred to as---Input or Input Return), and output return current should be directed through pin 5 (also referred to as---Output or Output Return) as short as possible.

I/O Filtering

All the specifications of the MQ7250ASIP/IBA Series are tested with specified output capacitors. However, certain input capacitors are necessary to improve the power modules' operating conditions and to reduce the ac impedance. For example, under

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some conditions, the power modules can't normally start up when fully loaded due to the high ac-impedance input source. External input capacitors serve primarily as energy-storage devices. They should be added close to the input pins of the MQ7250ASIP/IBA and selected for bulk capacitance (at appropriate frequencies), low ESR, and high rms-ripple-current ratings. All external capacitors should have appropriate voltage ratings. To reduce the amount of ripple current fed back to the input supply (input reflected-ripple current), an external L-C filter can be added with the inductance as close to the power module as possible.

MQ7250ASIP/IBA's output ripple and transient response can be improved with the increasing output capacitance. When using output capacitors, take care that the total output capacitance does not exceed MQ7250ASIP/IBA's Maximum Capacitive Load to avoid issuing the module's over-current protection mechanism in the start-up procedure.

When an external L-C filter is added to reduce ripple on load, for best results, the filter components should be mounted close to the load circuit rather than the power module.

When testing the relationship between external capacitors and output voltage noise, the oscilloscope's probe should be applied to the module's end directly with scope probe ground less than 10mm in length。

Input Fusing

The MQ7250ASIP/IBA Series is not internally fused. Certain applications and/or safety agencies may require the installation of fuses at the inputs of power conversion components. The selection of the fuses should conform to the following：

1. The fuse value should be fast-blow 8.5A fuses.
2. Both input traces must be capable of carrying a current of 1.5 times the value of the fuse without opening.

Safety Considerations

MQ7250ASIP/IBA's are non-isolated DC/DC converters. In general, all DC-DC's must be installed in compliance with relevant safety-agency specifications (usually UL/IEC/EN60950). In particular, for a non-isolated converter's output voltage to meet SELV (safety extra low voltage) requirements, its input must be SELV compliant. If the output needs to be ELV (extra low voltage), the input must be ELV.

Remote Sense

MQ7250ASIP/IBA Power Modules with suffix "S" offer a positive output sense function on pin SENSE. The sense function enables point-of-use regulation for overcoming moderate IR drops in conductors and/or cabling. The sense line carries very little current and consequently requires a minimal cross-sectional-area conductor. As such, it is not a low-impedance point and must be treated with care in layout and cabling. Sense lines should be run adjacent to signals (preferably ground). If the remote sense is not needed the sense pin should be left open or connected to V_{OUT} directly.

Use of trim and sense functions can cause the output voltage to increase, thereby increasing output power beyond the MQ7250ASIP/IBA's specified rating. Therefore:

$$V_{OUT} \text{ (at pins) } \times I_{OUT} \leq P \text{ (rated output power)}$$

ON/OFF Control

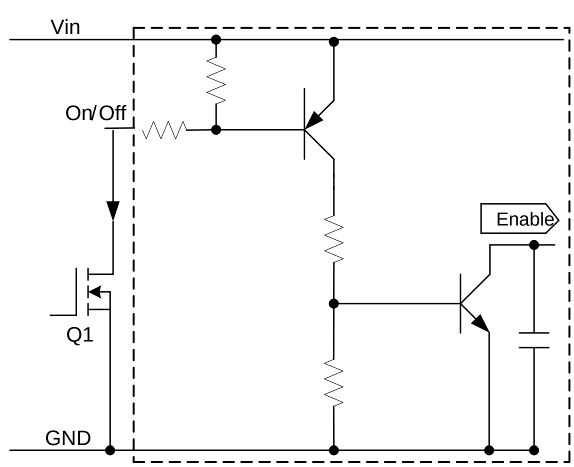

Fig1a. Circuit configuration for using Positive logic On/OFF

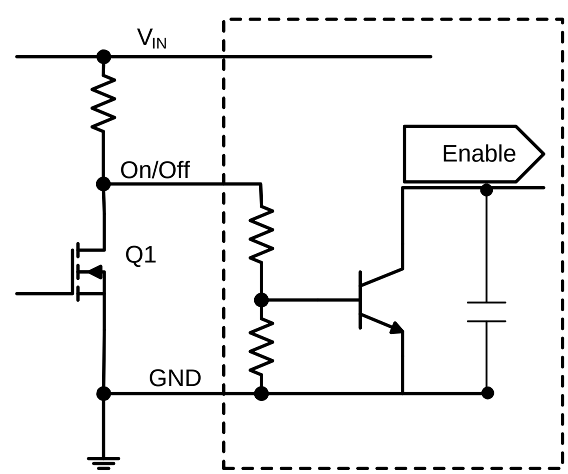

Fig1b, Circuit configuration for using negative logic On/OFF

For positive logic modules, the circuit configuration for using On/Off pin is shown in Fig1a.The On/Off pin is an open collector/drain logic input signal(Von/Off) that is referenced to ground. During a logic-high(On/Off pin is pulled high internal to the

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module) when the Q1 is in the Off state, the power module is ON. Applying a logic-low when the transistor Q1 is turned-On, the power module is Off.

For negative logic On/Off devices, the circuit configuration is shown in Fig1b. The On/Off pin is pulled high with an external pull-up resistor. When transistor Q1 is in Off state, logic High is applied to the On/Off pin and the power is Off. The minimum On/off voltage for logic High on the On/Off pin is 2.5Vdc. To turn the module ON, logic low is applied to the On/Off pin by turning on Q1

The regulator will run in normal operation when the ON/OFF pin is left open.

Output Overvoltage Protection

MQ7250ASIP/IBA Series products do not incorporate output over voltage protection. If the operating circuit requires protection against abnormal output voltage, voltage-limiting circuitry must be provided external to the power module.

Output Overcurrent Protection (OCP)

MQ7250ASIP/IBA incorporates overcurrent and short circuit protection. If the load current exceeds the overcurrent protection setpoint, the MQ7250ASIP/IBA's internal overcurrent-protection circuitry immediately turns off the module, which then goes into Hiccup mode. The unit operates normally once the output current is brought back into its specified range. The typical average output current during hiccup is less than 3A.

***Caution**：Be careful never to operate MQ7250ASIP/IBA in a "heavy overload" condition that is between the rated output current and the overcurrent protection setpoint. This can cause permanent damage to the components*.

Overtemperature Protection (OTP)

To ensure MQ7250ASIP/IBA's reliability and avoid damaging its internal components， MQ7250ASIP/IBA incorporates over-temperature protection circuit。When the temperature of the PCB is above 125℃, the overtemperature protection circuit will be enabled and the module will stop working. When the temperature of the temperature-testing component is below about 80℃, the overtemperature protection circuit will release and the module will automatically recover from shutdown. To avoid permanently damaging components, the surface temperature of MQ7250ASIP/IBA's power components, esp. of the MOSFET (T_{REF} in Fig2) should be ensured below 125℃.

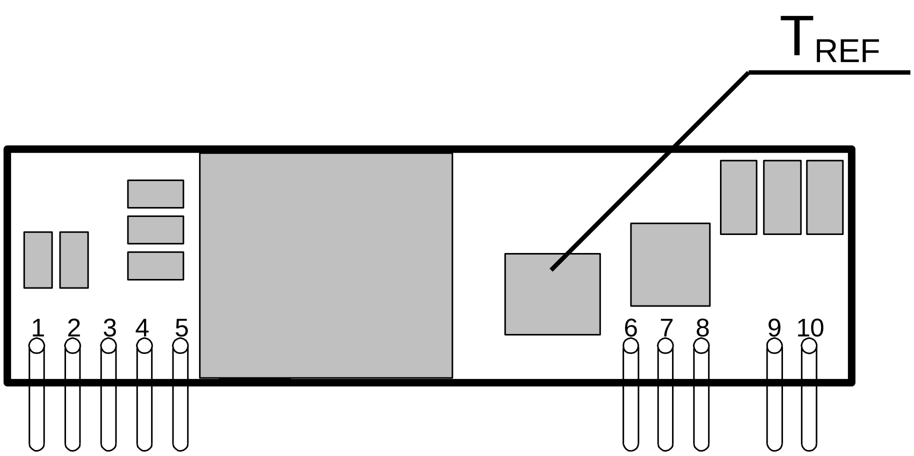

Fig2, Temperature Reference Point

***Note**：The over temperature protection may be issued when MQ7250ASIP/IBA operates in a "heavy overload" condition for a long time. Thus, the airflow should be improved.*

Output Voltage Trimming

MQ7250ASIP/IBA's output voltage can be trimmed in certain ranges. See Figure 3 for the 2 programming methods. See Performance Specifications for allowable trim ranges in detail. Also customized products are offered.

Trim with external resistor (Fig3a), the equation as below：

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$$R_{TRIM} = \frac{10500}{V_O - 0.7525} - 1000$$

Resistor values are in Ω; V_O is desired output voltage.
For examples, to trim output to 1.5V, then

$$R_{TRIM} = \frac{10500}{1.5 - 0.7525} - 1000 = 13046$$

So, R_{TRIM} = 13.046kΩ

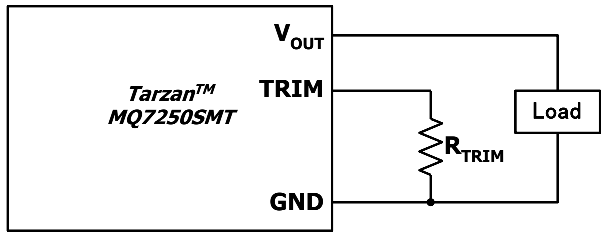

Fig3. Circuit configuration for programming output voltage using external resistor

For most common voltages, the required Trim resistors as Table 1.

Table 1, the required trim resistors R_{TRIM} for most common voltages

Desired Voltages (V)	**R_{TRIM} (kΩ)**
0.7525	Open
1.2	22.46
1.5	13.05
1.8	9.024
2.5	5.009
3.3	3.122
5.0	1.472

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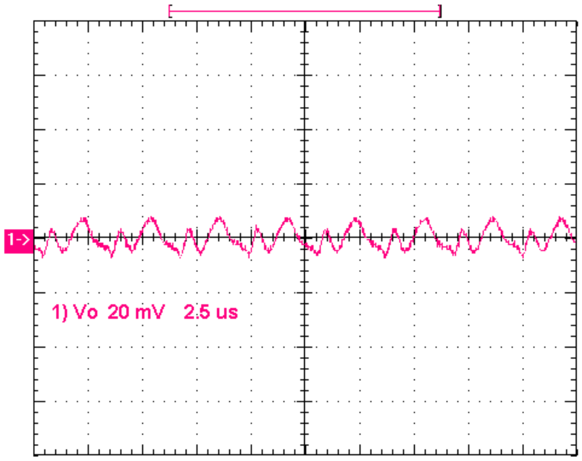
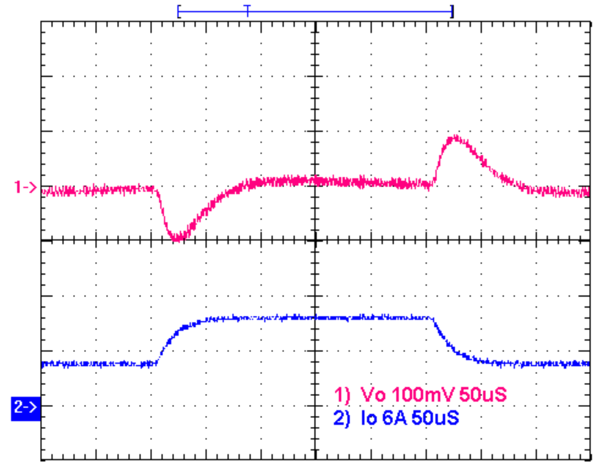
Typical Characteristics – output adjusted to 1V

General conditions:

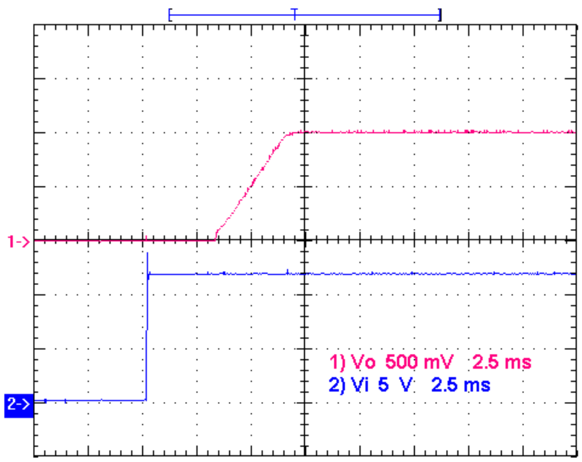
Input filter 22µF Ceramic + 47µF TAN (150mΩ ESR), Output filter 22µF Ceramic + 100µF TAN (150mΩ ESR)



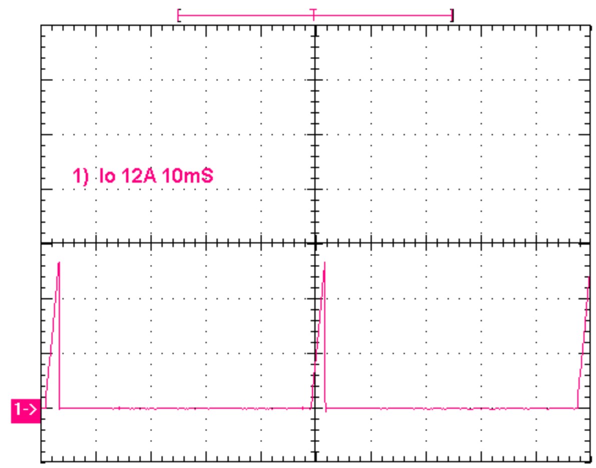
Noise V_{IN}=12V, I_O=10A, 5~20MHz Bandwidth



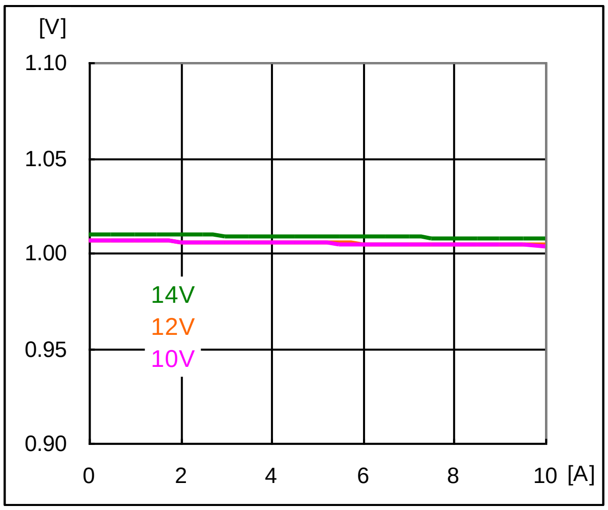
Transient Response V_{IN}=12V, Step from 5A~10A~5A



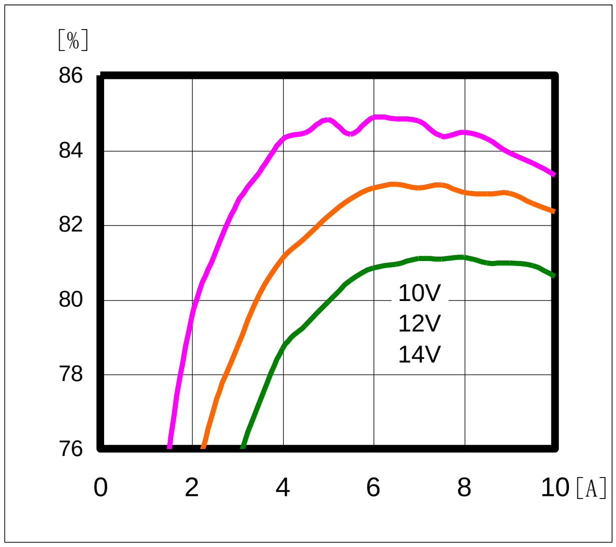
Start-up V_{IN}=12V, I_O=10A



Short-Circuit Output V_{IN}=12V



Regulation
Output voltage vs. Load Current,



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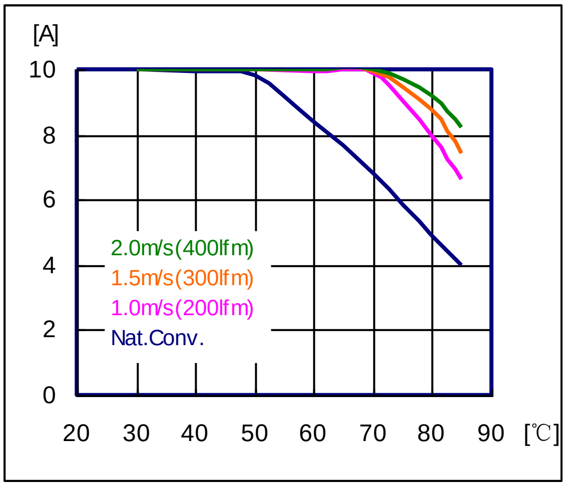

Output Current Derating (Load Current vs. Ambient Temperature (T_{REF}, See Page 6)), V_{IN}=12V,

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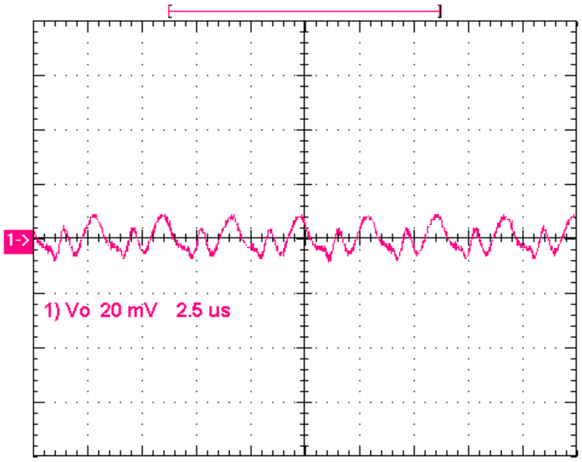
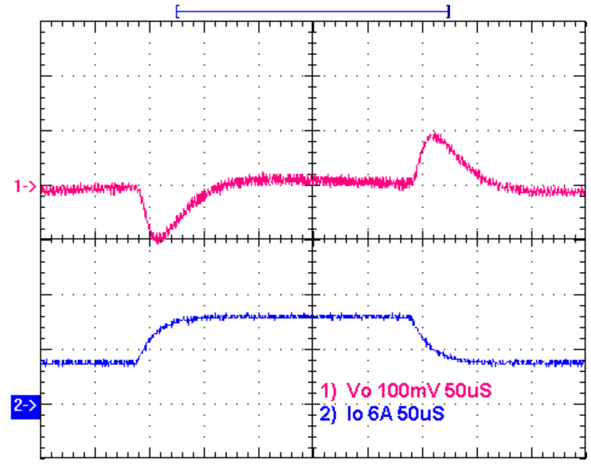
Typical Characteristics – output adjusted to 1.2V

General conditions:

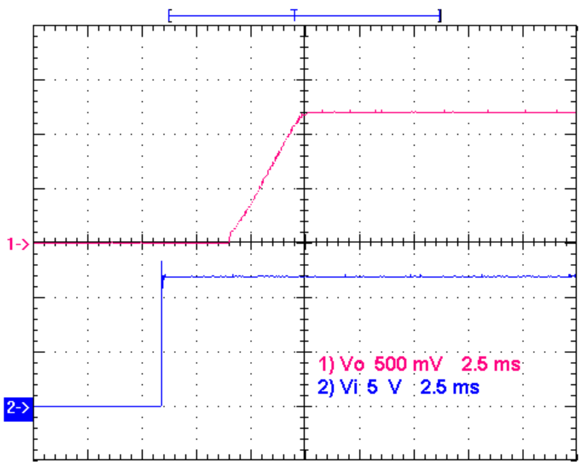
Input filter 22µF Ceramic + 47µF TAN (150mΩ ESR), Output filter 22µF Ceramic + 100µF TAN (150mΩ ESR)



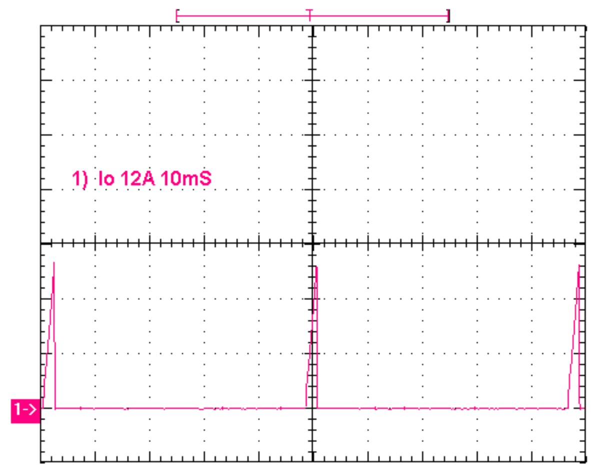
Noise V_{IN}=12V, I_O=10A, 5~20MHz Bandwidth



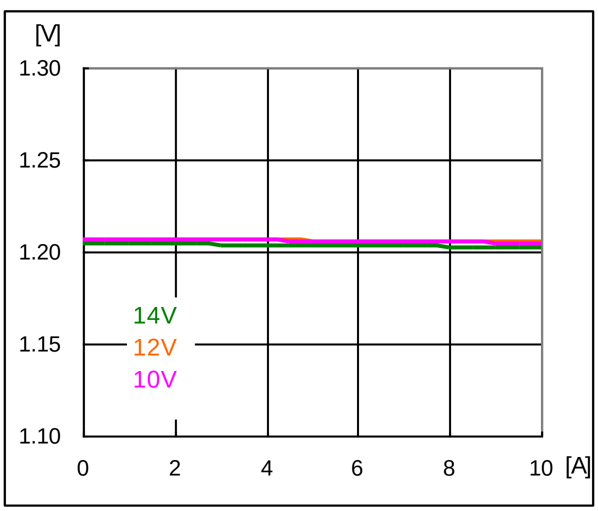
Transient Response V_{IN}=12V, Step from 5A~10A~5A



Start-up V_{IN}=12V, I_O=10A

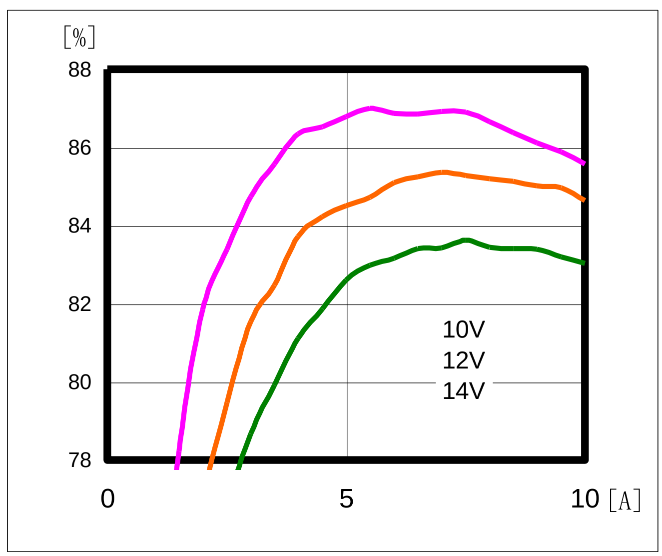


Short-Circuit Output V_{IN}=12V



Regulation

Output voltage vs. Load Current,



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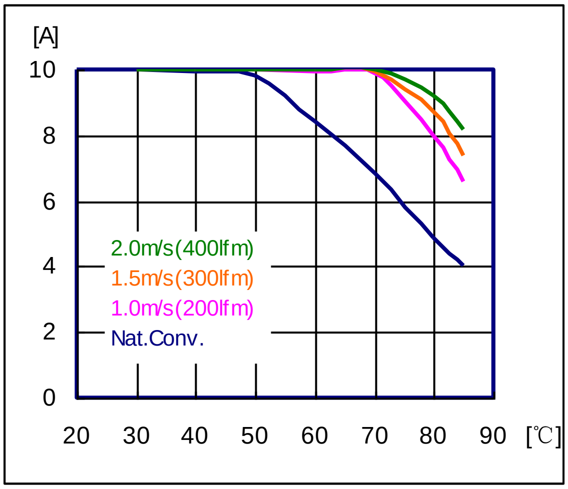

Output Current Derating (Load Current vs. Ambient Temperature (T_{REF}, See Page 6)), V_{IN}=12V,

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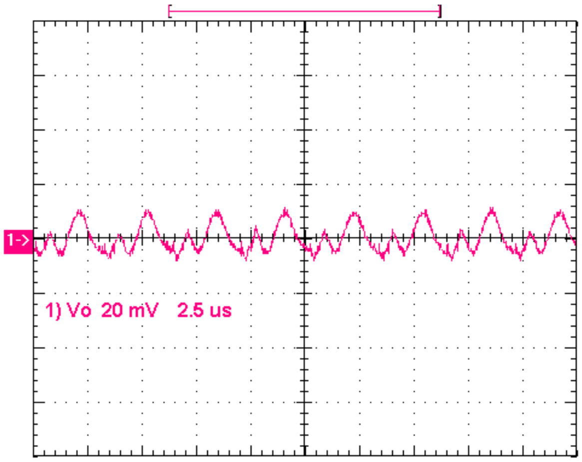
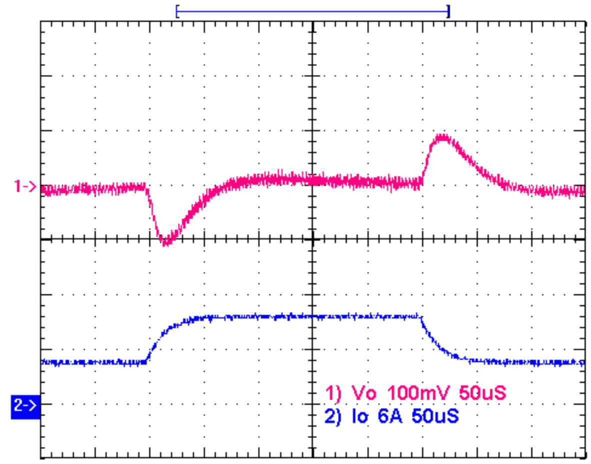
Typical Characteristics – output adjusted to 1.5V

General conditions:

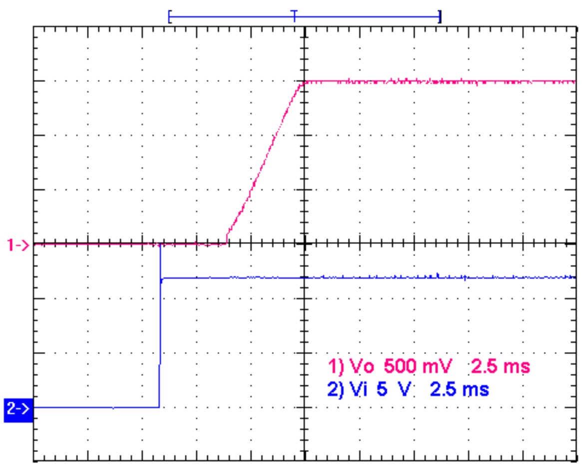
Input filter 22µF Ceramic + 47µF TAN (150mΩ ESR), Output filter 22µF Ceramic + 100µF TAN (150mΩ ESR)



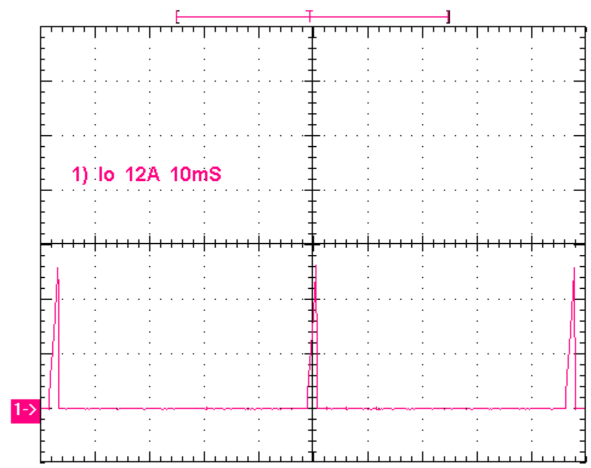
Noise V_{IN}=12V, I_O=10A, 5~20MHz Bandwidth



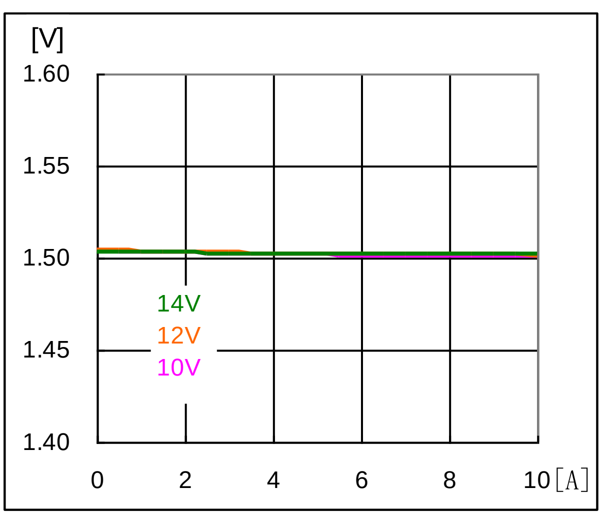
Transient Response V_{IN}=12V, Step from 5A~10A~5A

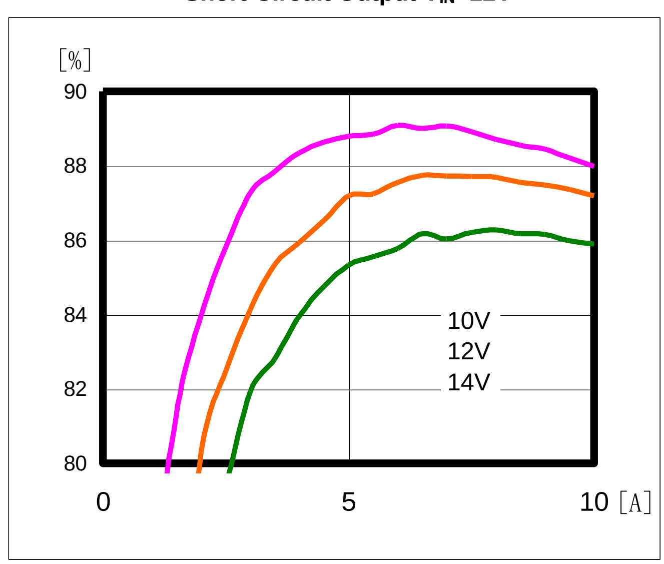


Start-up V_{IN}=12V, I_O=10A



Short-Circuit Output V_{IN}=12V





Regulation
Output voltage vs. Load Current,

Efficiency

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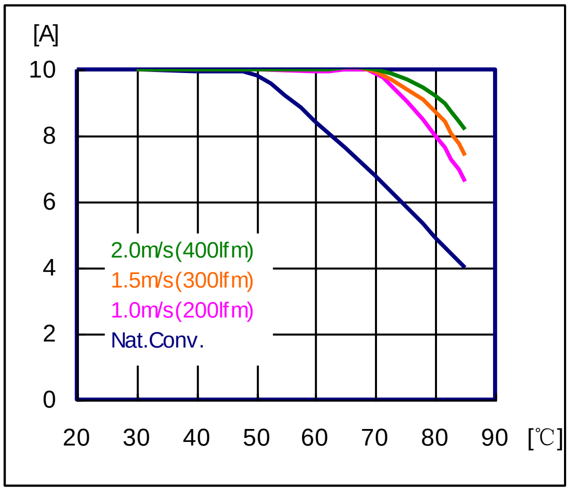

Output Current Derating (Load Current vs. Ambient Temperature (T_{REF}, See Page 6)), V_{IN}=12V,

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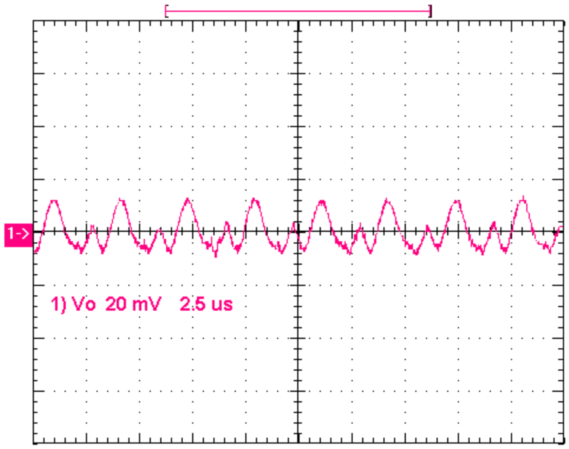
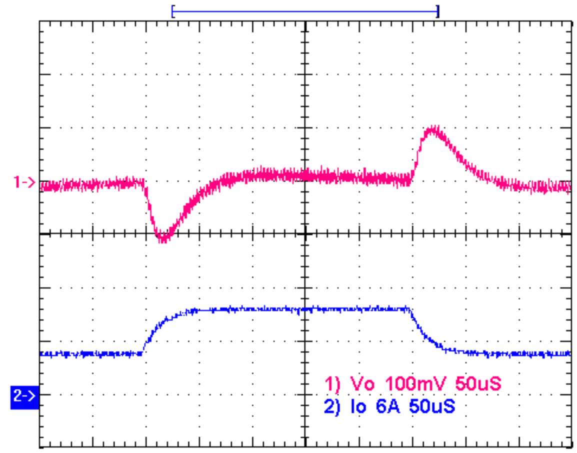
Typical Characteristics – output adjusted to 1.8V

General conditions:

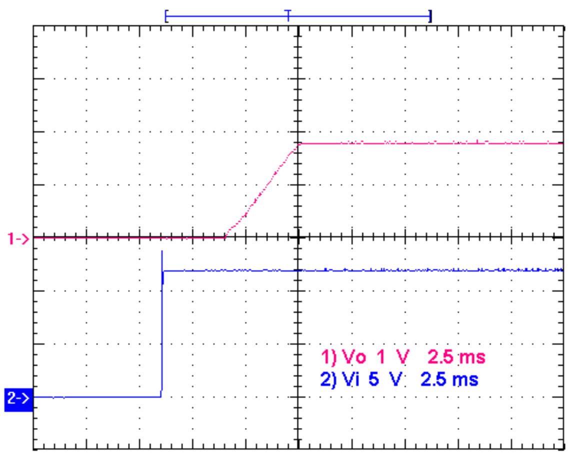
Input filter 22µF Ceramic + 47µF TAN (150mΩ ESR), Output filter 22µF Ceramic + 100µF TAN (150mΩ ESR)



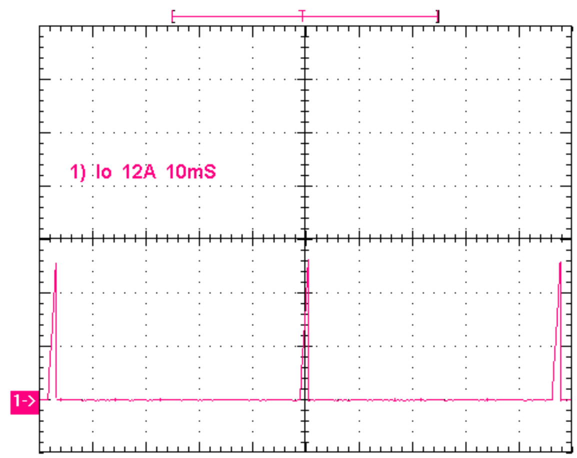
Noise V_{IN}=12V, I_O=10A, 5~20MHz Bandwidth



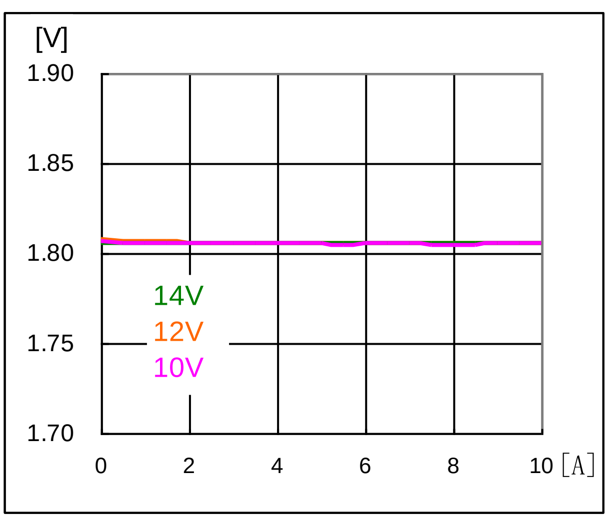
Transient Response V_{IN}=12V, Step from 10A~5A~10A

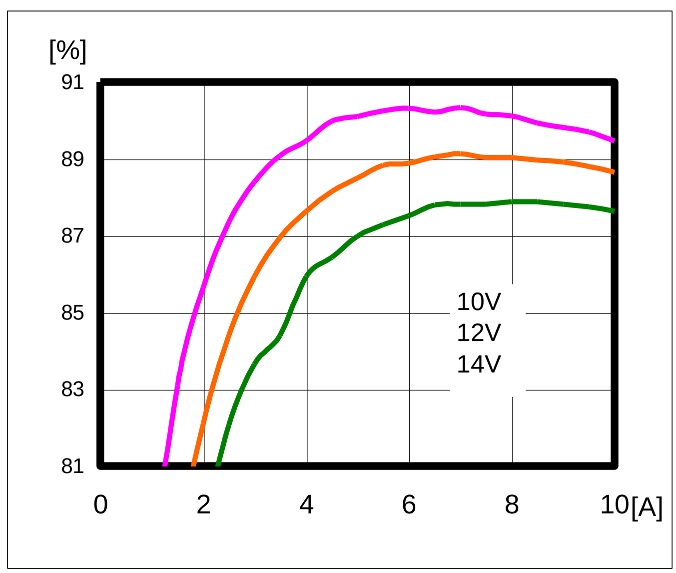


Start-up V_{IN}=12V, I_O=10A



Short-Circuit Output V_{IN}=12V





Regulation
Output voltage vs. Load Current,

Efficiency

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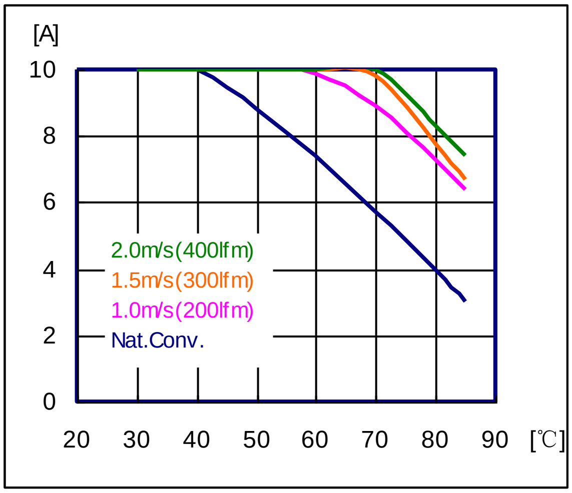

Output Current Derating (Load Current vs. Ambient Temperature (T_{REF}, See Page 6)), V_{IN}=12V,

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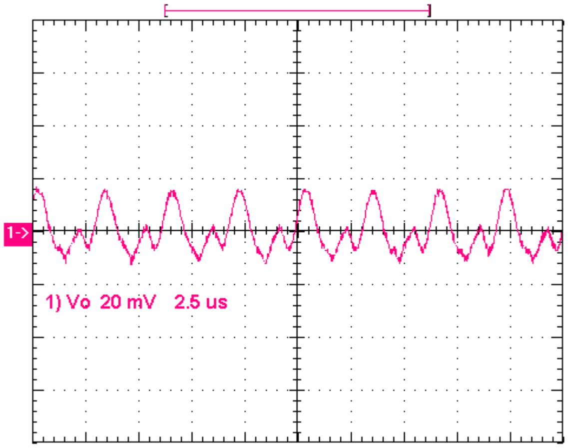
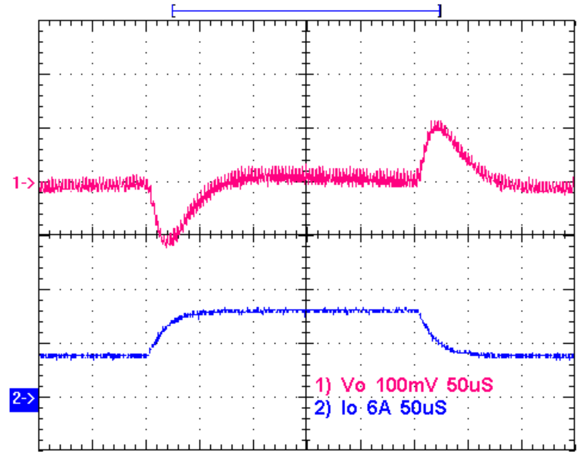
Typical Characteristics – output adjusted to 2.5V

General conditions:

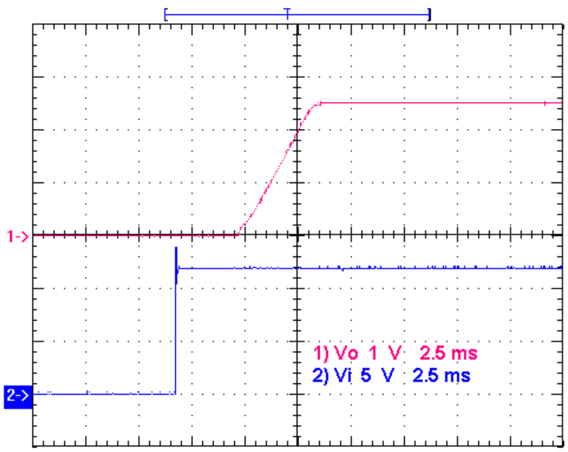
Input filter 22μF Ceramic + 47μF TAN (150mΩ ESR), Output filter 22μF Ceramic + 100μF TAN (150mΩ ESR)



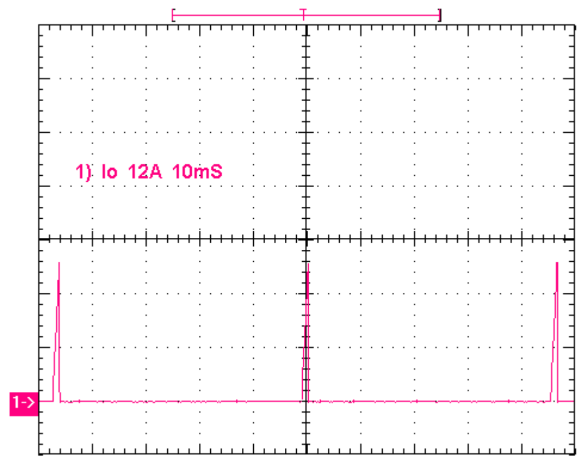
Noise V_{IN}=12V, I_O=10A, 5~20MHz Bandwidth



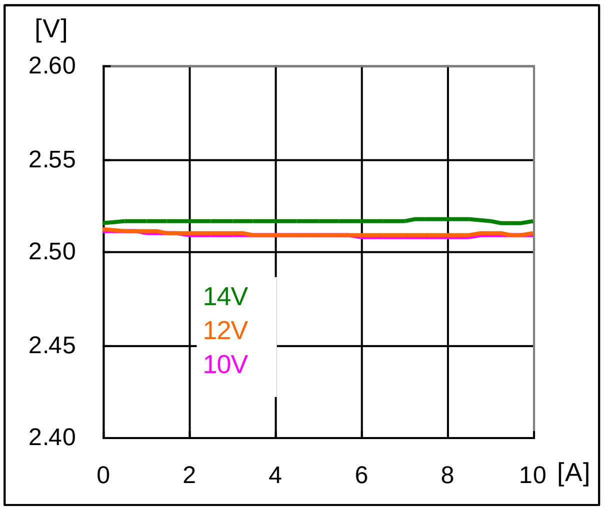
Transient Response V_{IN}=12V, Step from 5A~10A~5A



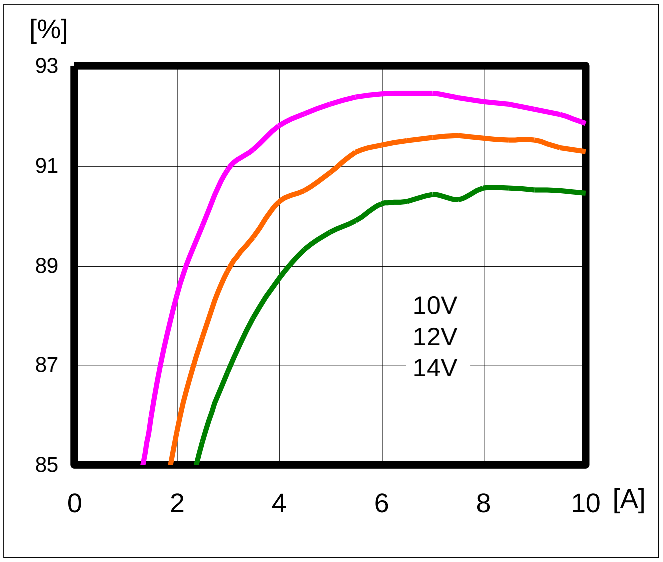
Start-up V_{IN}=12V, I_O=10A



Short-Circuit Output V_{IN}=12V



Regulation
Output voltage vs. Load Current,



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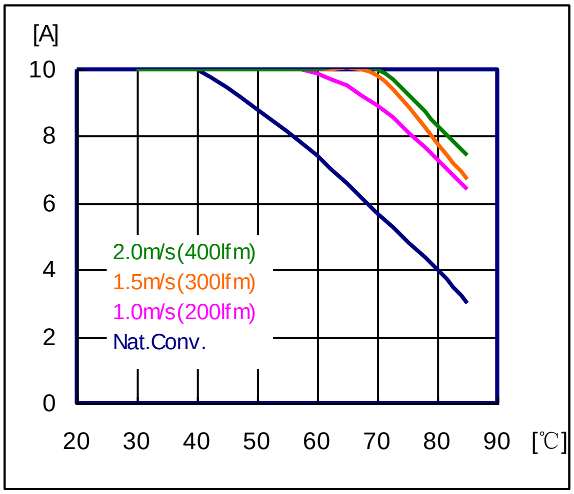

Output Current Derating (Load Current vs. Ambient Temperature (T_{REF}, See Page 6)), V_{IN}=12V,

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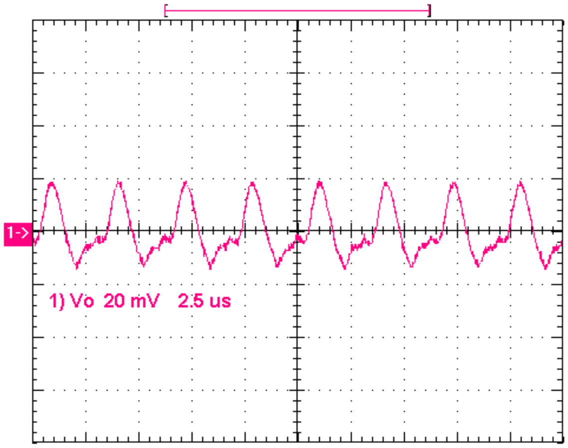
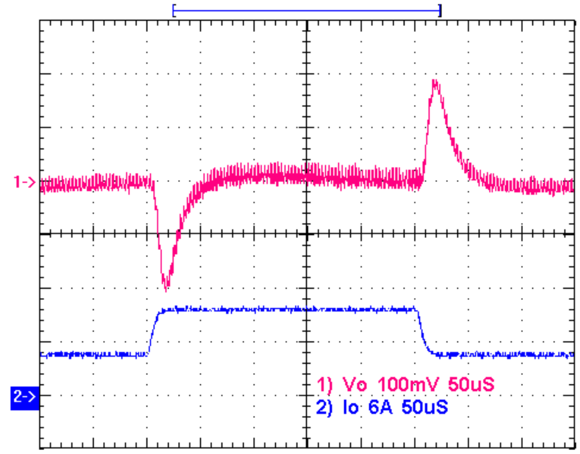
Typical Characteristics – output adjusted to 3.3V

General conditions:

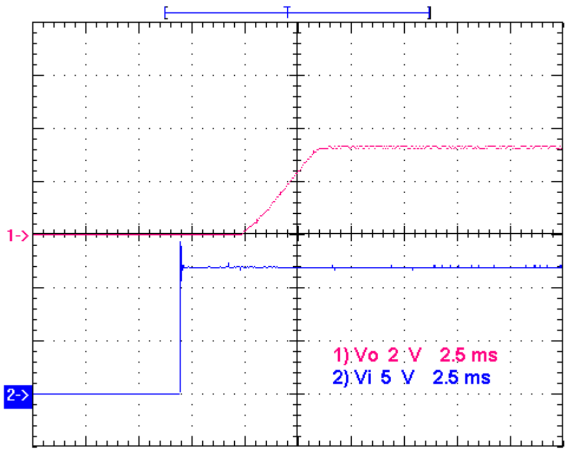
Input filter 22µF Ceramic + 47µF TAN (150mΩ ESR), Output filter 22µF Ceramic + 100µF TAN (150mΩ ESR)



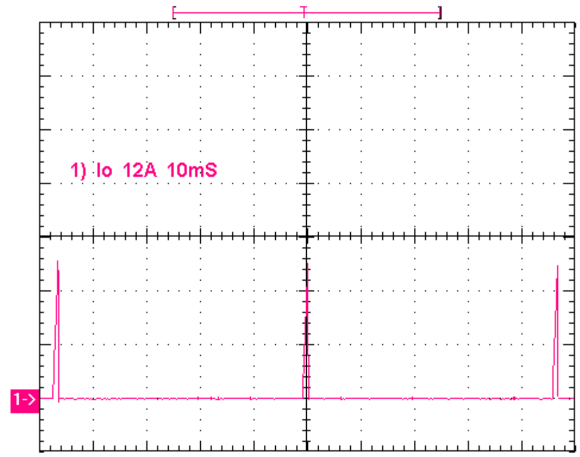
Noise V_{IN}=12V, I_O=10A, 5~20MHz Bandwidth



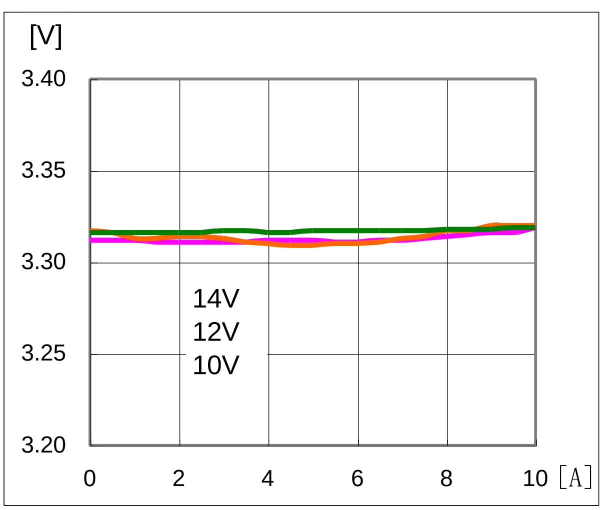
Transient Response V_{IN}=12V, Step from 5A~10A~5A

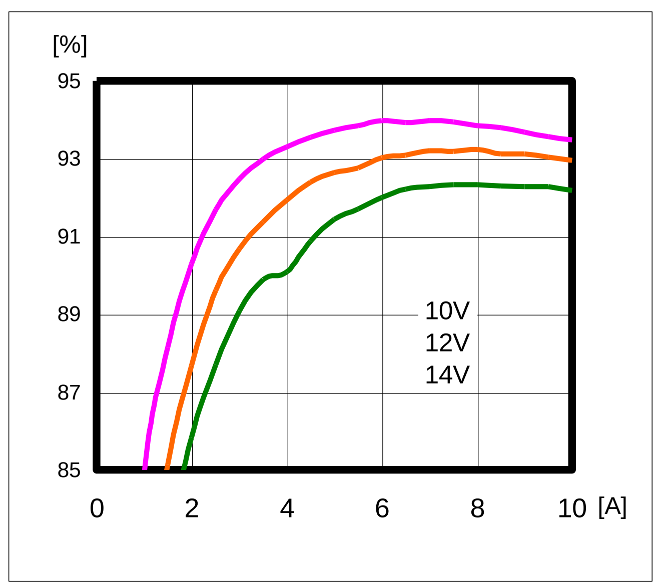


Start-up V_{IN}=12V, I_O=10A



Short-Circuit Output V_{IN}=12V





Regulation
Output voltage vs. Load Current,

Efficiency

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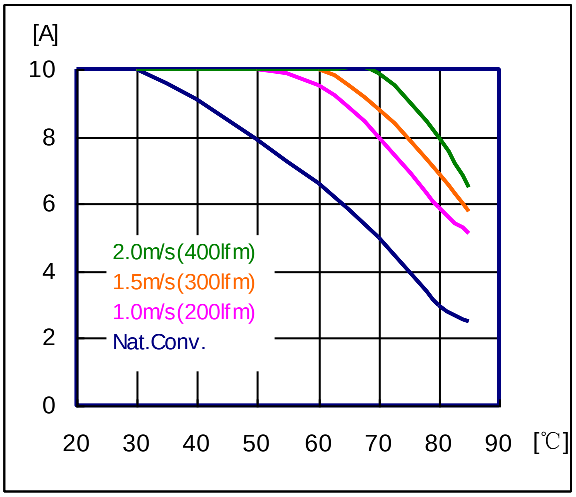

Output Current Derating (Load Current vs. Ambient Temperature (T_{REF}, See Page 6)), V_{IN}=12V,

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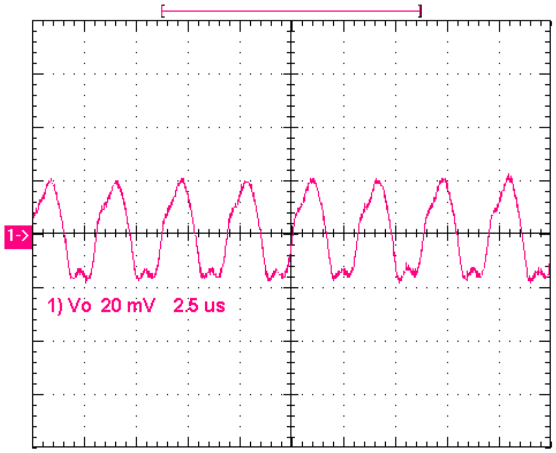
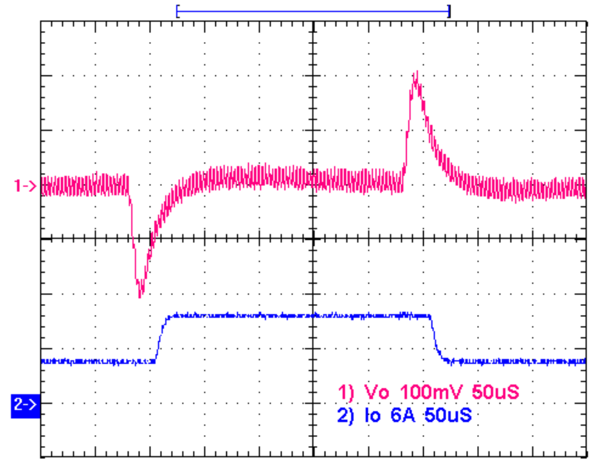
Typical Characteristics – output adjusted to 5.0V

General conditions:

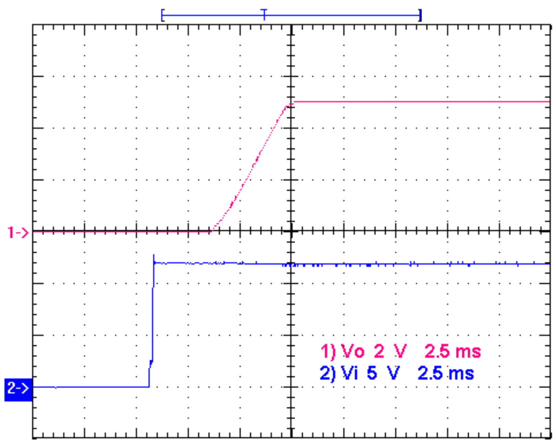
Input filter 22μF Ceramic + 47μF TAN (150mΩ ESR), Output filter 22μF Ceramic + 100μF TAN (150mΩ ESR)



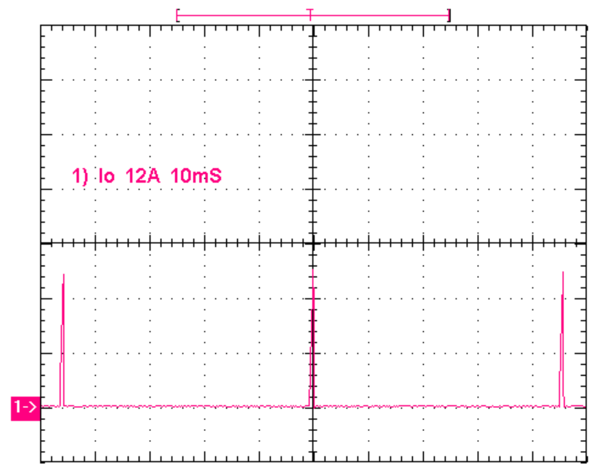
Noise V_{IN}=12V, I_O=10A, 5~20MHz Bandwidth



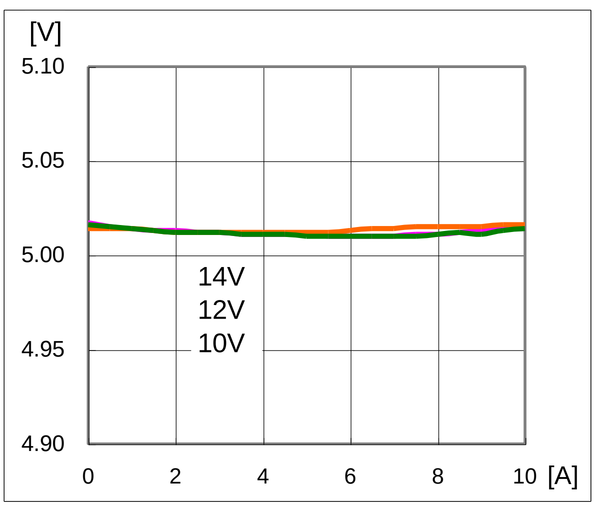
Transient Response V_{IN}=12V, Step from 5A~10A~5A



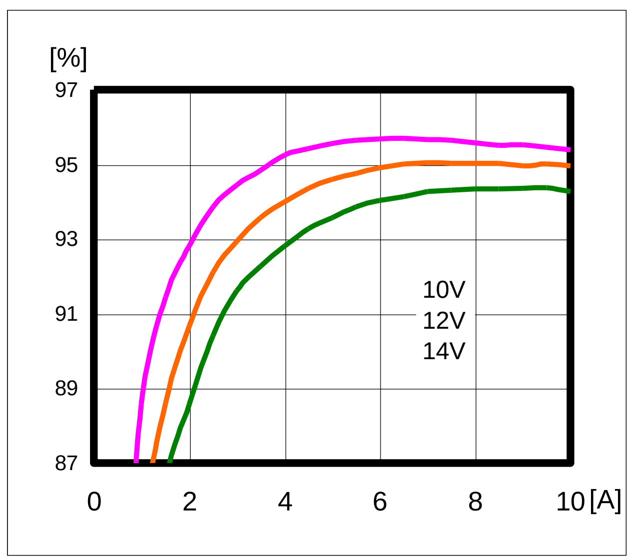
Start-up V_{IN}=12V, I_O=10A



Short-Circuit Output V_{IN}=12V



Regulation
Output voltage vs. Load Current,



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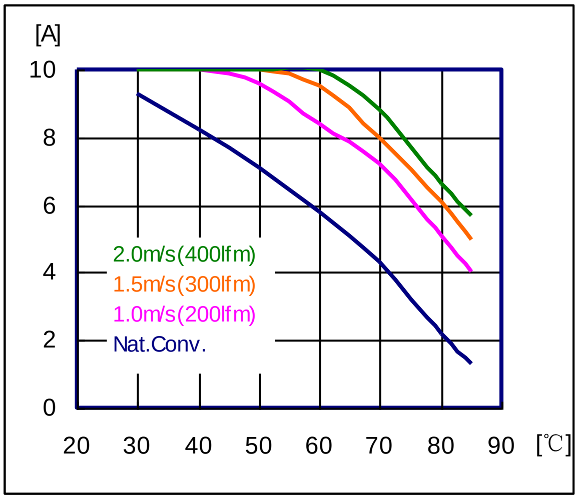

Output Current Derating (Load Current vs. Ambient Temperature (T_{REF}, See Page 6)), V_{IN}=12V,

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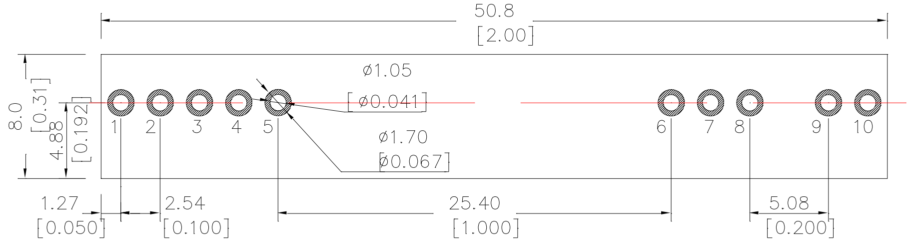
Recommended Hole Pattern

Dimensions are in millimeters (inches)

Tolerances: x.x mm±0.5mm (x.xx in ±0.02 in);

x.xx mm±0.25mm (x.xxx in ±0.01 in)



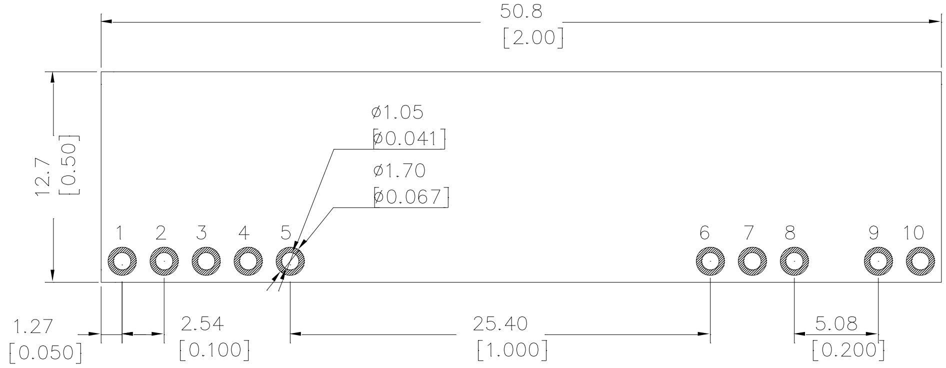
Component-side footprint

Recommended Hole Pattern for "R" suffix

Dimensions are in millimeters (inches)

Tolerances: x.x mm±0.5mm (x.xx in ±0.02 in);

x.xx mm±0.25mm (x.xxx in ±0.01 in)



Component-side footprint

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Application Notes

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