



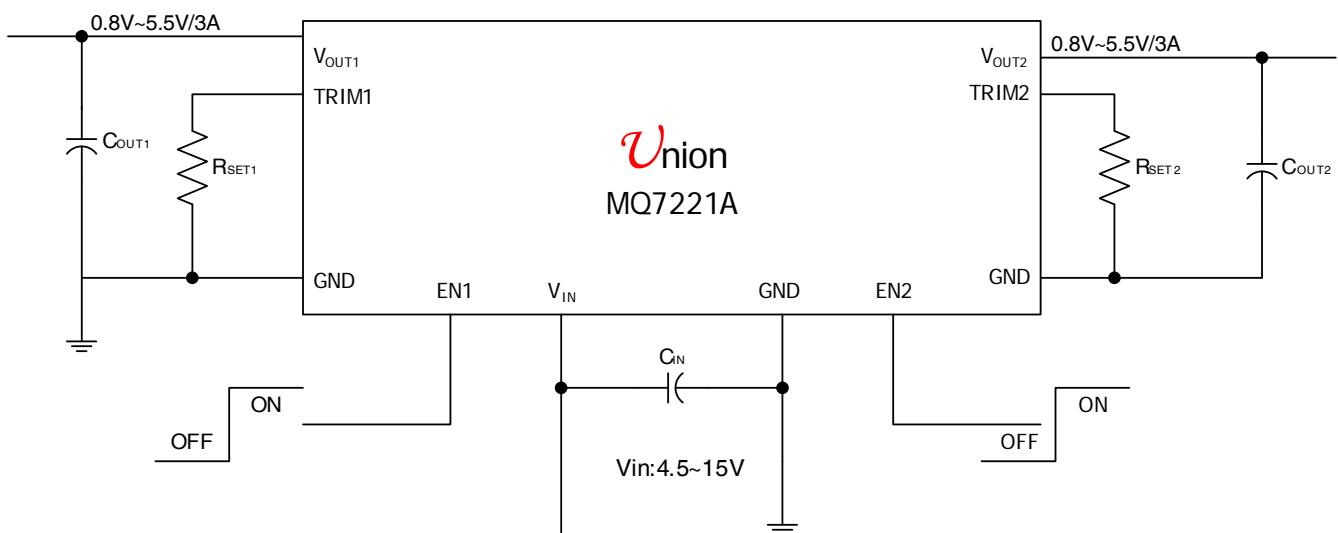
## Applications

- Workstations, servers
- Desktop computers
- DSP applications
- Distributed power architectures
- Telecommunications equipment
- Data communications equipment
- Wireless communications equipment

## Description

The **Micro-BiTarzan™ MQ7221A** Power Modules are non-isolated dc-dc converters that operate over a wide input voltage range of 4.5Vdc to 15Vdc and provide a precisely (4%) regulated dc output. Such a module is suitable to applications with unregulated 12V power supply bus or low-cost ac-wall adapter, which have two and more lower voltages with each current not too high. The modules have a maximum output current rating of 3A for each output at typical full-load efficiency over 90%. Standard features include remote on/off with positive logic and output voltage wide trim, over-current protection etc.

### \*\*\*\*\* Typical Application Circuit \*\*\*\*\*



## Micro-BiTarzan™ MQ7221A

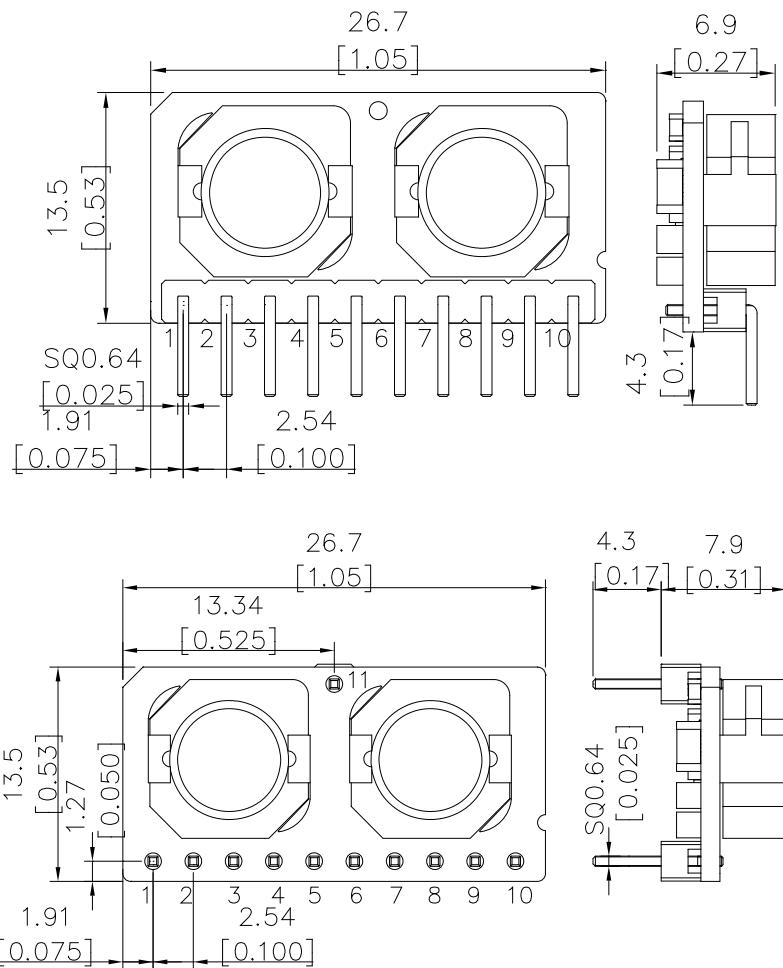
### Performance Specifications (at TA=+25°C)

| Model   | Input V <sub>IN</sub> Range (V) | Output               |                |            |   | Efficiency (%) |  |
|---------|---------------------------------|----------------------|----------------|------------|---|----------------|--|
|         |                                 | I <sub>OUT</sub> (A) | Trim Range (V) | Regulation |   |                |  |
|         |                                 | Line (%)             | Load (%)       |            |   |                |  |
| MQ7221A | 4.5~15                          | 3                    | 0.8V~5.5V*     | 4          | 4 | 90%            |  |

Note: \*, for 3.3V output, Vin must be not less than 5V; For 5V and above output, Vin must be over 8V.

### Mechanical Specifications

Dimensions are in mm (inches)



| PIN  | DESCRIPTION            |
|------|------------------------|
| 1/6  | Vin1/Vin2*             |
| 2/7  | GND                    |
| 3/8  | Vout1/Vout2            |
| 4/9  | EN1/EN2                |
| 5/10 | Trim1/Trim2            |
| S    | Standoff (only for -R) |

Note: \* Vin1, Vin2 are not connected each other internal.

### Ordering Information

| ITEM       | Description  | Note |
|------------|--|------|
| MQ7221A    | Standard product, SIP, front PIN                           |      |
| MQ7221A-X  | Standard product, SIP, with 3.3mm length front PIN         |      |
| MQ7221A-NP | SIP, front PIN without plastic stand off                   |      |
| MQ7221A-R  | Right angle without extra standoff PIN                     |      |
| MQ7221A-R1 | Right angle with extra standoff PIN                        |      |
| MQ7221A-RX | Right angle with 3.3mm length PIN & without extra standoff |      |

**MQ7221A Absolute Maximum Ratings**

Note: These are stress ratings. Exposure of devices to any of these conditions may adversely affect long-term reliability. Proper operation under conditions other than those listed in the Performance Specifications Table is not implied.

| Parameter           | Symbol           | Min  | Max | Unit |
|---------------------|------------------|------|-----|------|
| Input Voltage       | V <sub>IN</sub>  | -0.3 | 16  | V    |
| Storage Temperature | T <sub>STG</sub> | -40  | 125 | °C   |

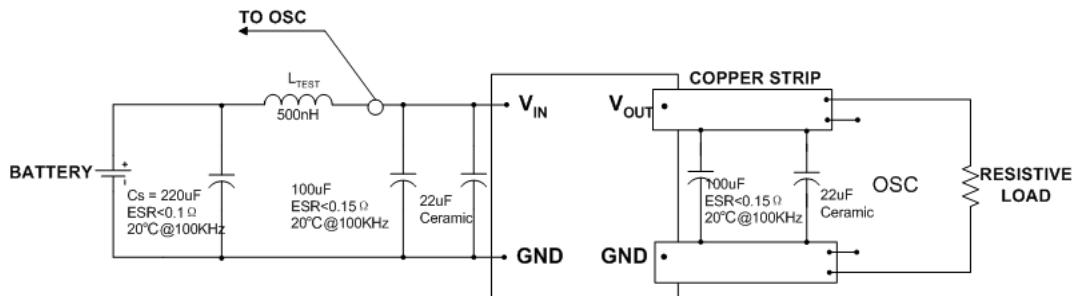
**MQ7221A Electrical Specifications:** ( $T_A=+25^\circ\text{C}$ , input voltage 12V, unless otherwise noted)

| Parameter                       | Condition   | Symbol          | Min | Typ | Max | Unit                |
|---------------------------------|---|-----------------|-----|-----|-----|---------------------|
| Input Voltage Range             |   | V <sub>IN</sub> | 4.5 |     | 15  | V                   |
| Output Current                  |   | I <sub>O</sub>  | 0   |     | 3   | A                   |
| Output Voltage Set point        | 100% load   | ΔV <sub>O</sub> | -4  |     | +4  | %                   |
| Temperature Regulation          | T <sub>A</sub> = T <sub>A,MIN</sub> To T <sub>A,MAX</sub>           | -               |     | 0.4 |     | %V <sub>O,SET</sub> |
| Output Trim Range               | See Performance Specifications (TBD)                                |                 |     |     |     |                     |
| Line Regulation                 | See Performance Specifications (TBD)                                |                 |     |     |     |                     |
| Load Regulation                 | See Performance Specifications (TBD)                                |                 |     |     |     |                     |
| Output Ripple and Noise Voltage | I <sub>O</sub> =3A, 0~20MHz (Detail Please see Ripple Figures, TBD) |                 |     |     |     |                     |
| Transient Response              |   |                 |     |     |     |                     |

**General Specifications**

| Parameter                        | Condition  | Symbol                                 | Min  | Typ        | Max     | Unit |
|----------------------------------|--|--|------|------------|---------|------|
| Maximum Capacitive Load          | 100% resistive load + Aluminum capacitor           |  |      | 4700       |         | μF   |
|                                  | 100% resistive load + Sanyo POSCAP                 |  |      | 1000       |         |      |
| Over-current Protection          | 4.5~15V input                                      |  | 5    |            | 8.5     | A    |
| Output short-circuit current     | All  |  |      | Auto-Reset |         |      |
| Under Voltage Lockout Trip Level | Rising V <sub>IN</sub>                             |  |      | 4.5        |         | V    |
|                                  | Falling V <sub>IN</sub>                            |  |      | 4.2        |         |      |
| Logic High (Module ON)           |  | V <sub>IH</sub>                        |      | 2.5        | Vin+0.7 | V    |
| Logic Low (Module OFF)           |  | V <sub>IL</sub>                        | -0.7 |            | 0.3     | V    |
| Start-up Time                    | 100% resistive load, no external output capacitors |  |      | 10         |         | mS   |
| Switching Frequency              |  | F <sub>O</sub>                         |      | 500        |         | kHz  |
| Operating Temperature            | Natural convection, no forced air flow             |  | -40  |            | 85      | °C   |
| Vibration                        | 3 Axes, 5 Min Each                                 | 10~55Hz, 0.35mm, 5g                    |      |            |         |      |
|                                  | 3 Axes, 6 Times Each                               | Peak Deviation 300g, Settling Time 6mS |      |            |         |      |
| MTBF                             |  | 2,000,000                              |      |            |         | Hour |

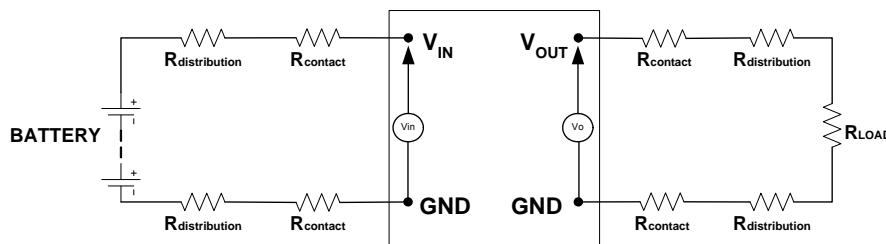
## Test Configurations



**Test setup for input noise, output noise and ripple**

Note:

Output noise is measured with  $0.1\mu F$  ceramic capacitor connected at the output. OSC measurement should be made using a BNC socket.



**Test setup for efficiency**

Note:

All voltage measurements must be taken at the module's terminals, as shown above. If sockets are needed, Kelvin connections are required at the module terminals to avoid measurement errors due to socket contact resistance.

## Technical Notes

### Input Voltage Range

The MQ72221A Series can be used in a wide variety of applications, like most of unregulated 12V intermediate power supply bus system or low-cost ac-wall adapter. Its wide input voltage ranges can tolerate worst voltage drop from cheap isolated Brick-type Bus-converter, so it reduces total system cost on power supply. Because of its input is wide from 4.5V to 20V, so the change of power rail between them will not lead to any design changes, this simplifies engineer's load.

### Input Ends

MQ72221A power modules have two independently input ends, which are not connected internal. For single input rail system, they must be connected to input respectively. For dual input rails systems, for example 5V and 12V, each input ends can be connected different input 5V, that adds flexibility for system designers.

### Return Current Paths

The MQ7221A Series is non-isolated DC/DC converters. To the extent possible with the intent of minimizing ground loops, input and output return current should be directed through pin GND as short as possible.

### I/O Filtering

All the specifications of the MQ7221A Series are tested and specified without output capacitors. However, certain input capacitors are necessary to improve the power modules' operating conditions and to reduce the ac impedance. For example, under some conditions, the power modules can't normally start up when fully loaded due to the high ac-impedance input source. External input capacitors serve primarily as energy-storage devices. They should be added close to the input pins of the MQ7221A and selected for bulk capacitance (at appropriate frequencies), low ESR, and high rms-ripple-current ratings. All external capacitors

should have appropriate voltage ratings. To reduce the amount of ripple current fed back to the input supply (input reflected-ripple current), an external L-C filter can be added with the inductance as close to the power module as possible.

MQ7221A's output ripple and transient response can be improved with the increasing output capacitance. When using output capacitors, take care that the total output capacitance does not exceed MQ7221A's Maximum Capacitive Load to avoid the module's protection condition in the start-up.

When an external L-C filter is added to reduce ripple on load, for best results, the filter components should be mounted close to the load circuit rather than the power module.

**When testing the relationship between external capacitors and output voltage noise, the oscilloscope's probe should be applied to the module's end directly with scope probe ground less than 10mm in length.**

## Input Fusing

The MQ7221A Series is not internally fused. Certain applications and/or safety agencies may require the installation of fuses at the inputs of power conversion components. The selection of the fuses should conform to the following:

1. The fuse value should be selected to be greater than the maximum input current of the module which occurs at the minimum input voltage.
2. Use either slow-blow or normal-blow fuses.
3. Both input traces must be capable of carrying a current of 1.5 times the value of the fuse without opening.

## Safety Considerations

MQ7221A's are non-isolated DC/DC converters. In general, all DC-DC's must be installed in compliance with relevant safety-agency specifications (usually UL/IEC/EN60950). In particular, for a non-isolated converter's output voltage to meet SELV (safety extra low voltage) requirements, its input must be SELV compliant. If the output needs to be ELV (extra low voltage), the input must be ELV.

## ON/OFF Control

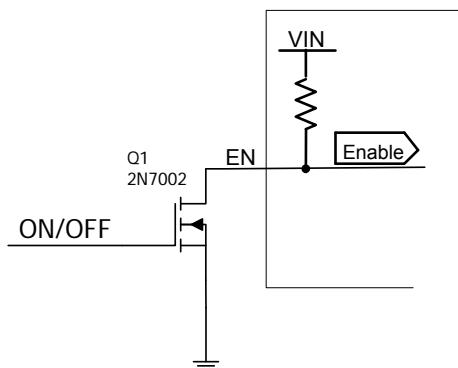


Fig1, Built-in Remote ON/OFF Control Logic Circuit

The MQ7221A power modules feature an On/Off pin for remote On/Off operation with positive logic. If not using the remote On/Off pin, leave the pin open (module will be On). The On/Off pin signal (Von/Off) is referenced to ground. The two outputs can be controlled independently.

## Output Overvoltage Protection

MQ7221A Series products do not incorporate output overvoltage protection. If the operating circuit requires protection against abnormal output voltage, voltage-limiting circuitry must be provided external to the power module.

## Output Overcurrent Protection (OCP)

MQ7221A incorporates overcurrent and short circuit protection. If the load current exceeds the overcurrent protection setpoint, the MQ7221A's internal overcurrent-protection circuitry goes into current-fold mode, this will keep a very low output voltage and a continuous 4.5A current. Once the overcurrent or short-circuit released, the module will be back normal operation status.

**Caution: Be careful never to operate MQ7221A in a "heavy overload" condition that is between the rated output current and the overcurrent protection setpoint. This can cause permanent damage to the components.**

## Output Voltage Trimming

MQ7221A's output voltage can be trimmed in certain ranges. Figure2 shows the circuit used to program output voltage. See Performance Specifications for allowable trim ranges in detail. Also customized products are available.

Trim with external resistor (Fig2), the equation as below:

$$R_{TRIM} = \frac{24200}{V_o - 0.804} - 5110$$

Resistor values are in  $\Omega$ ;  $V_o$  is desired output voltage.

For examples, to trim output to 1.5V, then

$$R_{TRIM} = \frac{24200}{1.5 - 0.804} - 5110 = 29600$$

So,  $R_{TRIM} = 29.4\text{k}\Omega$  1%

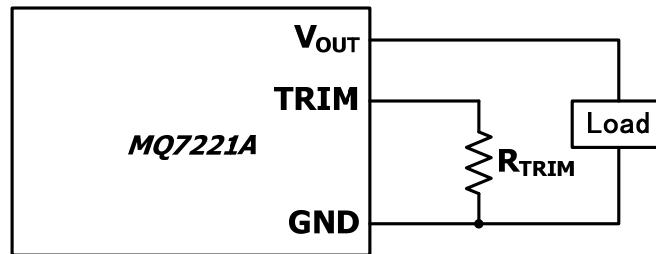


Fig2. Circuit configuration for programming output voltage using external resistor

For most common voltages, the required Trim resistors are as Table 1.

**Table 1, the required trim resistors  $R_{TRIM}$  for most common voltages**

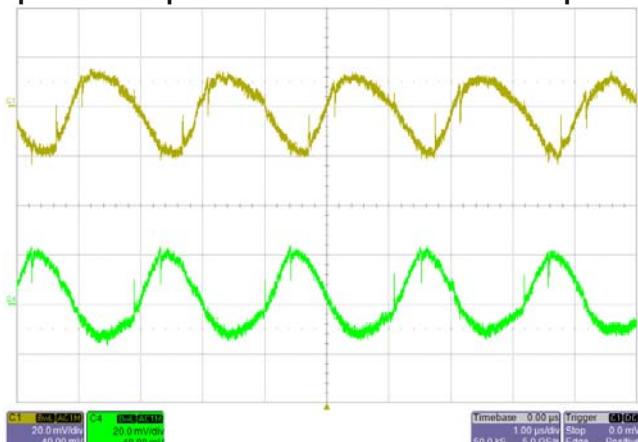
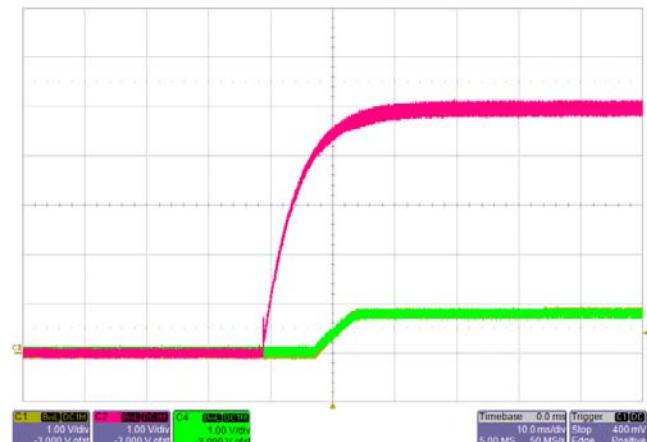
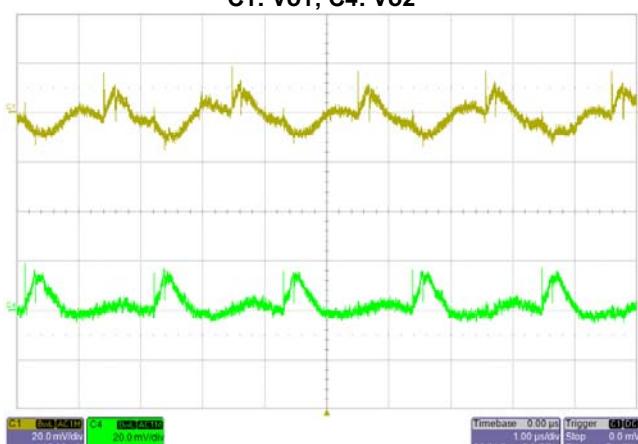
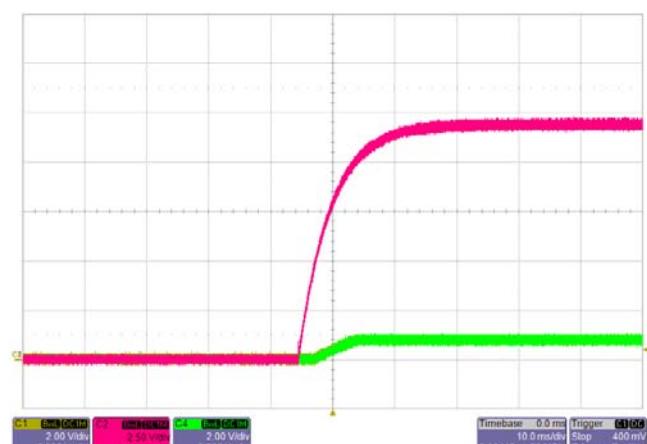
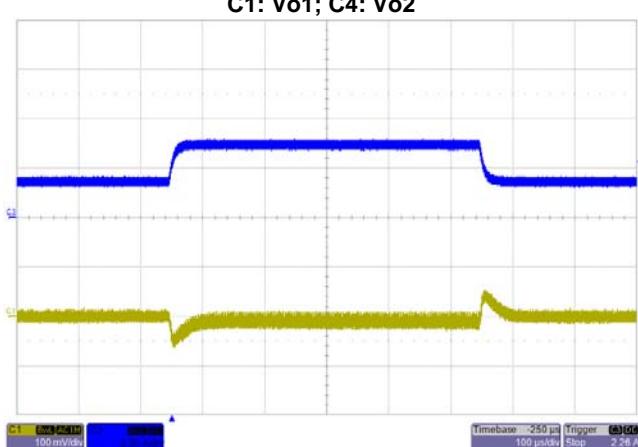
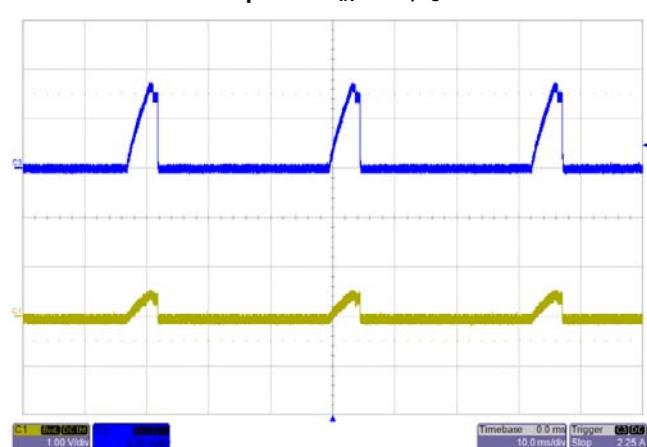
| Desired Voltages (V) | $R_{TRIM}$ ( $\text{k}\Omega$ ) |
|----------------------|---------------------------------|
| 0.8                  | OPEN                            |
| 0.9                  | 243                             |
| 1.0                  | 118                             |
| 1.1                  | 75                              |
| 1.2                  | 56.2                            |
| 1.3                  | 43.2                            |
| 1.5                  | 29.4                            |
| 1.8                  | 19.1                            |
| 2.5                  | 9.09                            |
| 3.3                  | 4.58                            |
| 5.0                  | 0.657                           |

**MQ7221A Typical Characteristics – output adjusted to 0.8V**

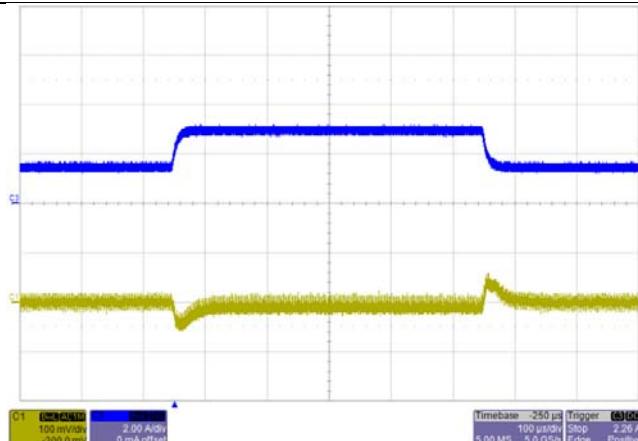
General conditions:

Input filter: 68µF/20Vx1 TAN;

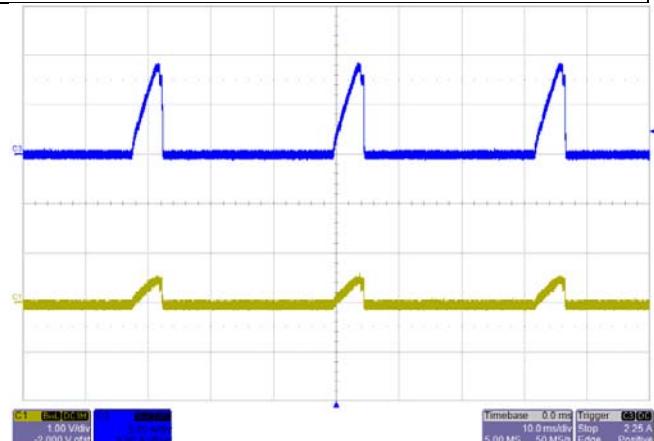
Output filter: 68µF/20V TAN+104/50V ceramic capacitor

Ripple&Noise  $V_{IN}=5V$ ,  $I_o=3A$  for both channel  
C1: Vo1; C4: Vo2Start-up With  $V_{IN}=5V$ ,  $I_o=3A$ Ripple&Noise  $V_{IN}=12V$ ,  $I_o=3A$  for both channel  
C1: Vo1; C4: Vo2Start-up With  $V_{IN}=12V$ ,  $I_o=3A$ Transient Response  $V_{IN}=5V$ ,  $I_o=3A$   
Step from 1.5A~3A~1.5A(Vo1)Short-Circuit Output  $V_{IN}=5V$ (Vo1)

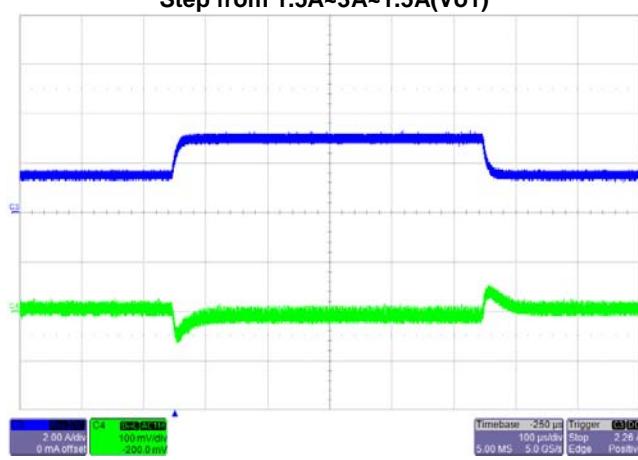
## Micro-BiTarzan™ MQ7221A



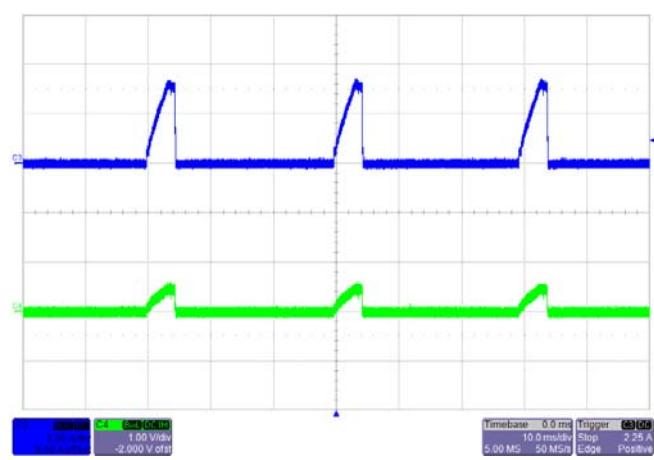
**Transient Response  $V_{IN}=12V$ ,  $I_O=3A$**   
Step from 1.5A~3A~1.5A( $Vo_1$ )



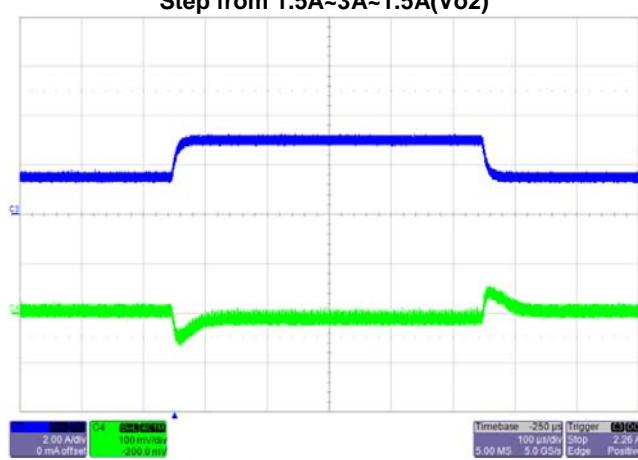
**Short-Circuit Output  $V_{IN}=12V(Vo_1)$**



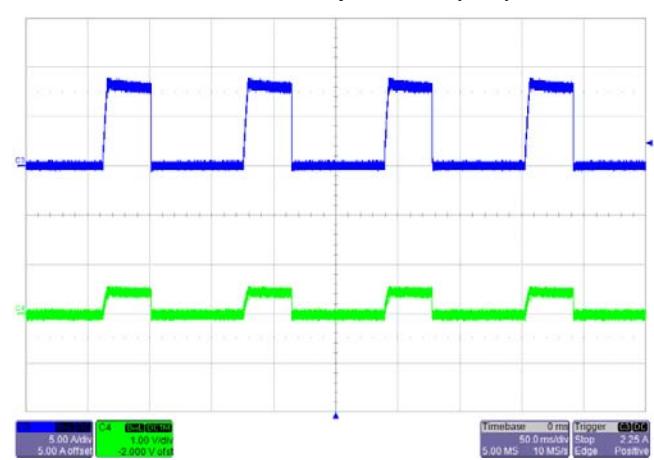
**Transient Response  $V_{IN}=5V$ ,  $I_O=3A$**   
Step from 1.5A~3A~1.5A( $Vo_2$ )



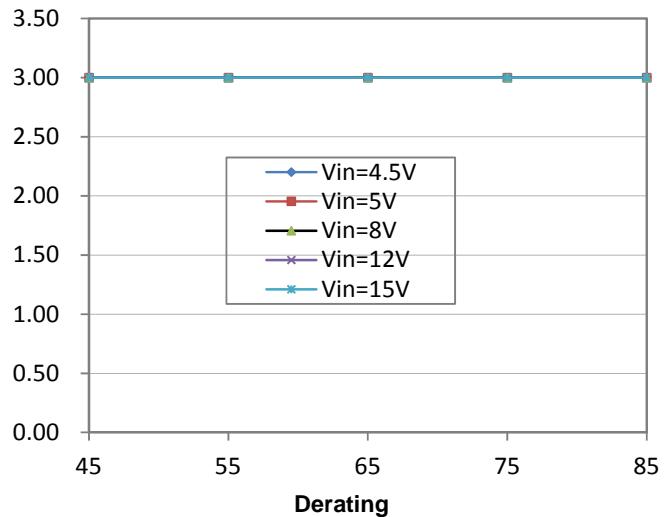
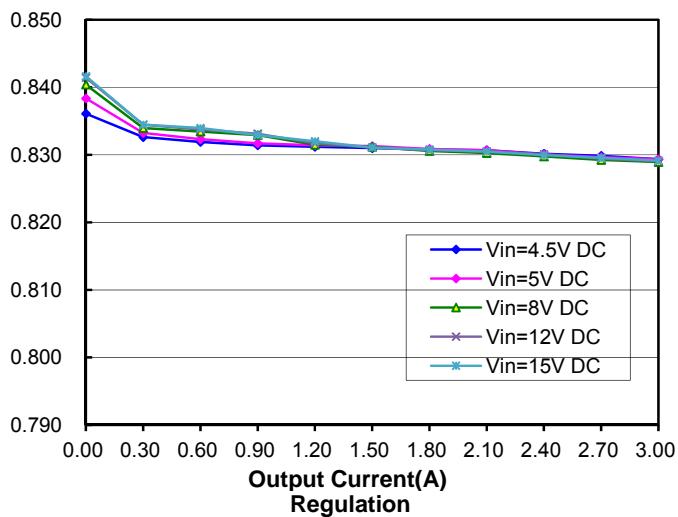
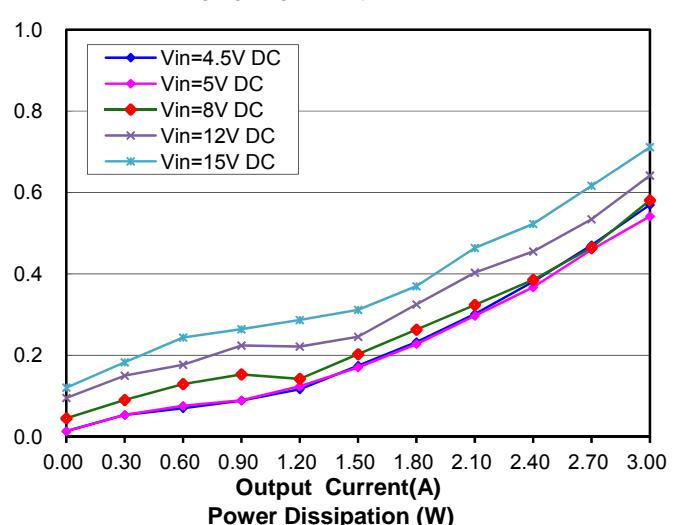
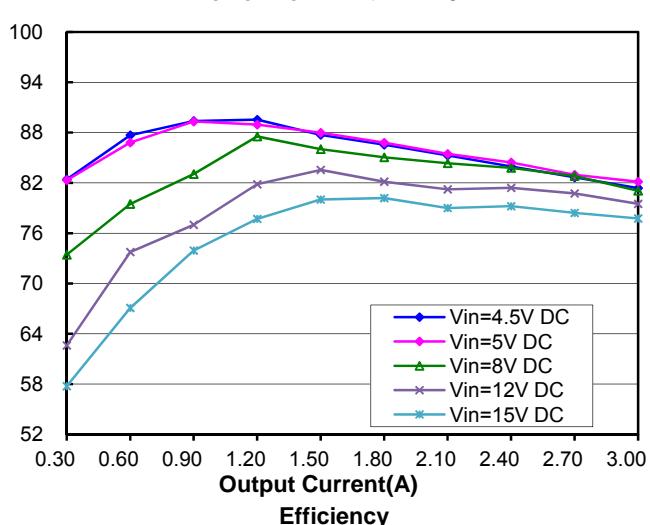
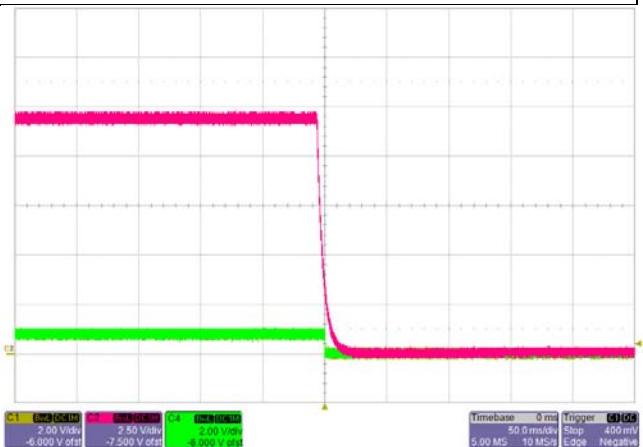
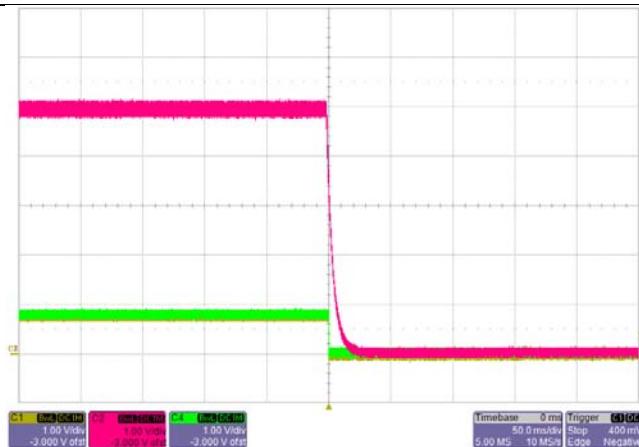
**Short-Circuit Output  $V_{IN}=5V(Vo_2)$**



**Transient Response  $V_{IN}=12V$ ,  $I_O=3A$**   
Step from 1.5A~3A~1.5A( $Vo_2$ )



**Short-Circuit Output  $V_{IN}=12V(Vo_2)$**

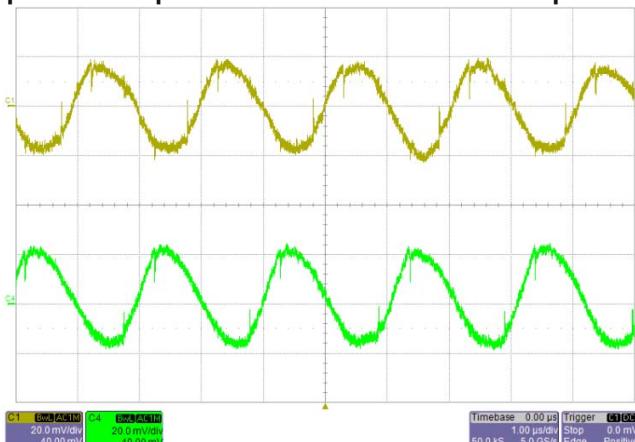


## MQ7221A Typical Characteristics – output adjusted to 1V

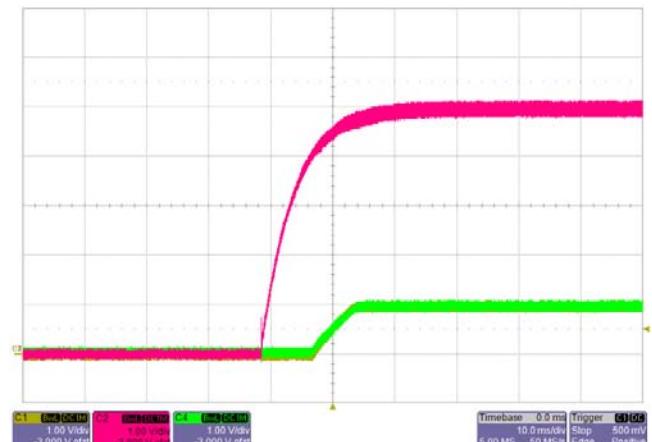
General conditions:

Input filter: 68µF/20Vx1 TAN;

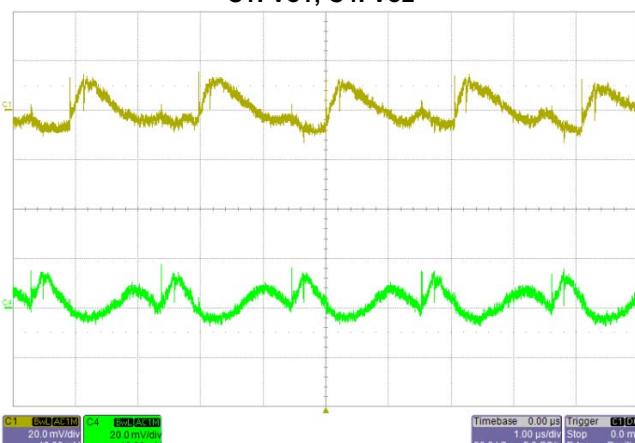
Output filter: 68µF/20V TAN+104/50V ceramic capacitor



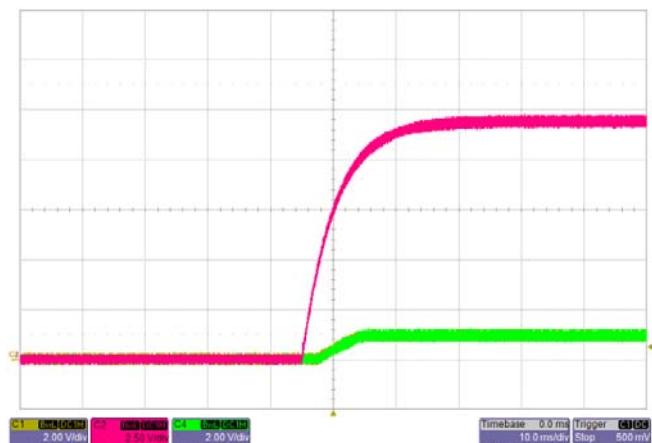
Ripple&Noise  $V_{IN}=5V$ ,  $I_o=3A$  for both channel  
C1: Vo1; C4: Vo2



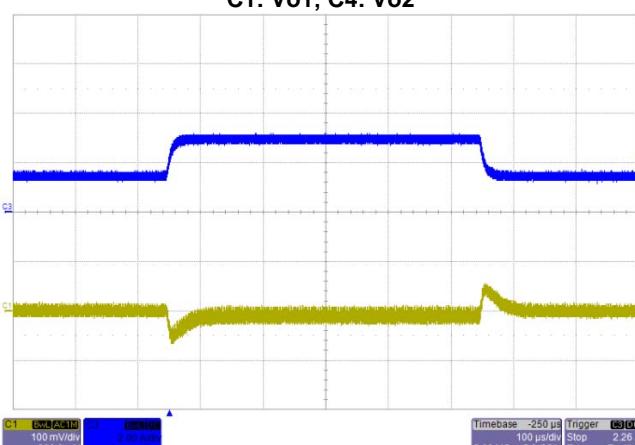
Start-up With  $V_{IN}=5V$ ,  $I_o=3A$



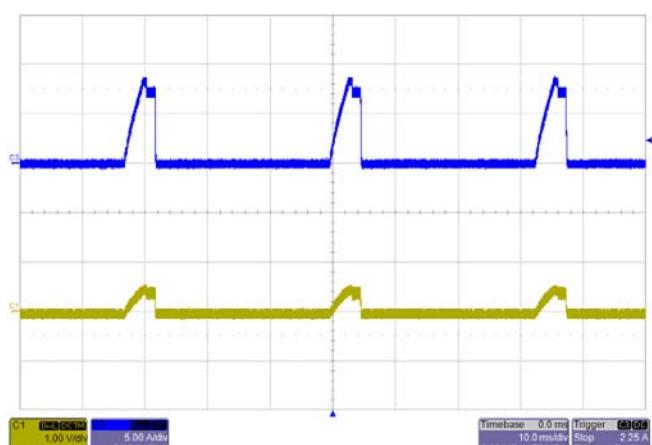
Ripple&Noise  $V_{IN}=12V$ ,  $I_o=3A$  for both channel  
C1: Vo1; C4: Vo2



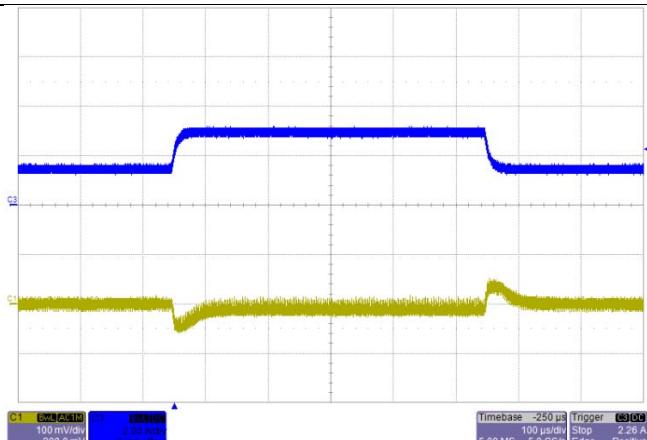
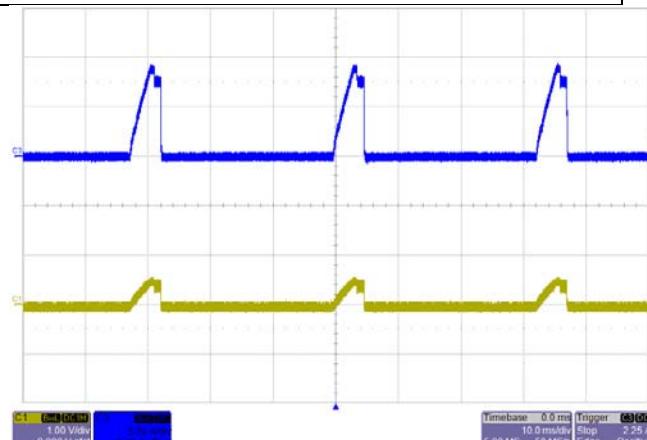
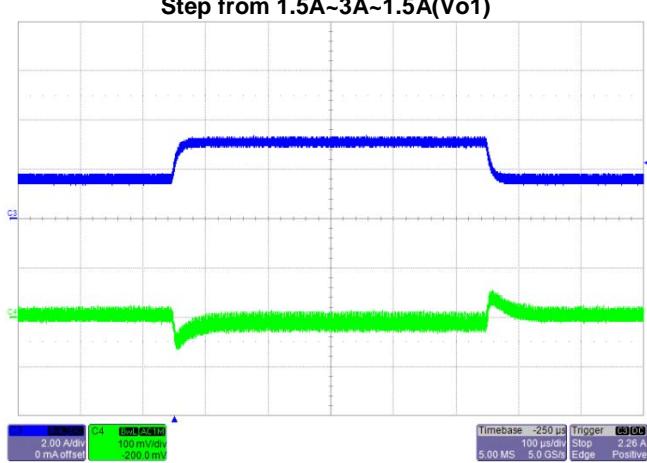
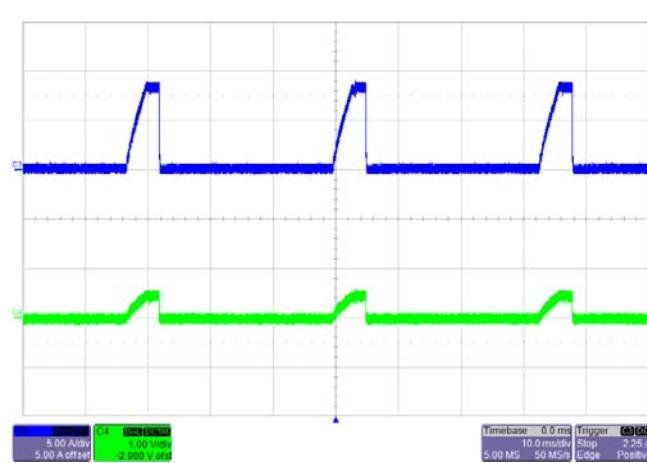
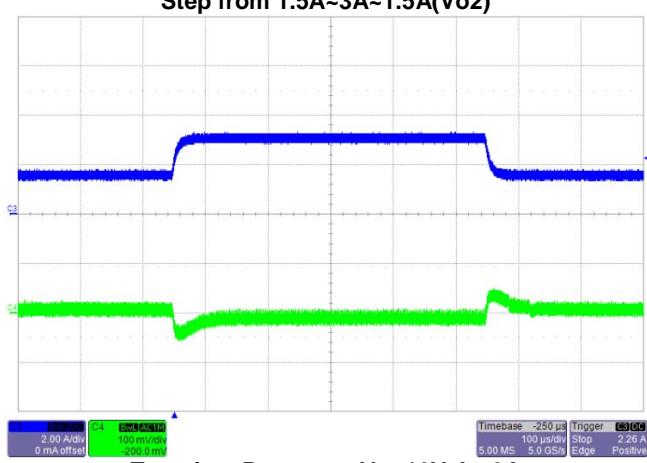
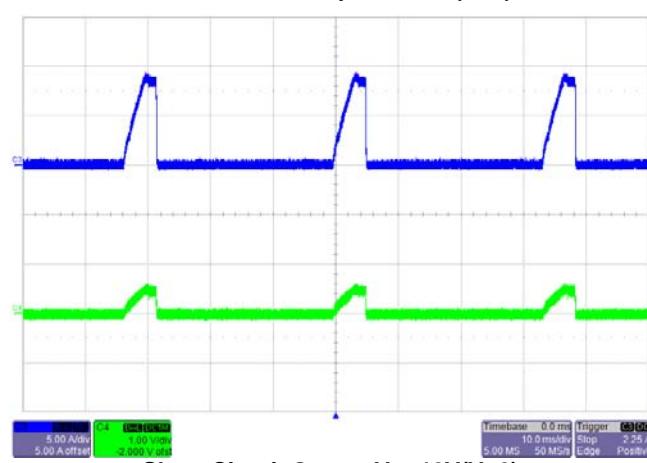
Start-up With  $V_{IN}=12V$ ,  $I_o=3A$

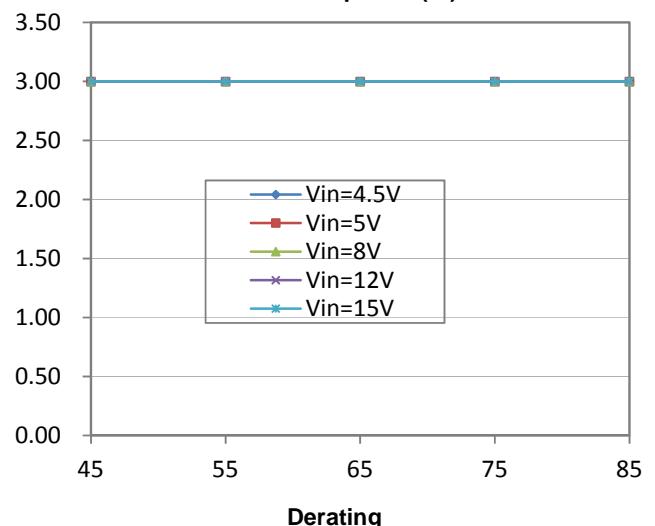
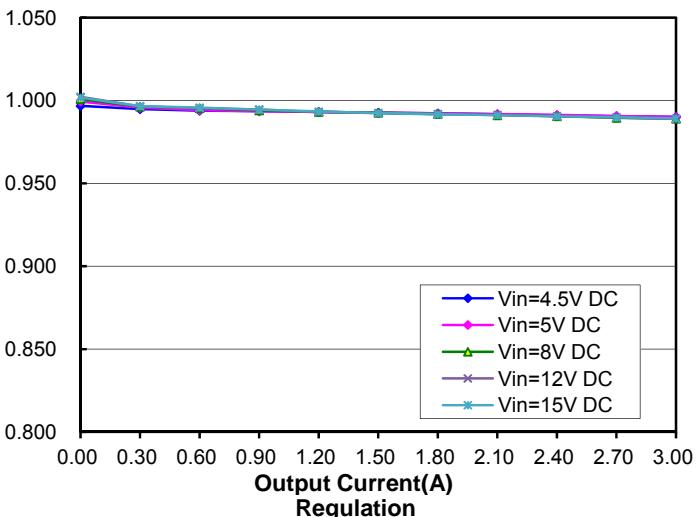
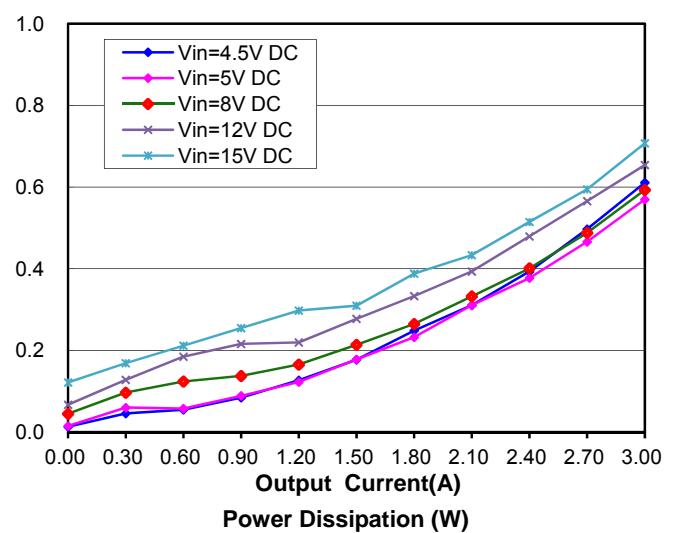
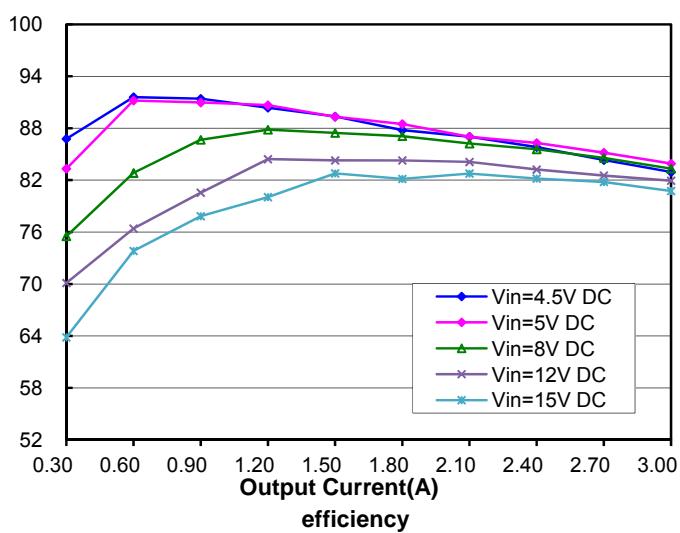
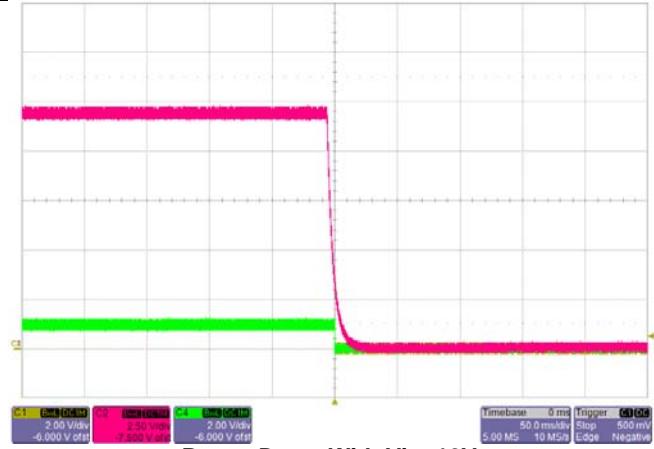
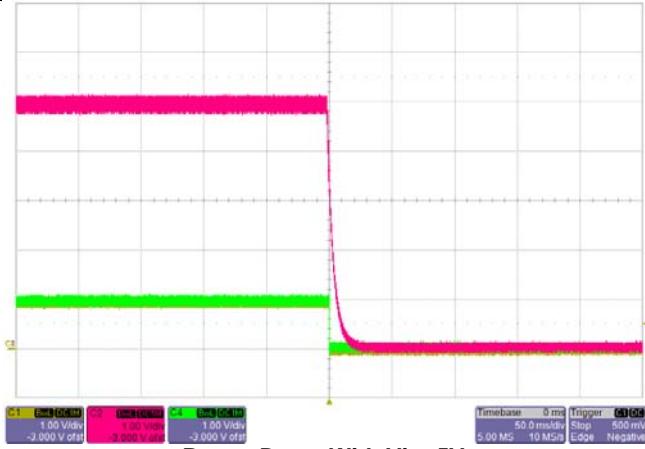


Transient Response  $V_{IN}=5V$ ,  $I_o=3A$   
Step from 1.5A~3A~1.5A(Vo1)



Short-Circuit Output  $V_{IN}=5V$ (Vo1)

Transient Response  $V_{IN}=12V$ ,  $I_o=3A$   
Step from 1.5A~3A~1.5A( $V_{o1}$ )Short-Circuit Output  $V_{IN}=12V(V_{o1})$ Transient Response  $V_{IN}=5V$ ,  $I_o=3A$   
Step from 1.5A~3A~1.5A( $V_{o2}$ )Short-Circuit Output  $V_{IN}=5V(V_{o2})$ Transient Response  $V_{IN}=12V$ ,  $I_o=3A$   
Step from 1.5A~3A~1.5A( $V_{o2}$ )Short-Circuit Output  $V_{IN}=12V(V_{o2})$

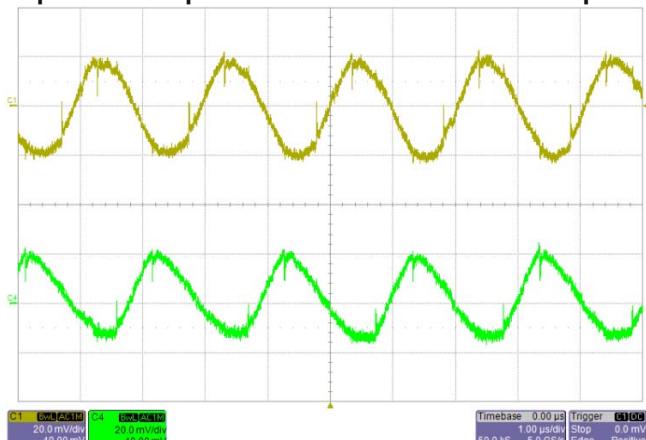
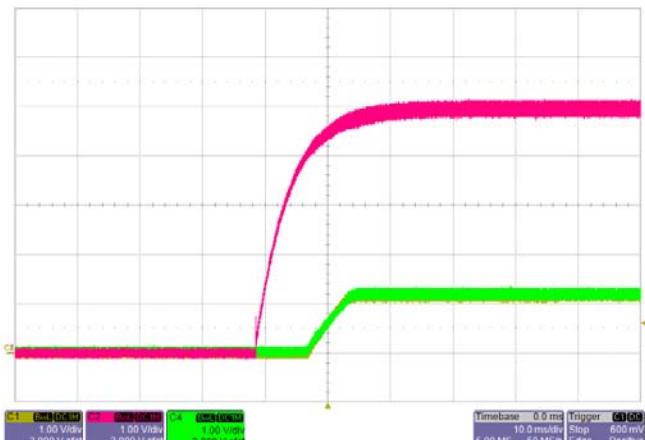
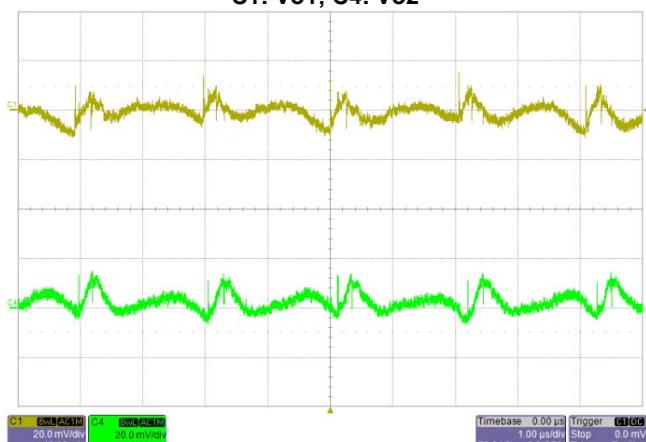
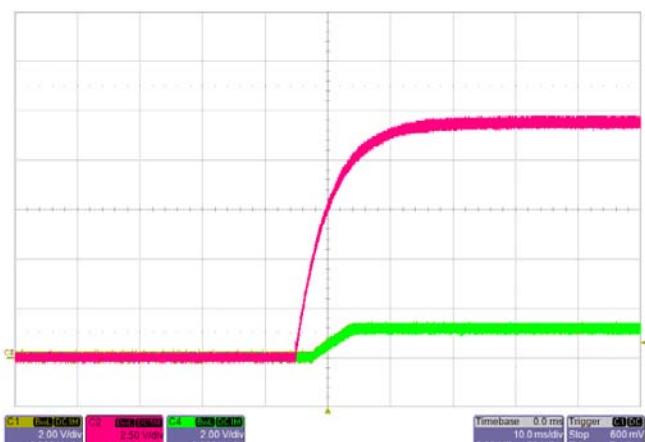
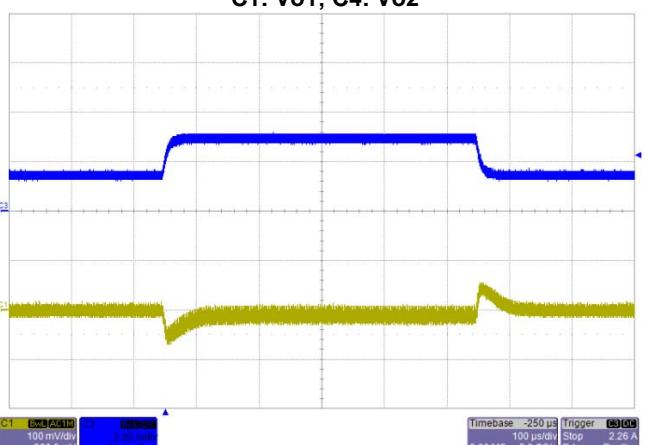
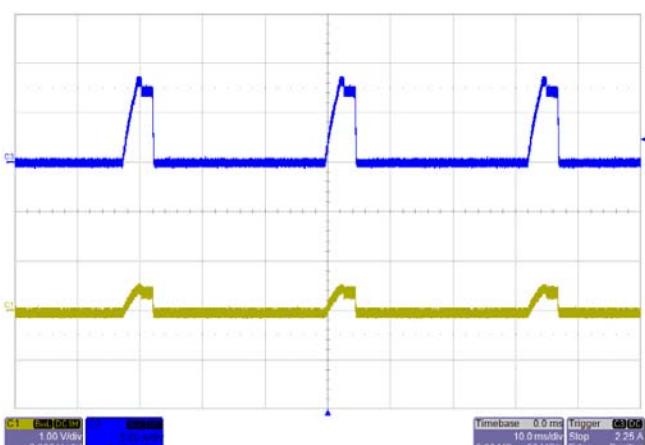


**MQ7221A Typical Characteristics – output adjusted to 1.2V**

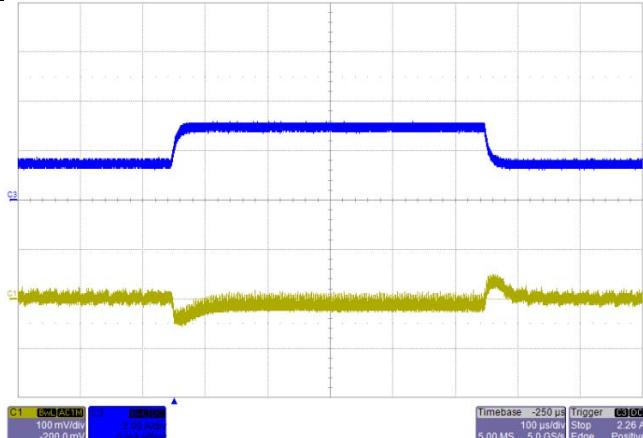
General conditions:

Input filter: 68µF/20Vx1 TAN;

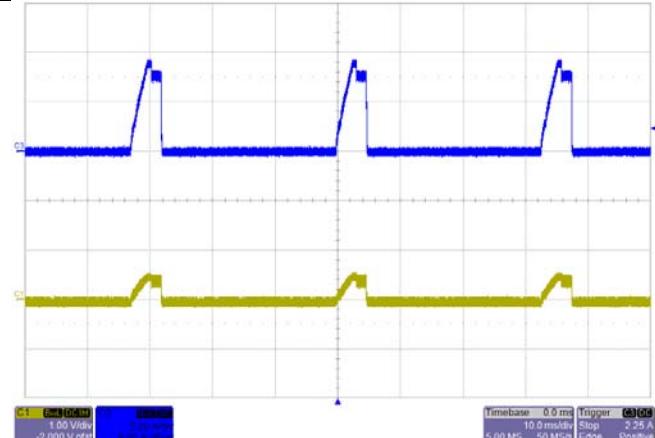
Output filter: 68µF/20V TAN+104/50V ceramic capacitor

Ripple&Noise  $V_{IN}=5V$ ,  $I_o=3A$  for both channel  
C1: Vo1; C4: Vo2Start-up With  $V_{IN}=5V$ ,  $I_o=3A$ Ripple&Noise  $V_{IN}=12V$ ,  $I_o=3A$  for both channel  
C1: Vo1; C4: Vo2Start-up With  $V_{IN}=12V$ ,  $I_o=3A$ Transient Response  $V_{IN}=5V$ ,  $I_o=3A$   
Step from 1.5A~3A~1.5A(Vo1)Short-Circuit Output  $V_{IN}=5V$ (Vo1)

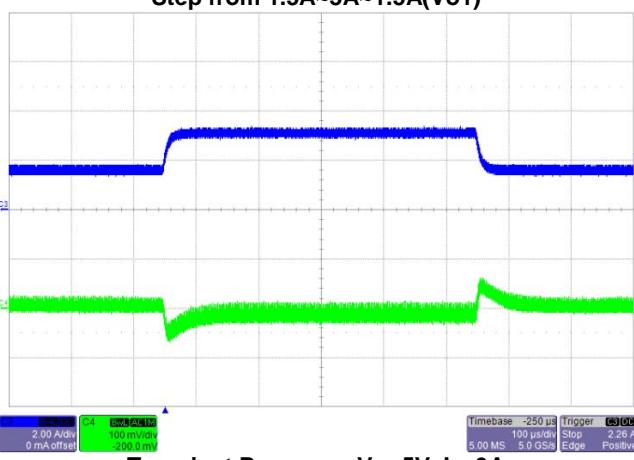
## Micro-BiTarzan™ MQ7221A



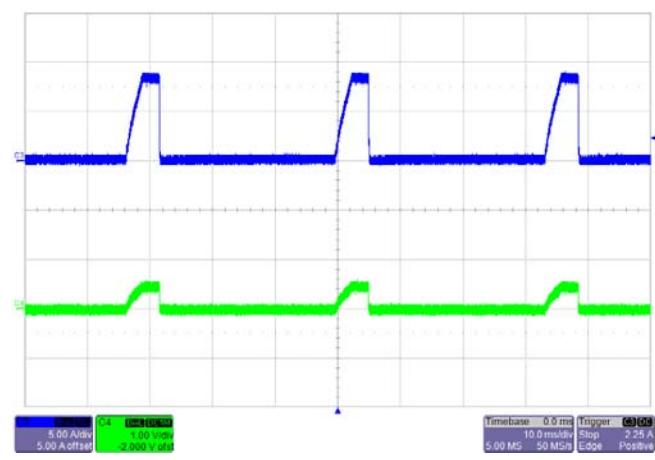
**Transient Response  $V_{IN}=12V$ ,  $I_o=3A$   
Step from 1.5A~3A~1.5A( $V_{O1}$ )**



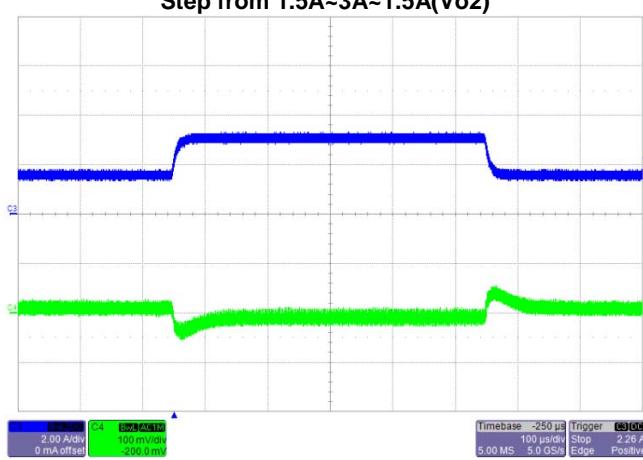
**Short-Circuit Output  $V_{IN}=12V(V_{O1})$**



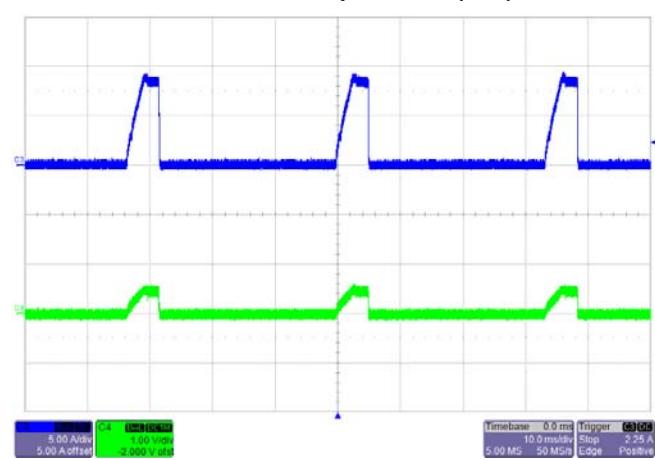
**Transient Response  $V_{IN}=5V$ ,  $I_o=3A$   
Step from 1.5A~3A~1.5A( $V_{O2}$ )**



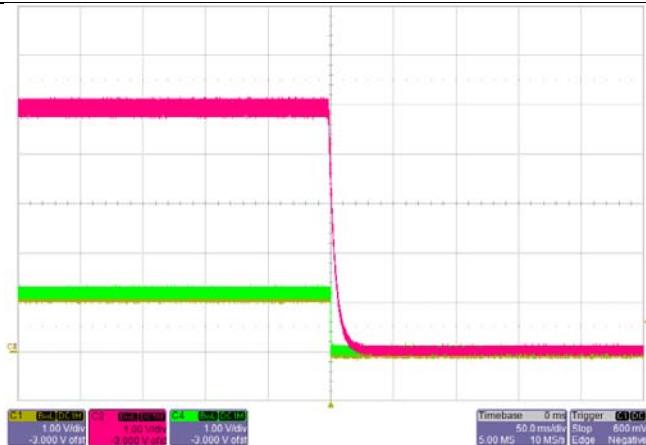
**Short-Circuit Output  $V_{IN}=5V(V_{O2})$**



**Transient Response  $V_{IN}=12V$ ,  $I_o=3A$   
Step from 1.5A~3A~1.5A( $V_{O2}$ )**



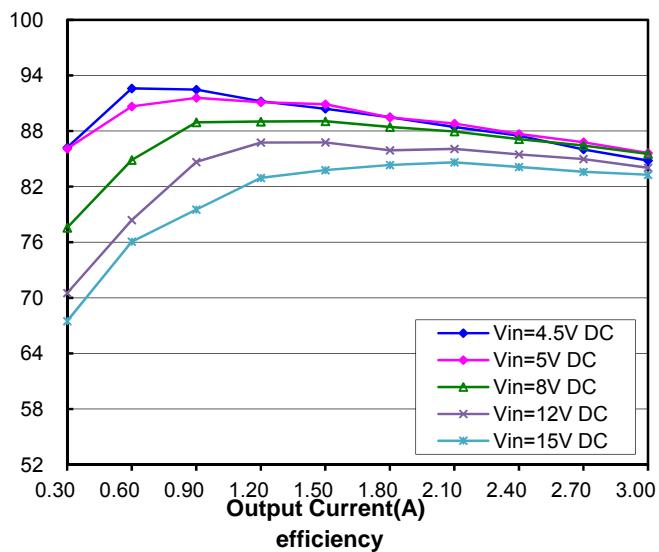
**Short-Circuit Output  $V_{IN}=12V(V_{O2})$**



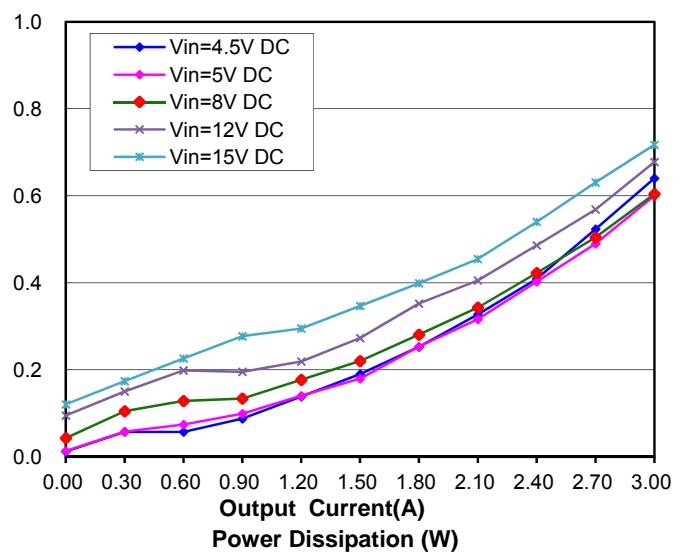
Power Down With Vin=5V



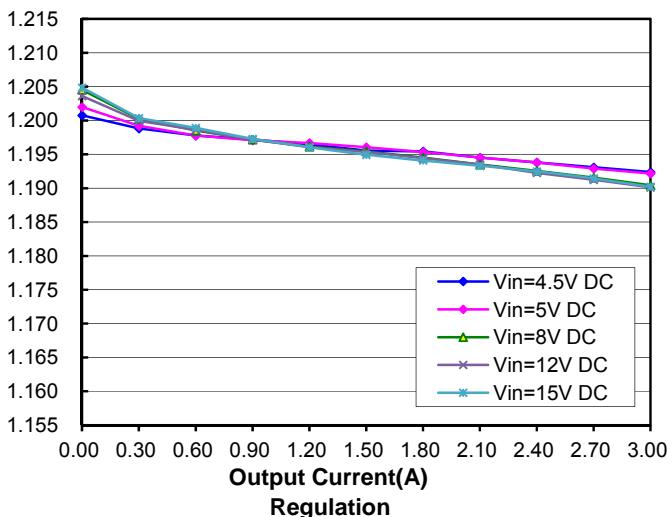
Power Down With Vin=12V



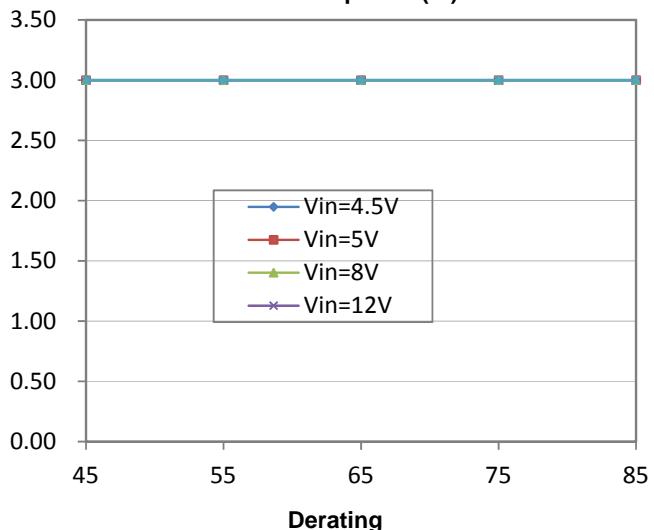
efficiency



Power Dissipation (W)



Regulation



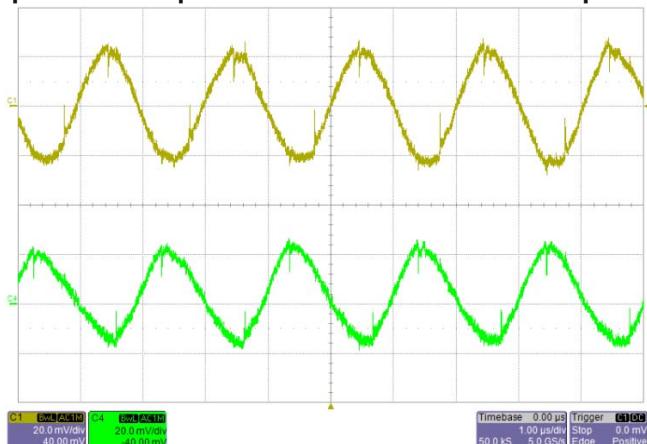
Derating

**MQ7221A Typical Characteristics – output adjusted to 1.5V**

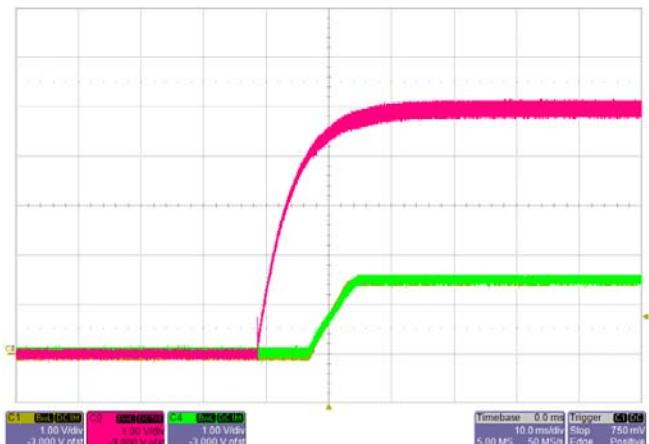
General conditions:

Input filter : 68µF/20Vx1 TAN;

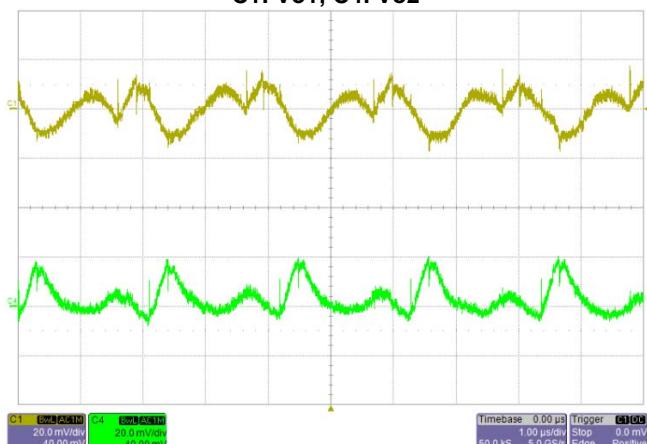
Output filter: 68µF/20V TAN+104/50V ceramic capacitor



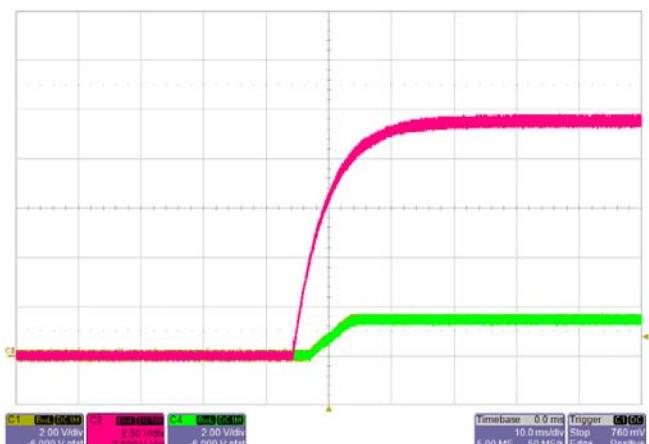
Ripple&Noise  $V_{IN}=5V$ ,  $I_o=3A$  for both channel  
C1: Vo1; C4: Vo2



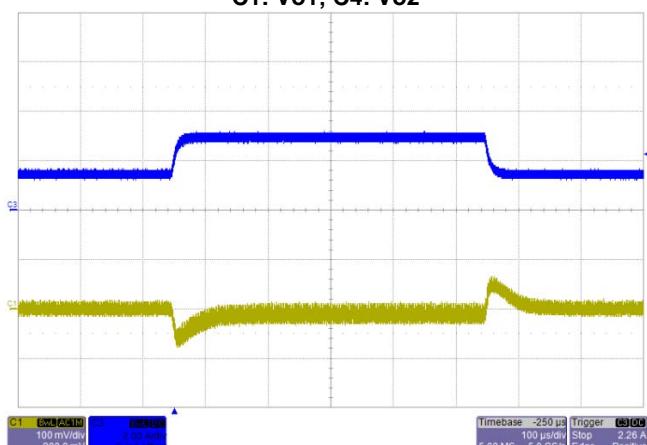
Start-up With  $V_{IN}=5V$ ,  $I_o=3A$



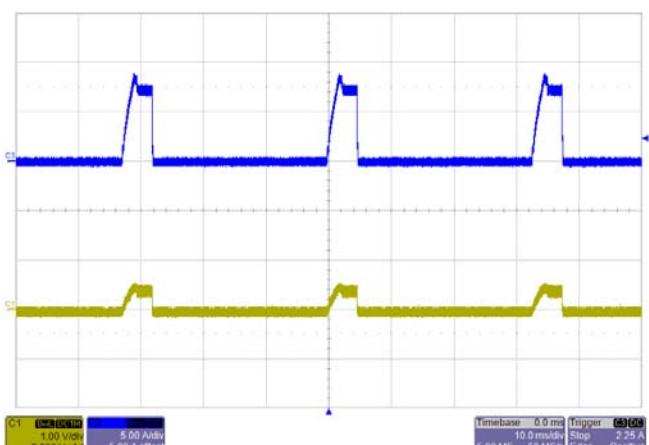
Ripple&Noise  $V_{IN}=12V$ ,  $I_o=3A$  for both channel  
C1: Vo1; C4: Vo2



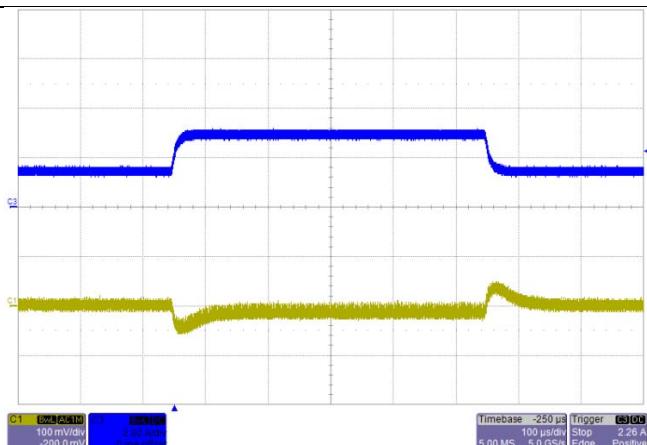
Start-up With  $V_{IN}=12V$ ,  $I_o=3A$



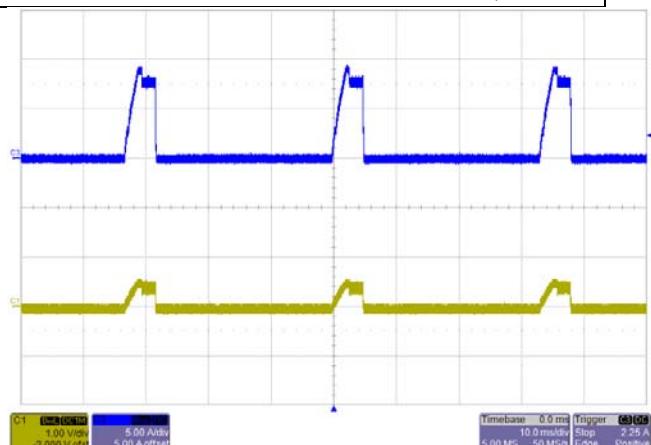
Transient Response  $V_{IN}=5V$ ,  $I_o=3A$   
Step from 1.5A~3A~1.5A(Vo1)



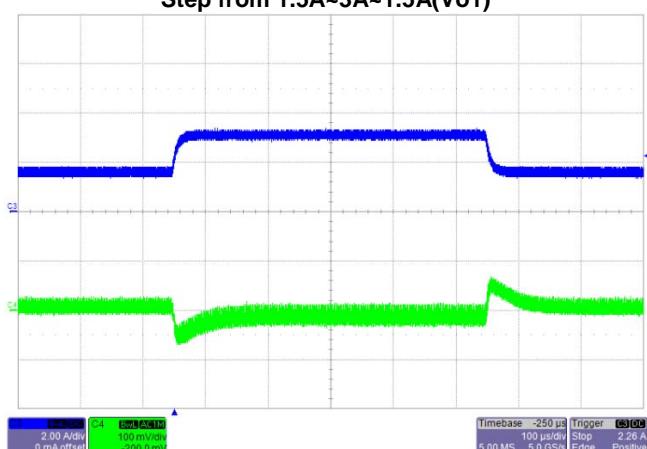
Short-Circuit Output  $V_{IN}=5V$ (Vo1)



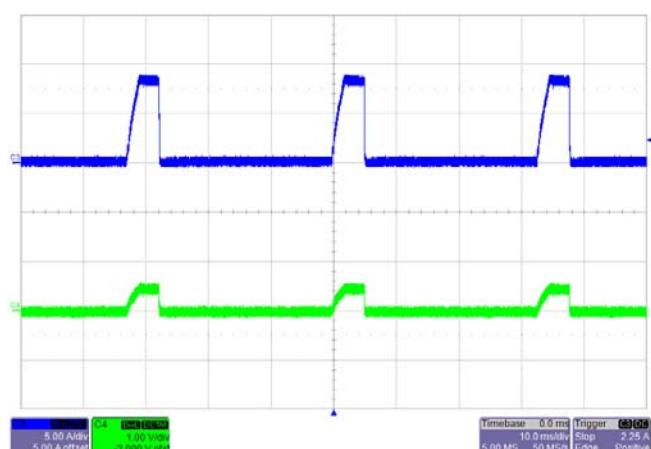
Transient Response  $V_{IN}=12V$ ,  $I_o=3A$   
Step from 1.5A~3A~1.5A( $V_{o1}$ )



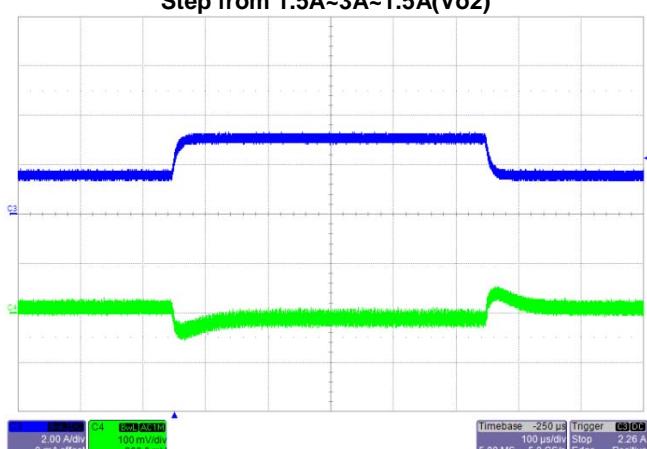
Short-Circuit Output  $V_{IN}=12V$ ( $V_{o1}$ )



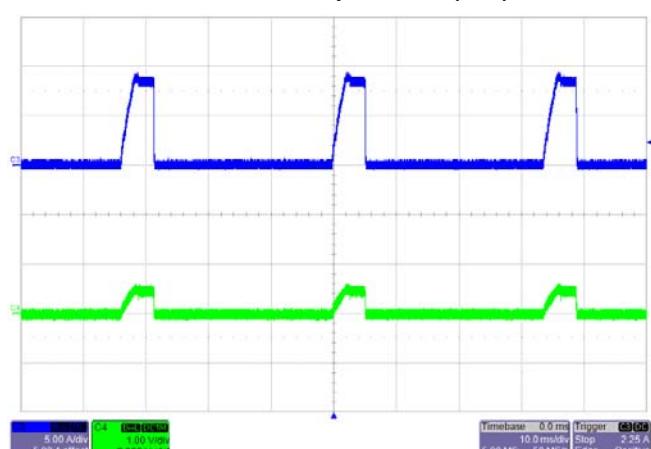
Transient Response  $V_{IN}=5V$ ,  $I_o=3A$   
Step from 1.5A~3A~1.5A( $V_{o2}$ )



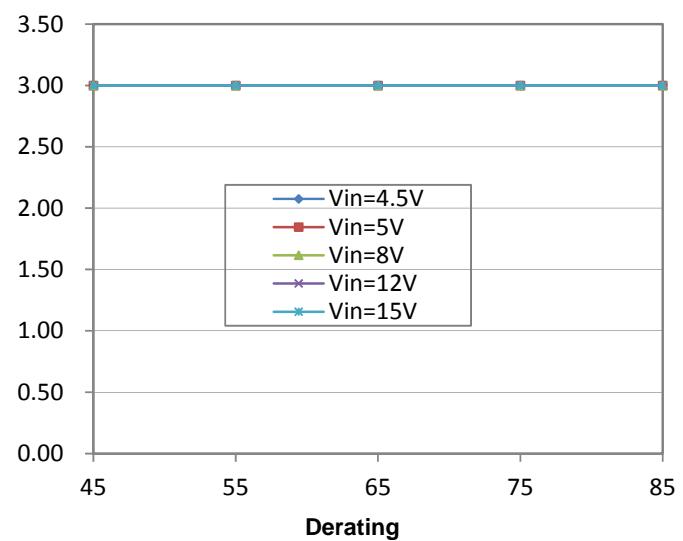
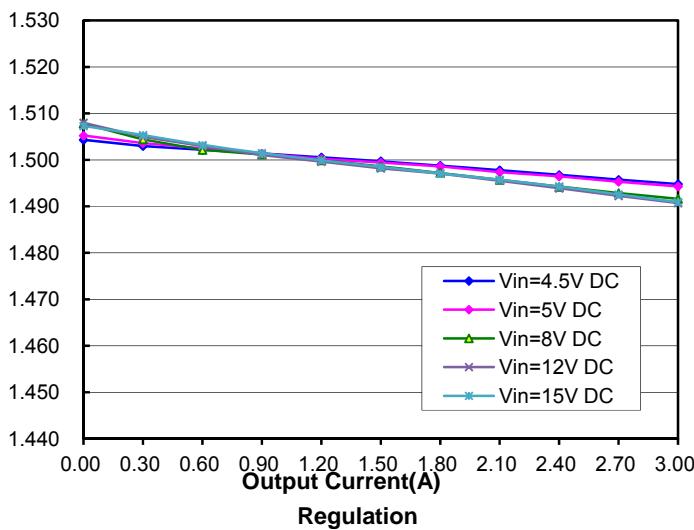
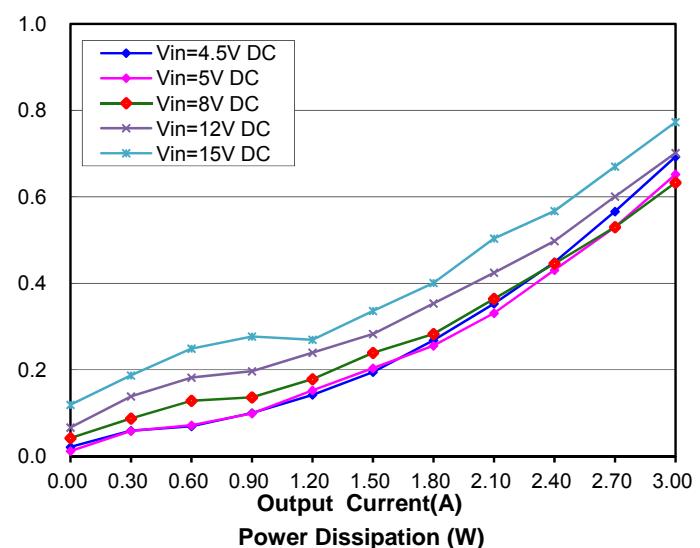
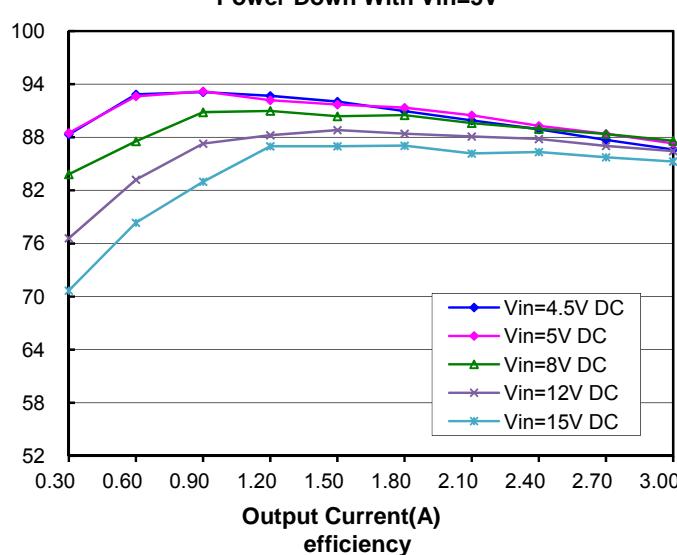
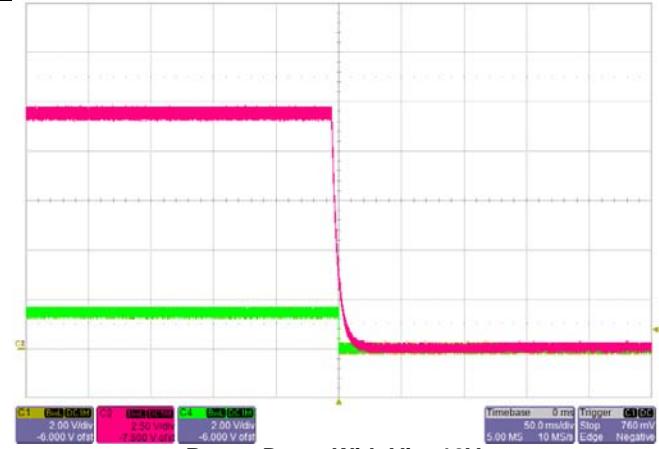
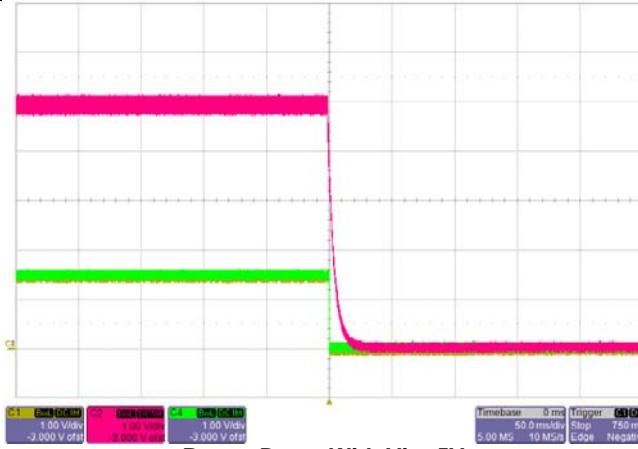
Short-Circuit Output  $V_{IN}=5V$ ( $V_{o2}$ )



Transient Response  $V_{IN}=12V$ ,  $I_o=3A$   
Step from 1.5A~3A~1.5A( $V_{o2}$ )



Short-Circuit Output  $V_{IN}=12V$ ( $V_{o2}$ )

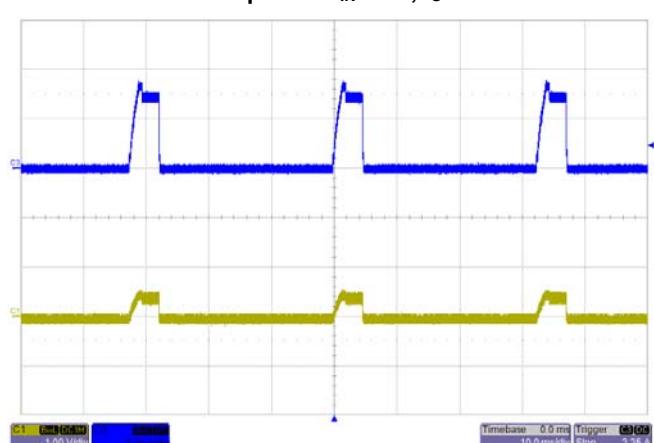
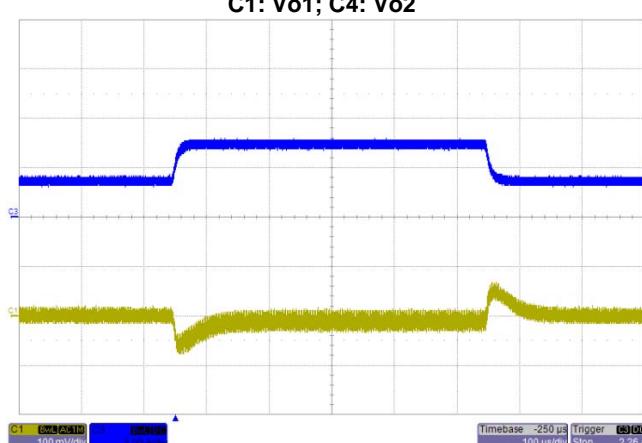
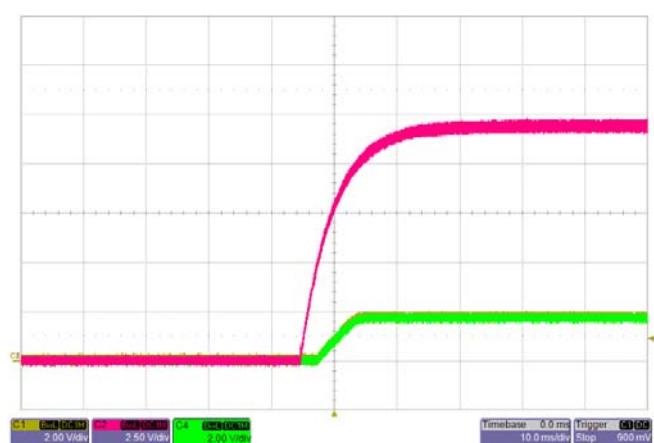
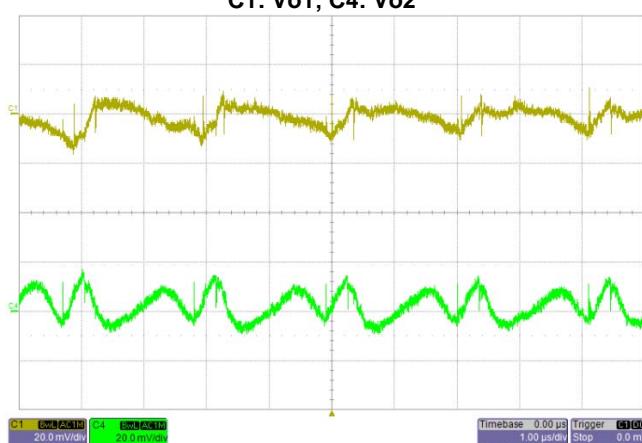
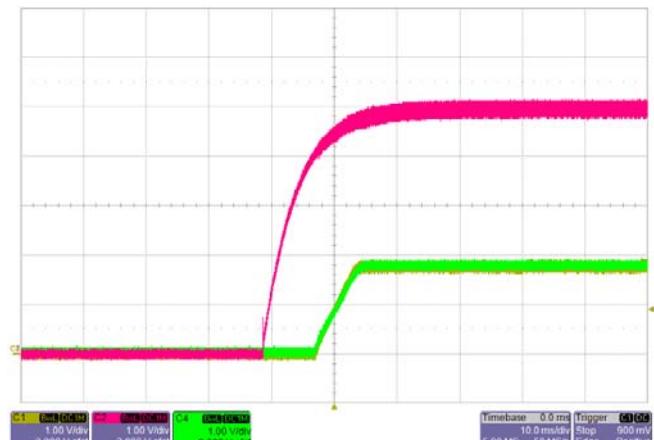
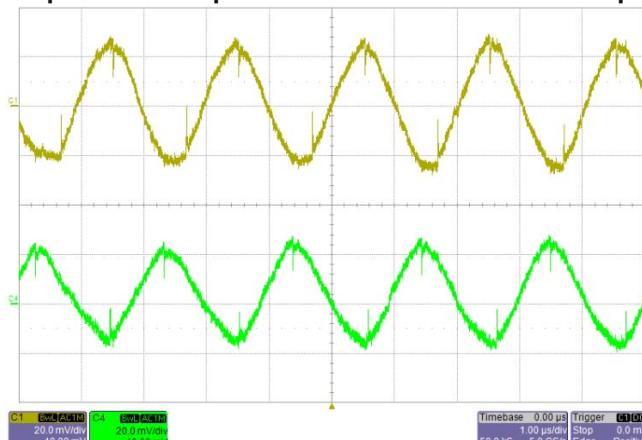


**MQ7221A Typical Characteristics – output adjusted to 1.8V**

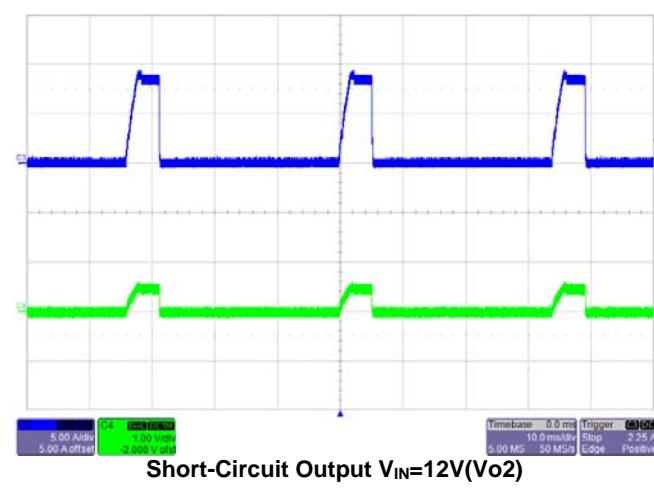
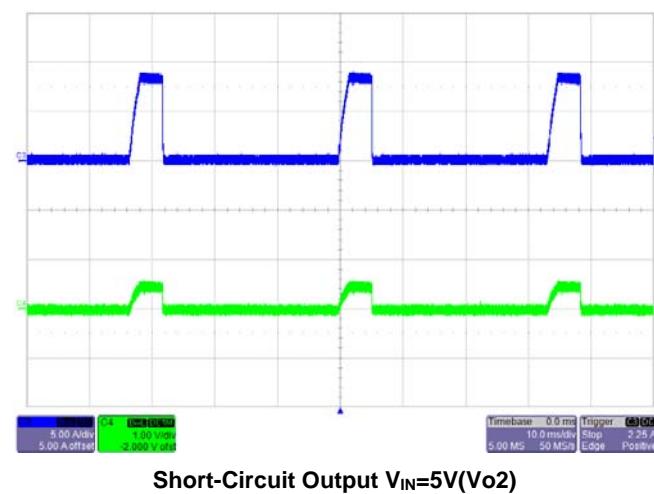
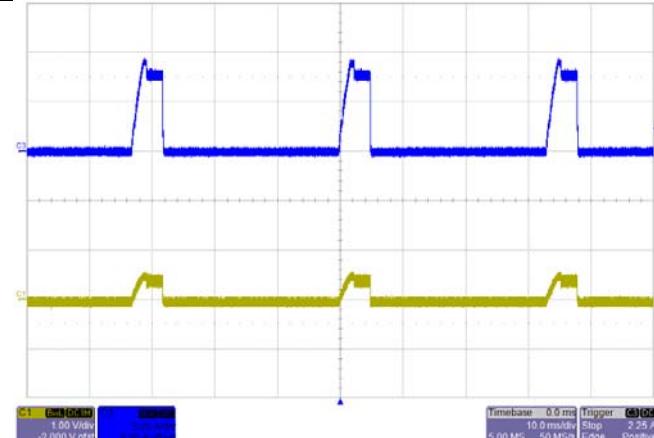
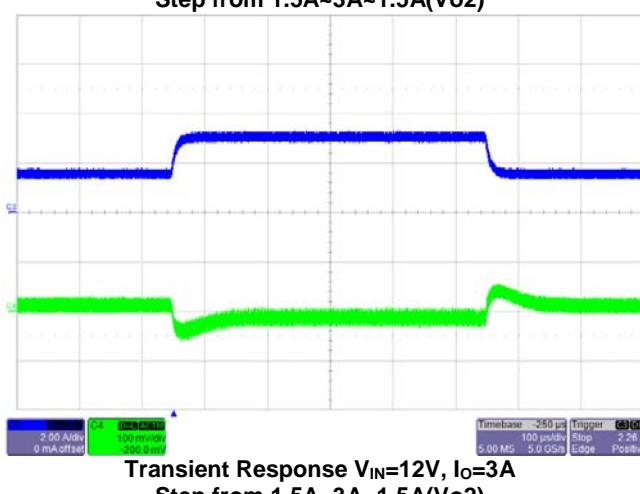
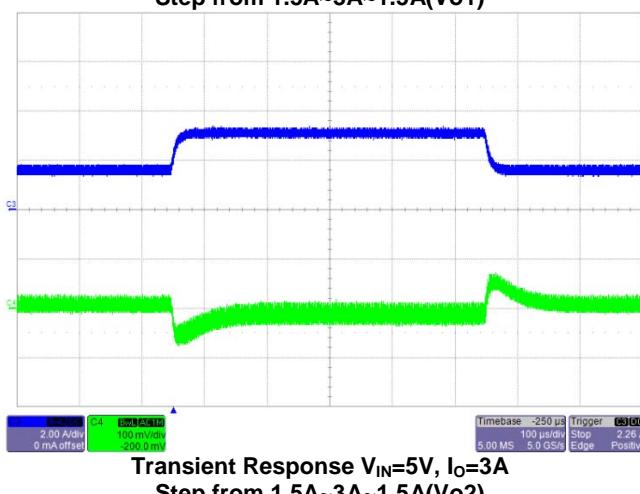
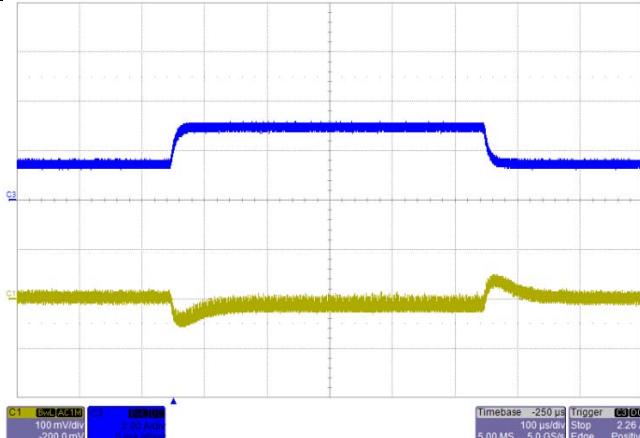
General conditions:

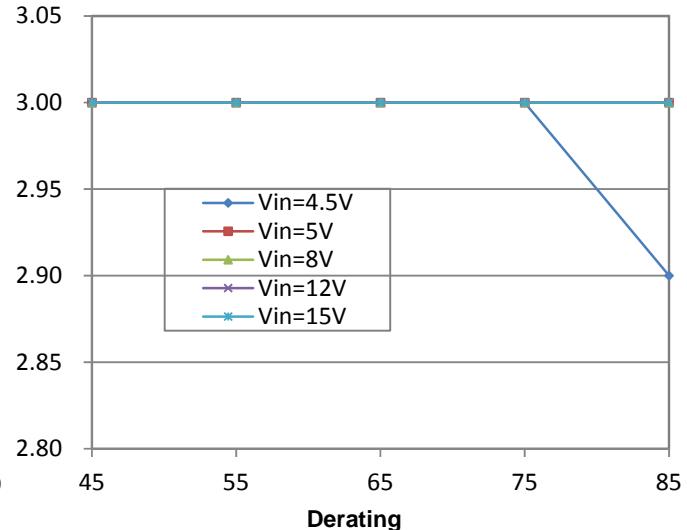
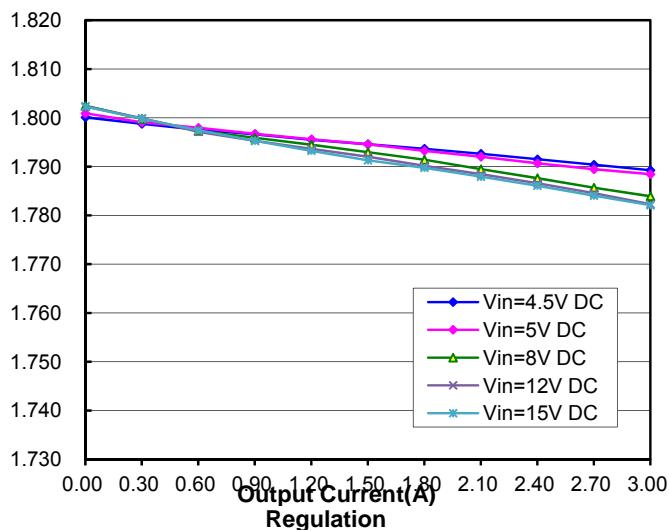
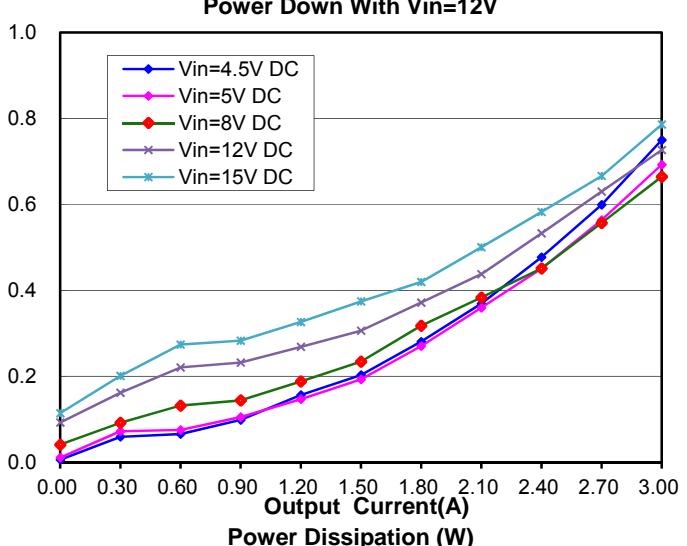
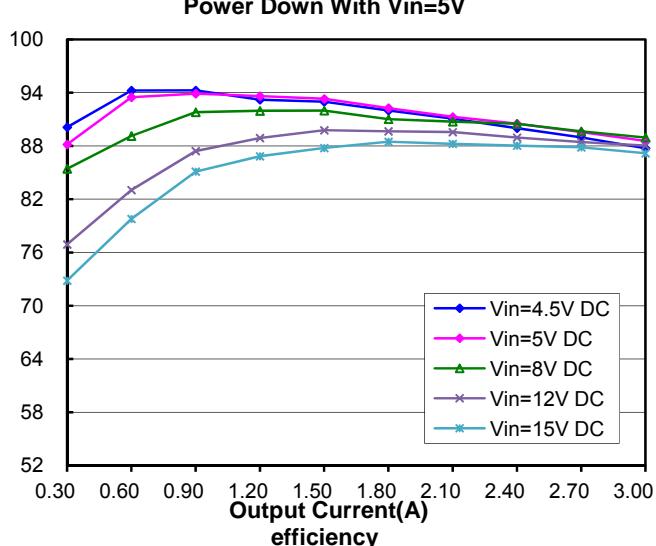
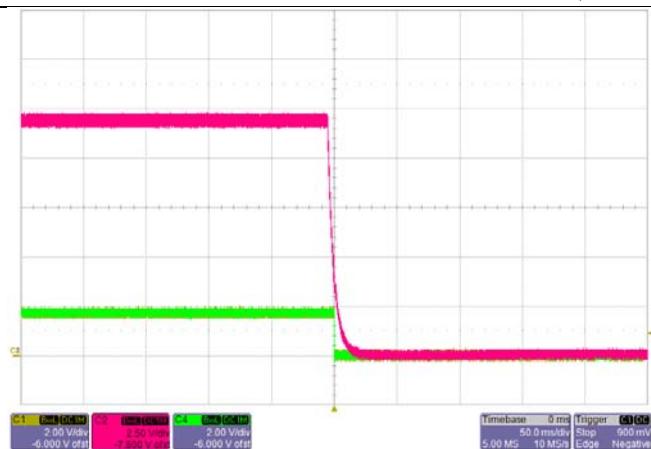
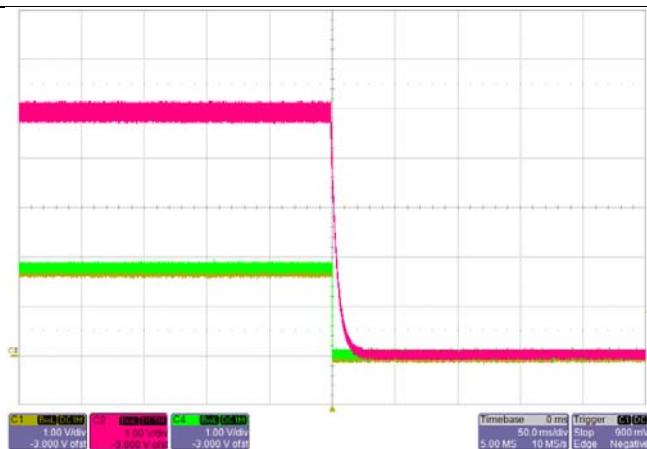
Input filter: 68µF/20Vx1 TAN;

Output filter : 68µF/20V TAN+104/50V ceramic capacitor



## Micro-BiTarzan™ MQ7221A



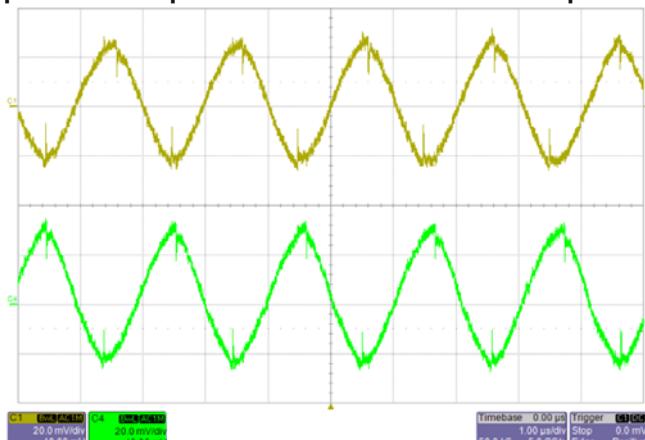


**MQ7221A Typical Characteristics – output adjusted to 2.5V**

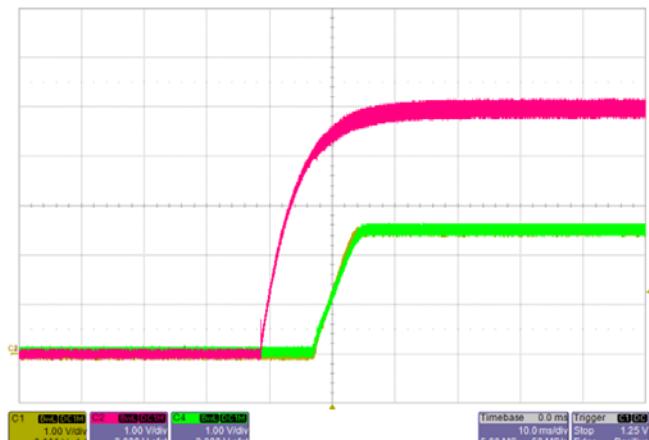
General conditions:

Input filter : 68µF/20Vx1 TAN;

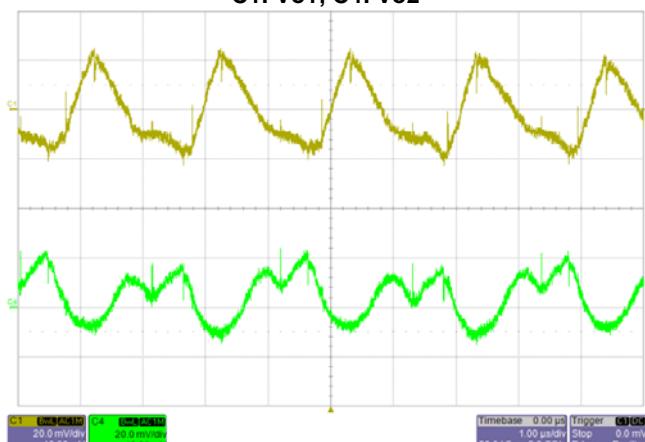
Output filter: 68µF/20V TAN+104/50V ceramic capacitor



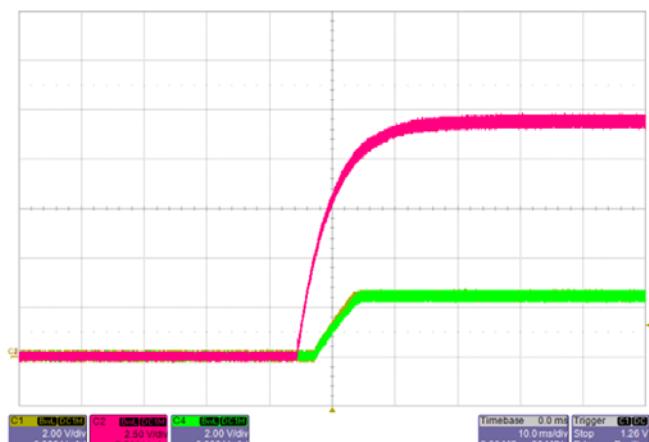
Ripple&Noise  $V_{IN}=5V$ ,  $I_o=3A$  for both channel  
C1: Vo1; C4: Vo2



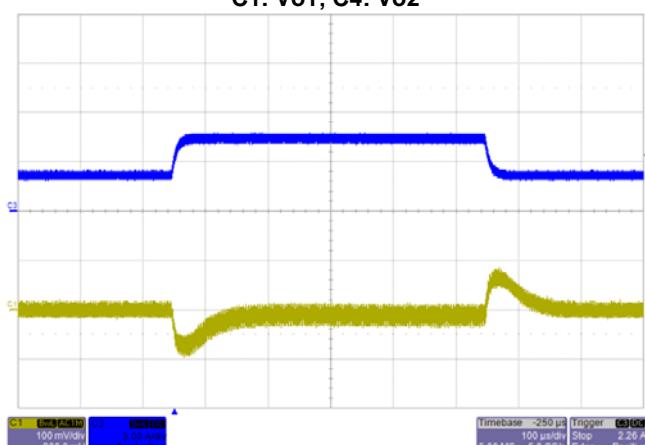
Start-up With  $V_{IN}=5V$ ,  $I_o=3A$



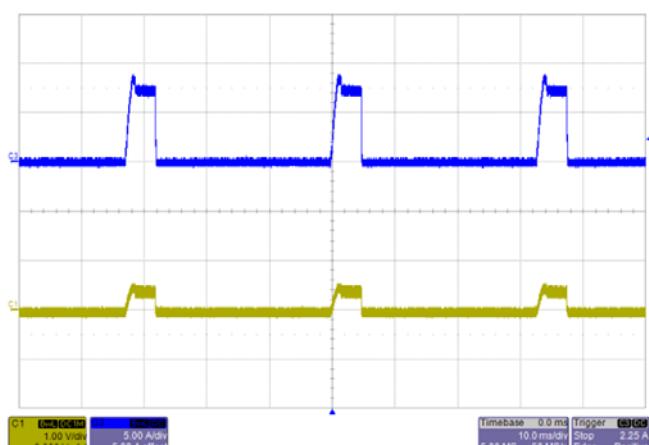
Ripple&Noise  $V_{IN}=12V$ ,  $I_o=3A$  for both channel  
C1: Vo1; C4: Vo2



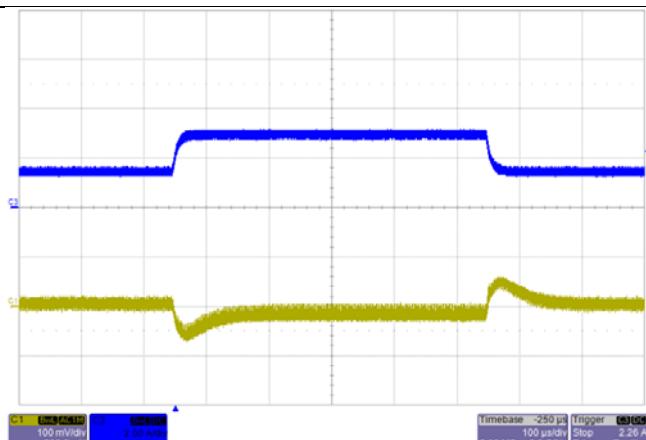
Start-up With  $V_{IN}=12V$ ,  $I_o=3A$



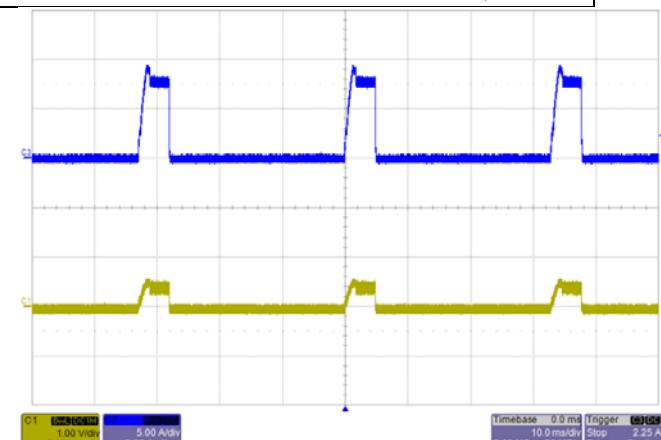
Transient Response  $V_{IN}=5V$ ,  $I_o=3A$   
Step from 1.5A~3A~1.5A(Vo1)



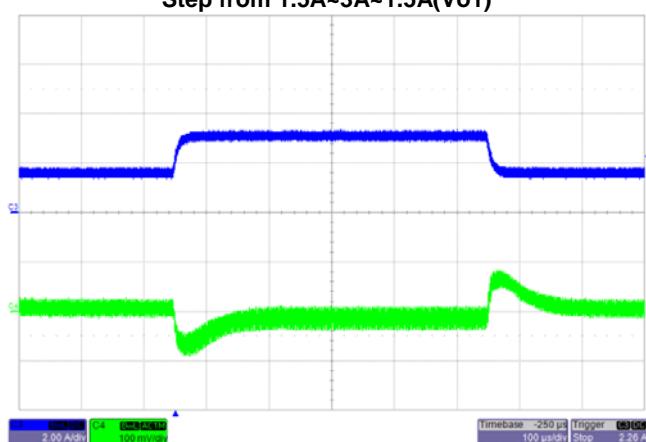
Short-Circuit Output  $V_{IN}=5V$ (Vo1)



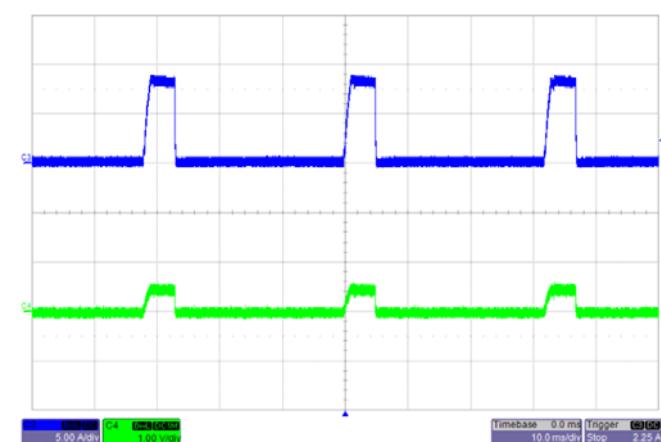
**Transient Response  $V_{IN}=12V$ ,  $I_o=3A$**   
Step from 1.5A~3A~1.5A( $V_{O1}$ )



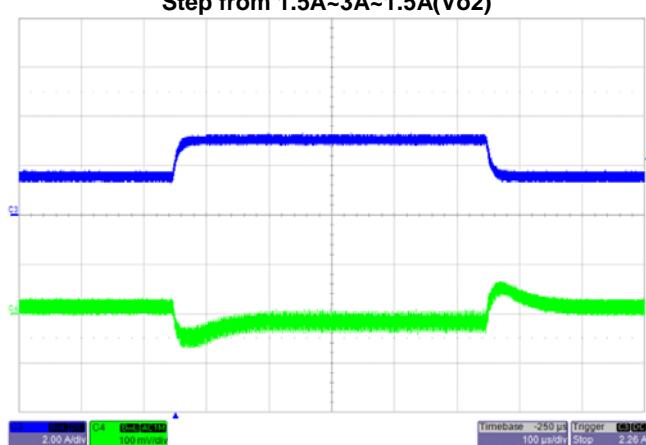
**Short-Circuit Output  $V_{IN}=12V(V_{O1})$**



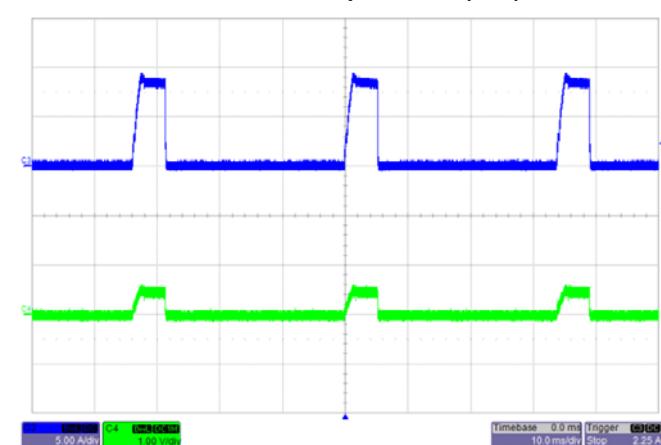
**Transient Response  $V_{IN}=5V$ ,  $I_o=3A$**   
Step from 1.5A~3A~1.5A( $V_{O2}$ )



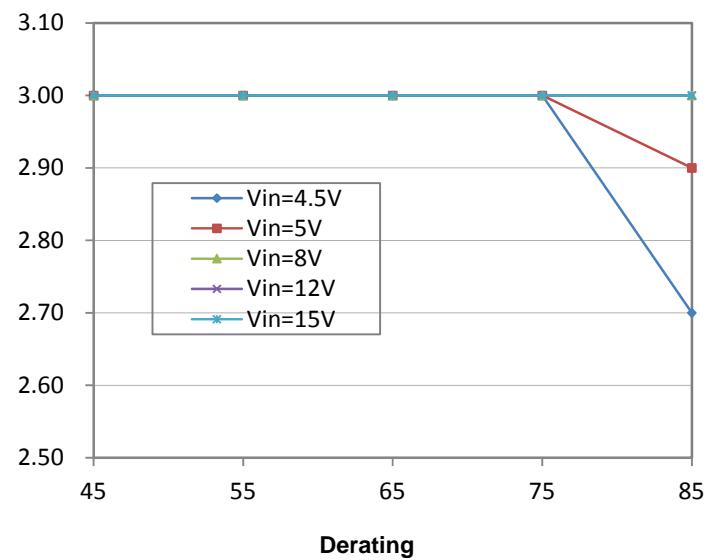
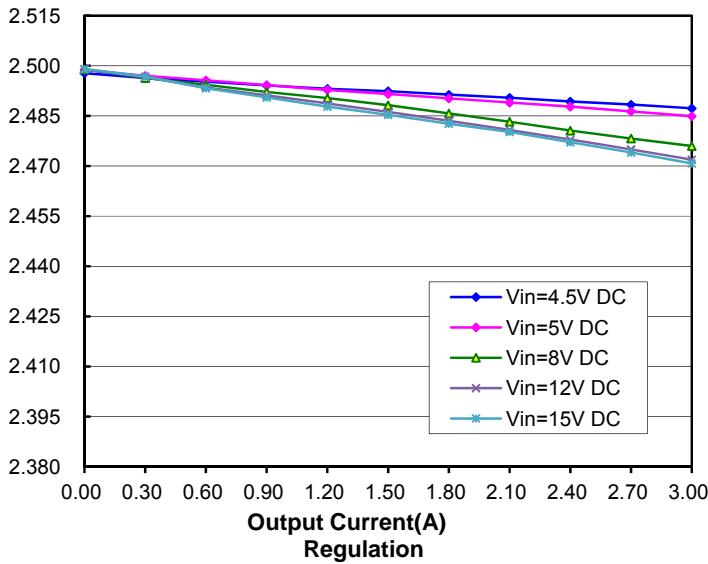
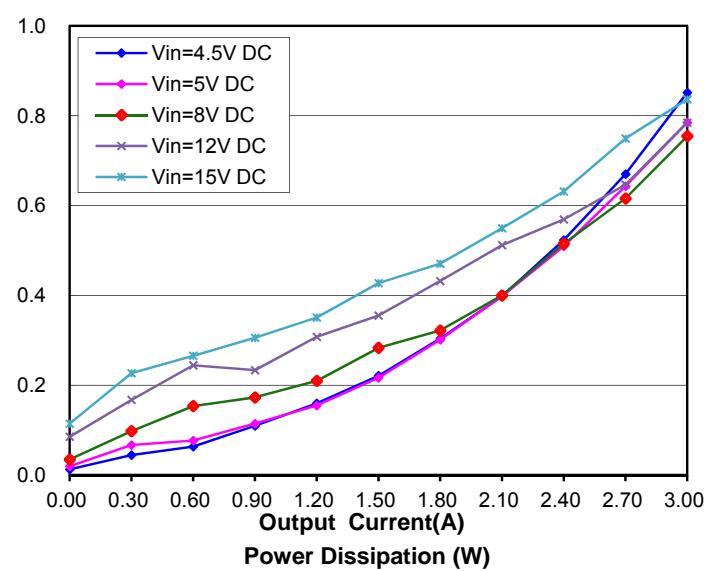
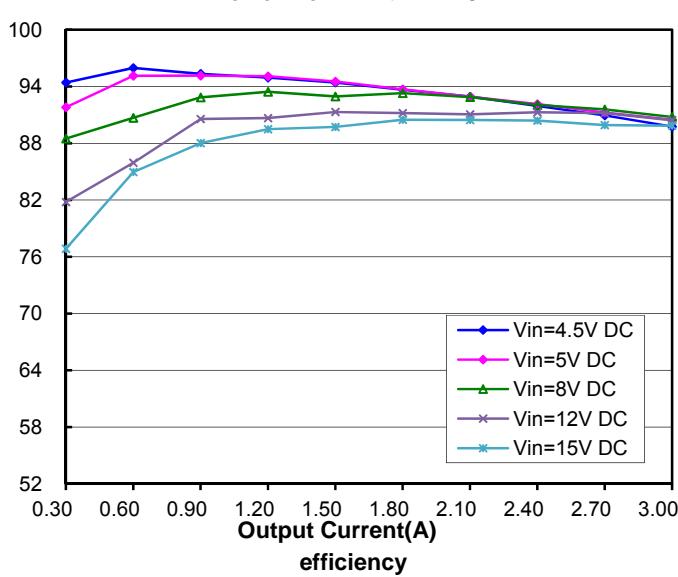
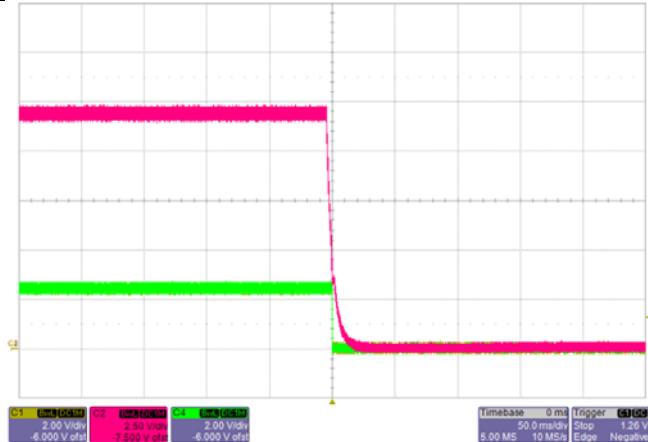
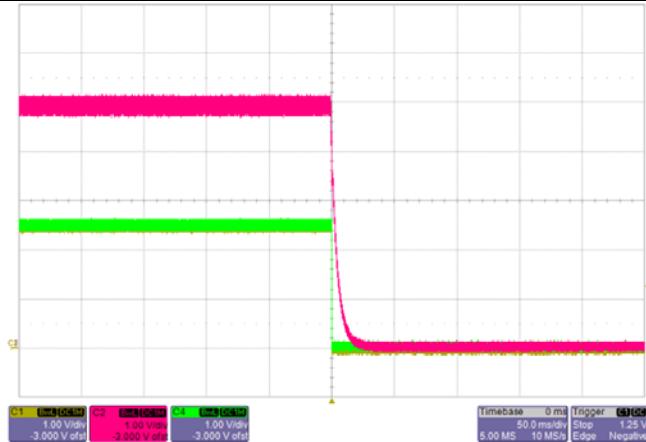
**Short-Circuit Output  $V_{IN}=5V(V_{O2})$**



**Transient Response  $V_{IN}=12V$ ,  $I_o=3A$**   
Step from 1.5A~3A~1.5A( $V_{O2}$ )



**Short-Circuit Output  $V_{IN}=12V(V_{O2})$**

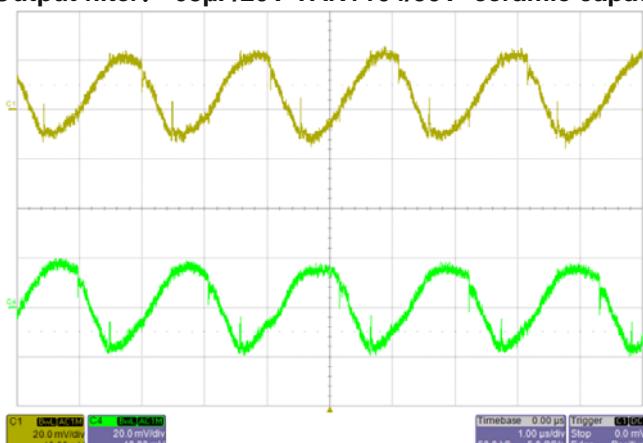
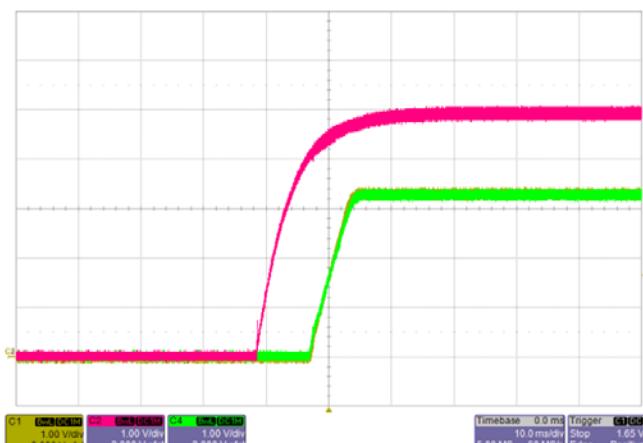
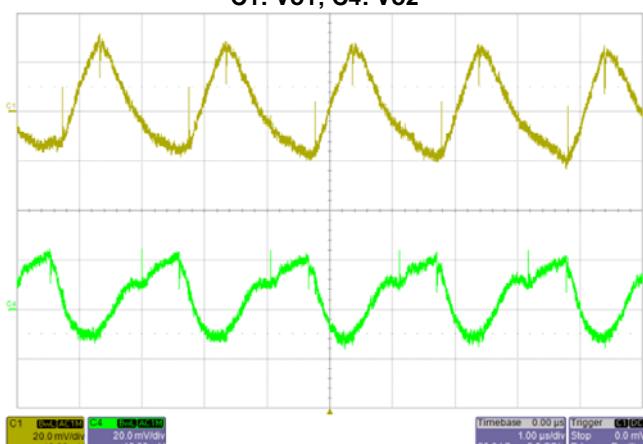
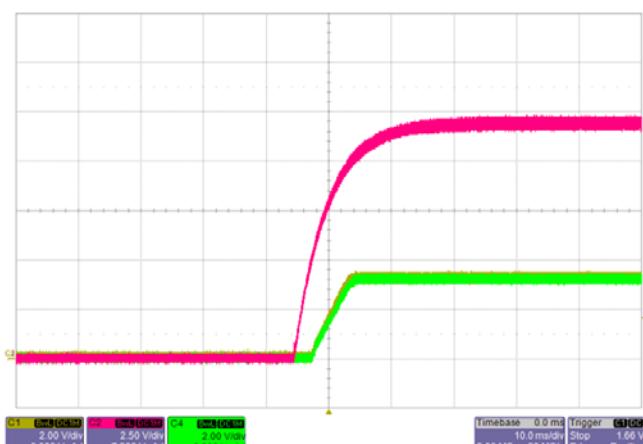
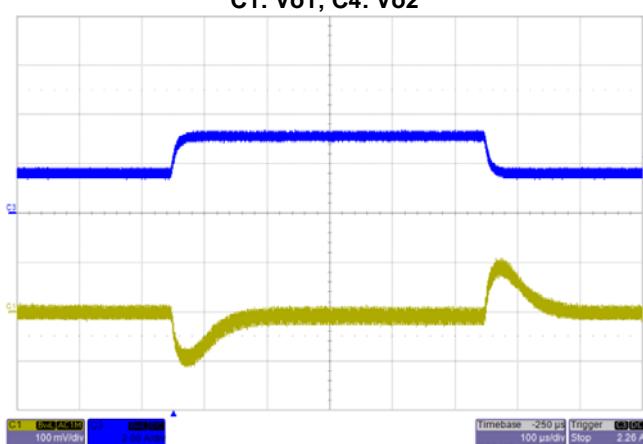
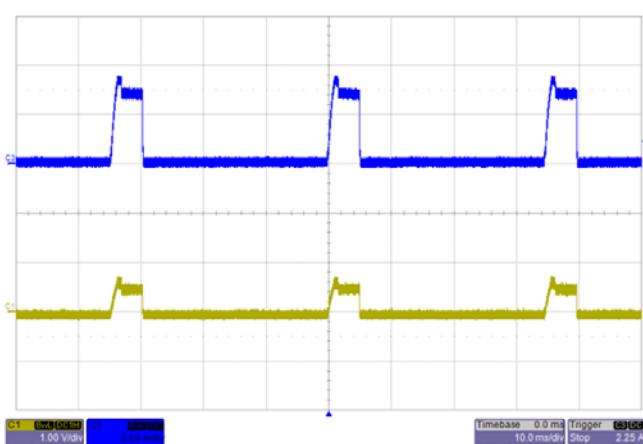


**MQ7221A Typical Characteristics – output adjusted to 3.3V**

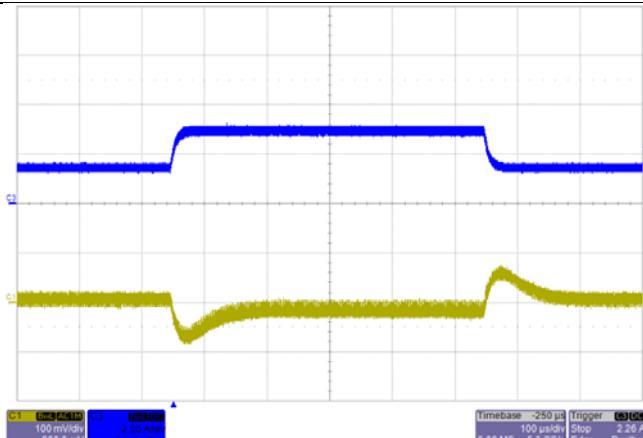
General conditions:

Input filter : 68µF/20Vx1 TAN;

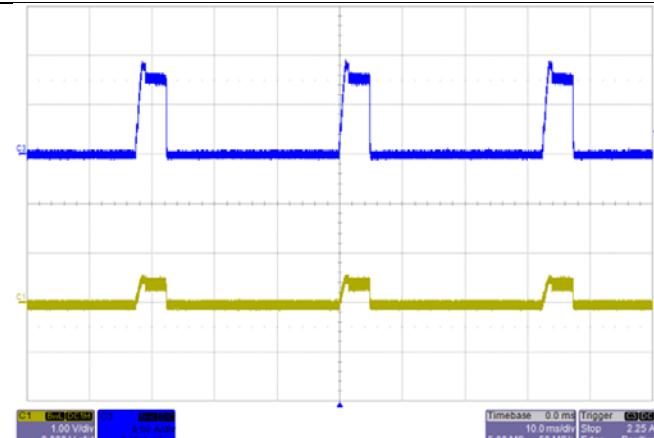
Output filter: 68µF/20V TAN+104/50V ceramic capacitor

Ripple&Noise  $V_{IN}=5V$ ,  $I_o=3A$  for both channel  
C1: Vo1; C4: Vo2Start-up With  $V_{IN}=5V$ ,  $I_o=3A$ Ripple&Noise  $V_{IN}=12V$ ,  $I_o=3A$  for both channel  
C1: Vo1; C4: Vo2Start-up With  $V_{IN}=12V$ ,  $I_o=3A$ Transient Response  $V_{IN}=5V$ ,  $I_o=3A$   
Step from 1.5A~3A~1.5A(Vo1)Short-Circuit Output  $V_{IN}=5V$ (Vo1)

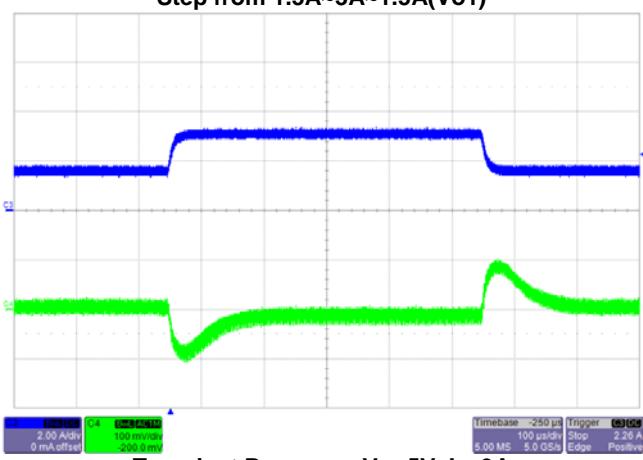
## Micro-BiTarzan™ MQ7221A



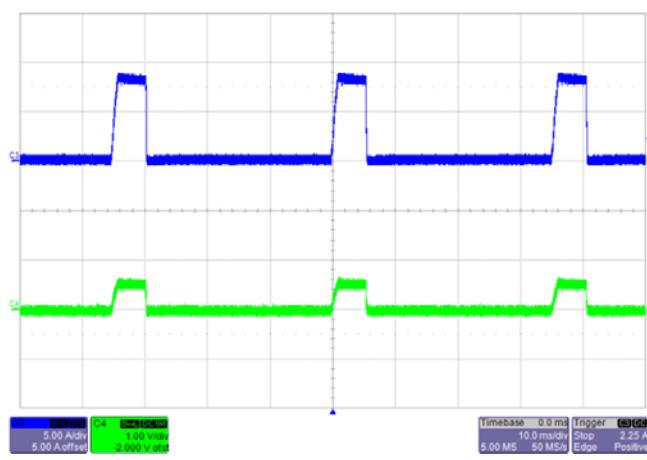
Transient Response  $V_{IN}=12V$ ,  $I_o=3A$   
Step from 1.5A~3A~1.5A( $V_{o1}$ )



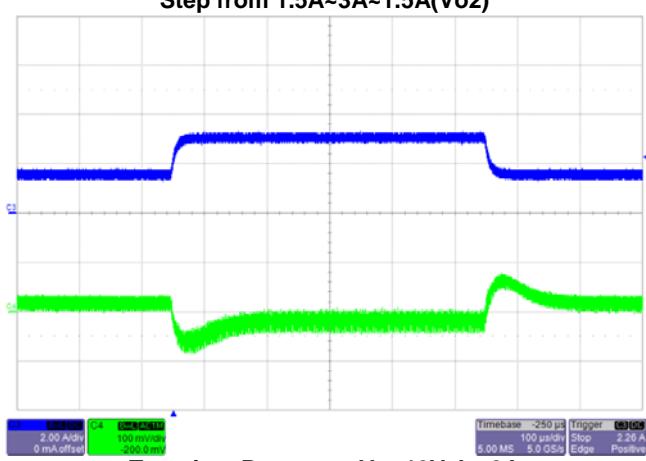
Short-Circuit Output  $V_{IN}=12V(V_{o1})$



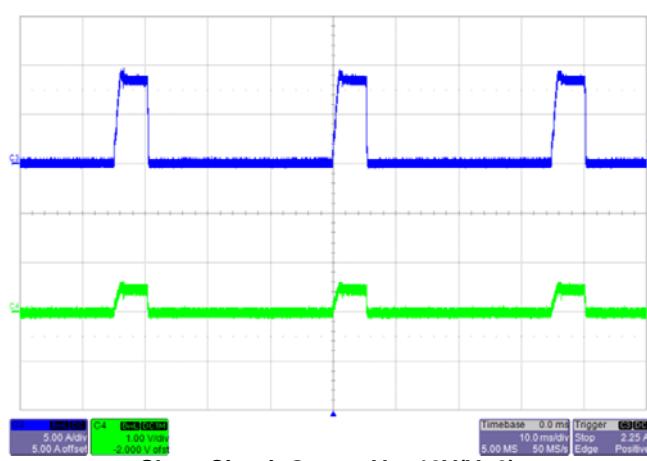
Transient Response  $V_{IN}=5V$ ,  $I_o=3A$   
Step from 1.5A~3A~1.5A( $V_{o2}$ )



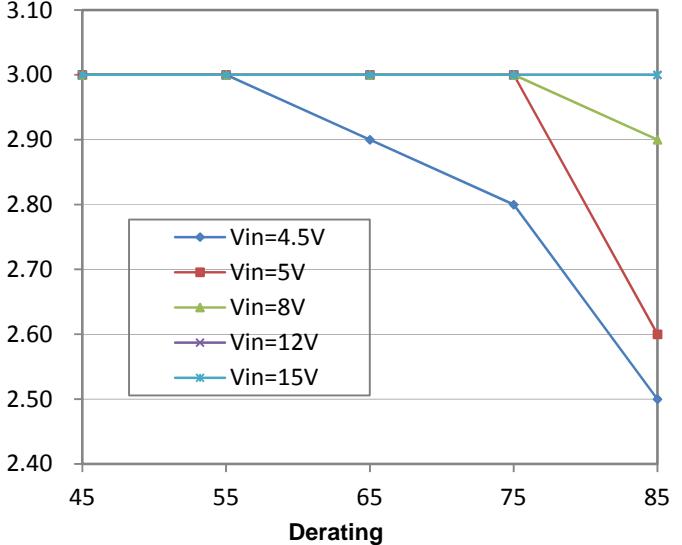
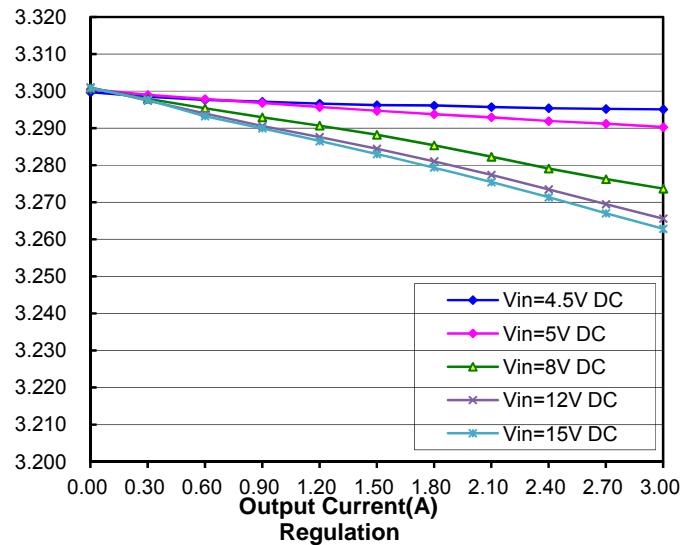
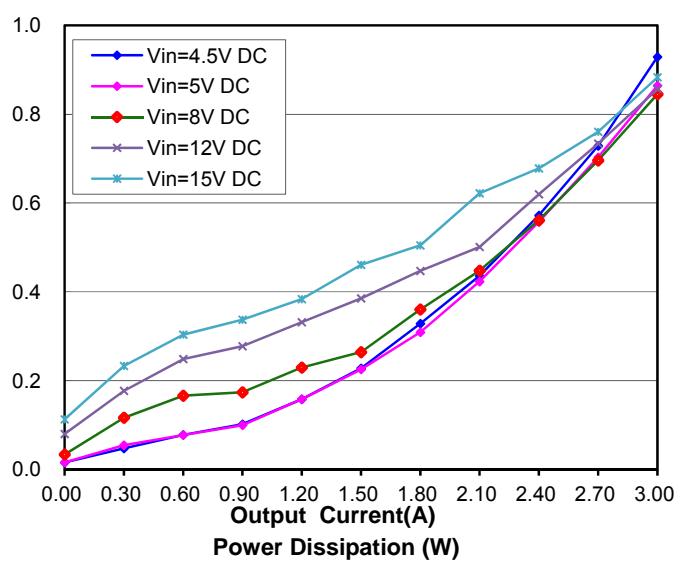
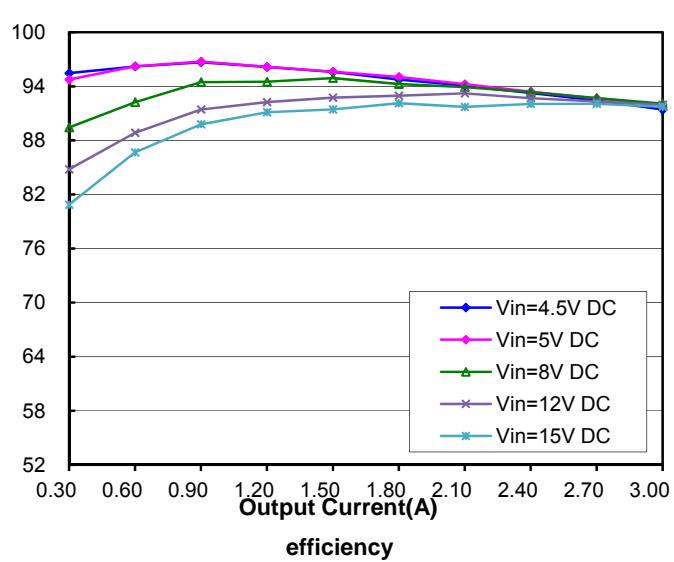
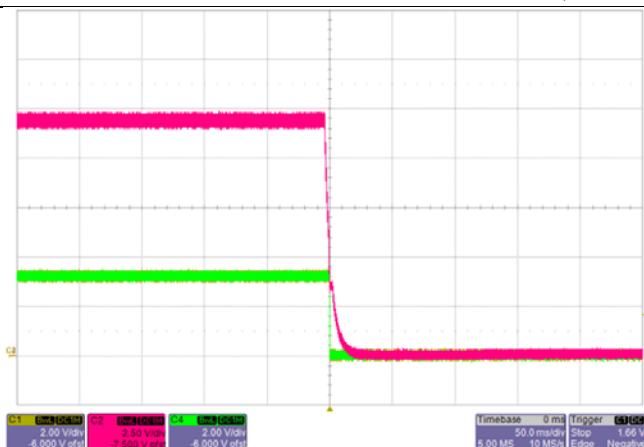
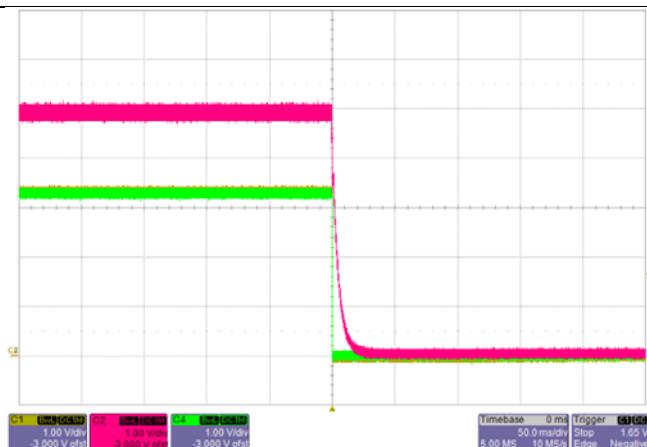
Short-Circuit Output  $V_{IN}=5V(V_{o2})$



Transient Response  $V_{IN}=12V$ ,  $I_o=3A$   
Step from 1.5A~3A~1.5A( $V_{o2}$ )



Short-Circuit Output  $V_{IN}=12V(V_{o2})$

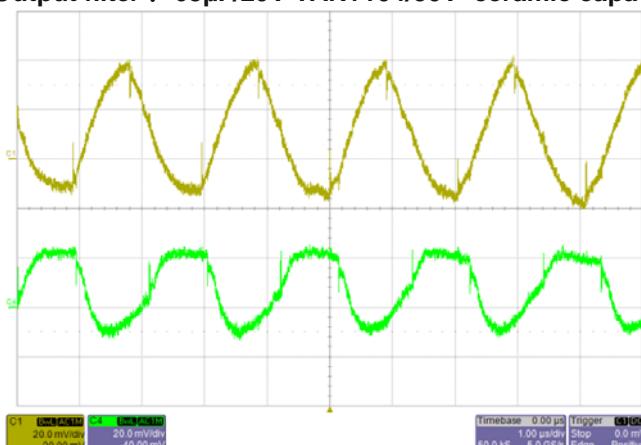


**MQ7221A Typical Characteristics – output adjusted to 5V**

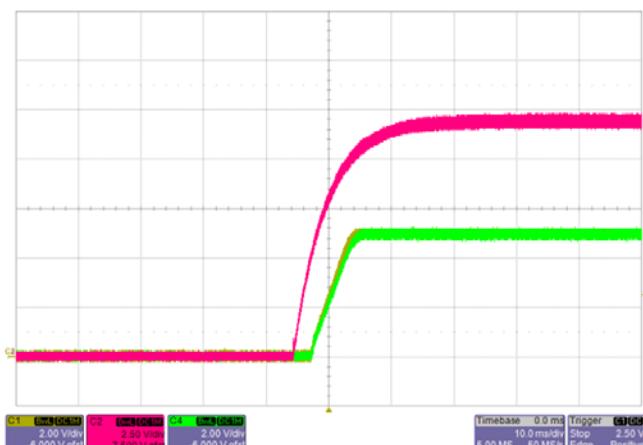
General conditions:

Input filter: 68µF/20Vx1 TAN;

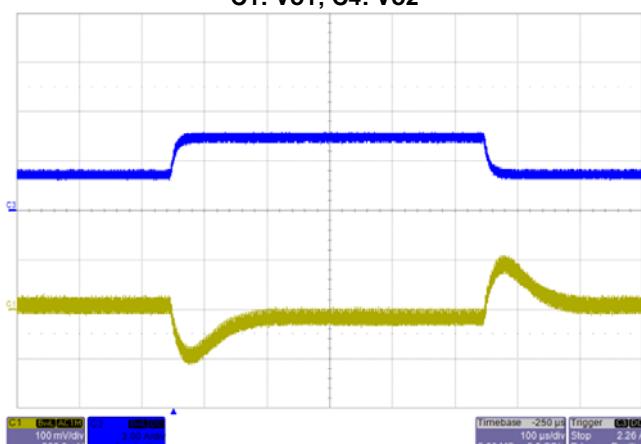
Output filter : 68µF/20V TAN+104/50V ceramic capacitor



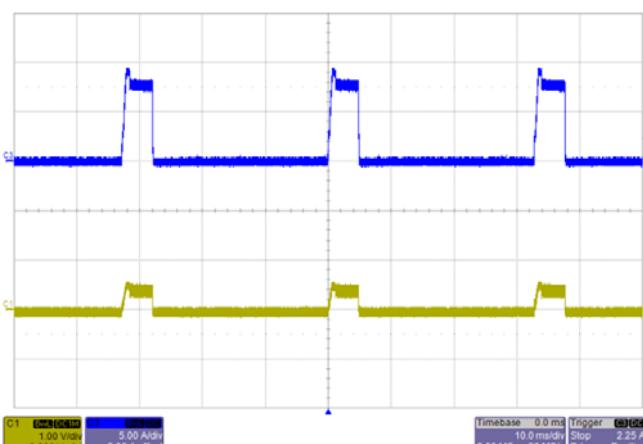
Ripple&Noise  $V_{IN}=12V$ ,  $I_o=3A$  for both channel  
C1: Vo1; C2: Vo2



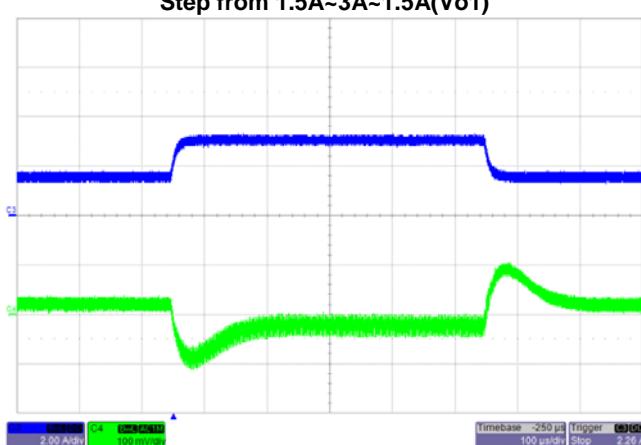
Start-up With  $V_{IN}=12V$ ,  $I_o=3A$



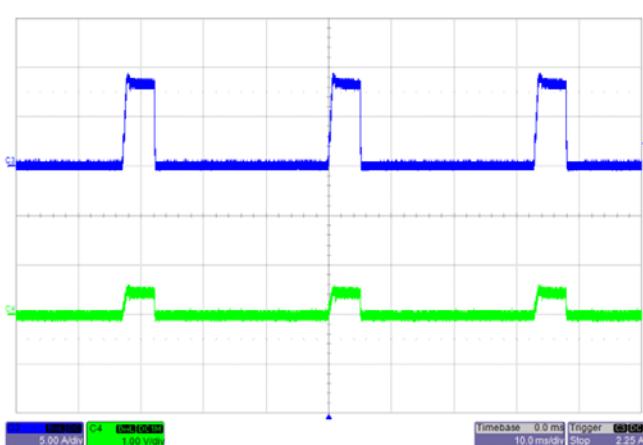
Transient Response  $V_{IN}=12V$ ,  $I_o=3A$   
Step from 1.5A~3A~1.5A(Vo1)



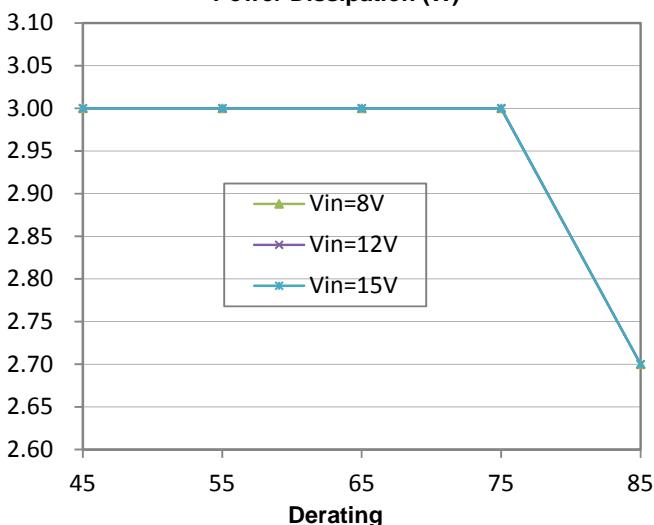
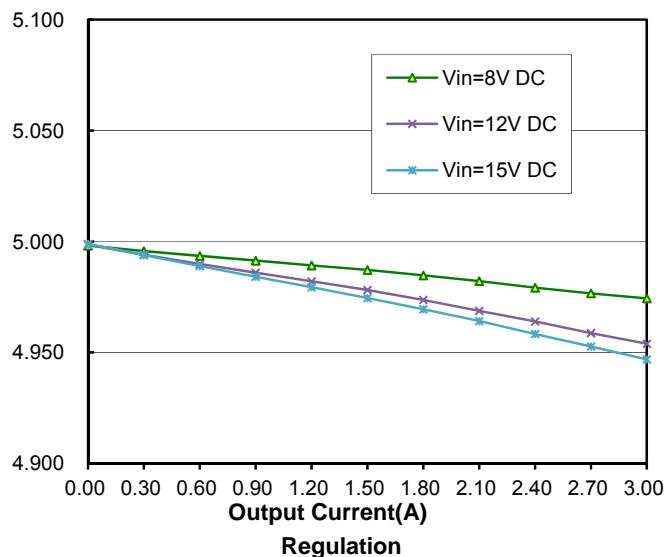
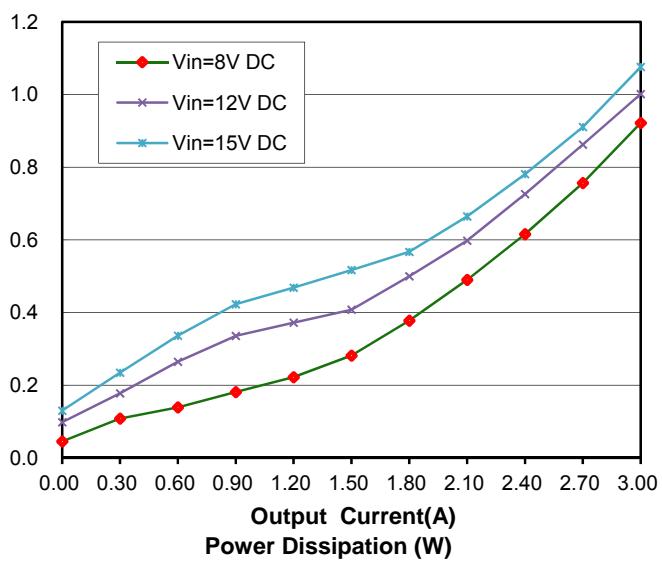
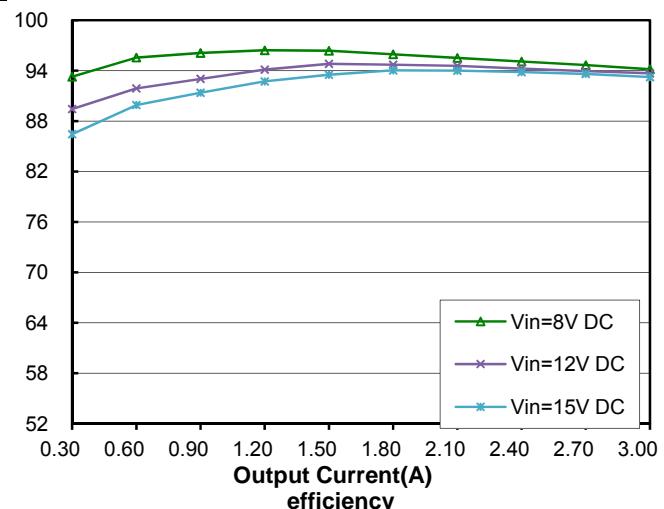
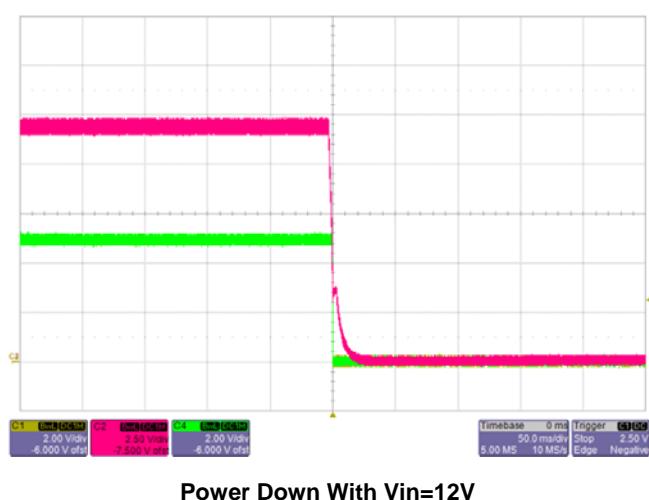
Short-Circuit Output  $V_{IN}=12V$ (Vo1)



Transient Response  $V_{IN}=12V$ ,  $I_o=3A$   
Step from 1.5A~3A~1.5A(Vo2)

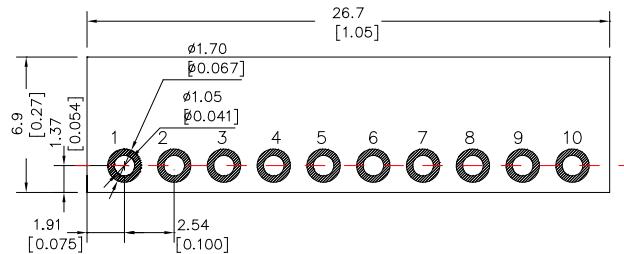


Short-Circuit Output  $V_{IN}=12V$ (Vo2)



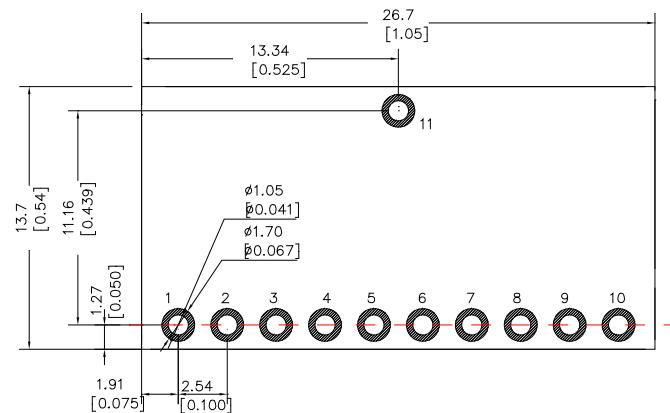
## Recommended Hole Pattern

Dimensions are in millimeters (inches)



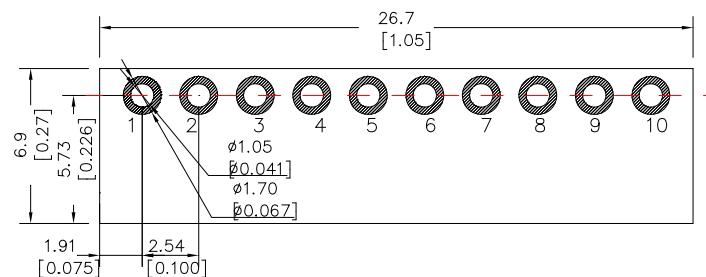
## Recommended Hole Pattern for “-R” option

Dimensions are in millimeters (inches)



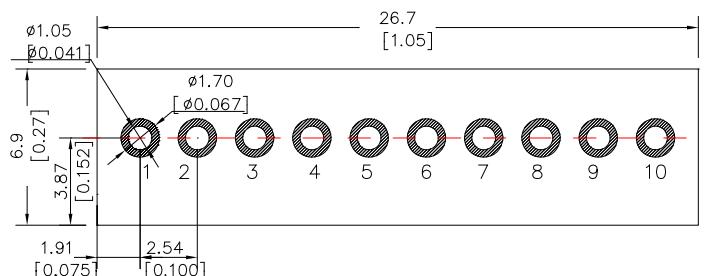
## Recommended Hole Pattern for “-B” option

Dimensions are in millimeters (inches)



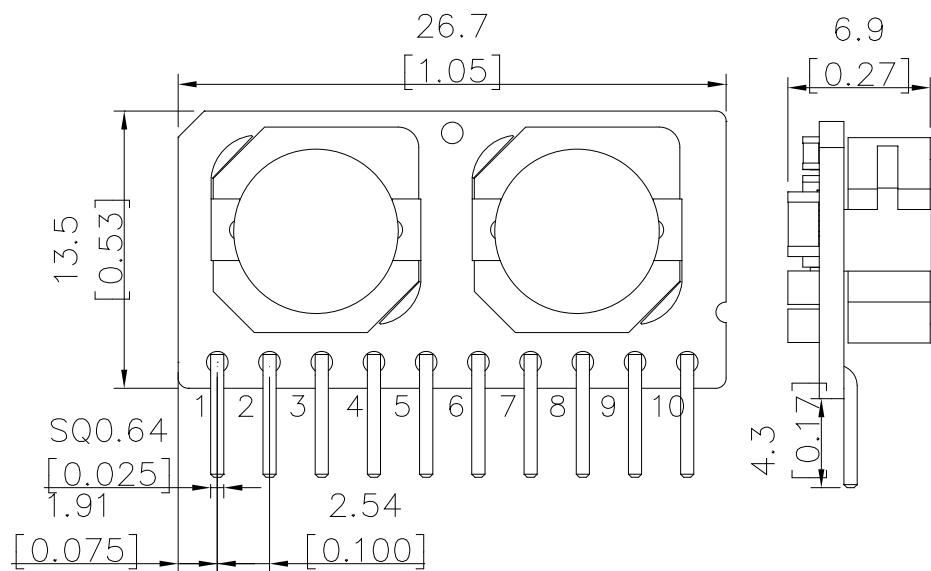
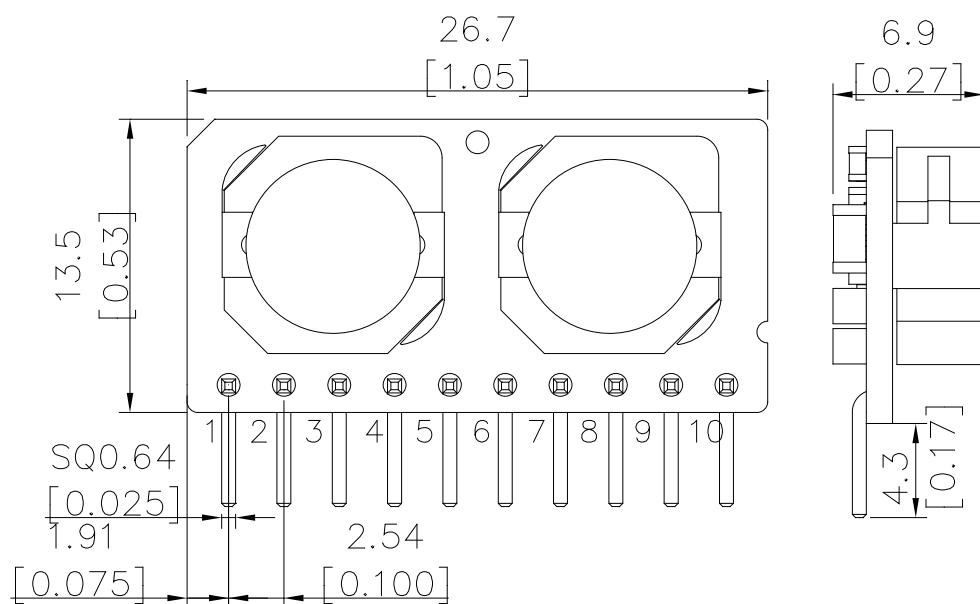
## Recommended Hole Pattern for “-NP” option

Dimensions are in millimeters (inches)



**Mechanical Specifications**

Dimensions are in mm

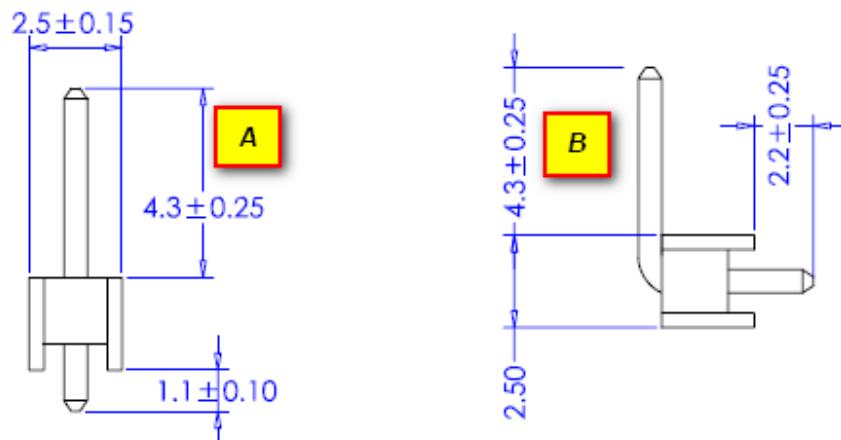
**MQ7221A-NP****MQ7221A-B**

## 声明

以下所列型号的产品，由于产品的 PIN 长度不同，所以为了内部管理方便，对 PIN 长度不同的产品重新命名，两者的其它方面是没有任何差异的，均为 Union 原厂产品。

举例，如 MQ7221A 与 MQ7221A-X 上是同一种产品，但 MQ7221A 的 PIN 长度为  $4.3 \pm 0.25\text{mm}$ ，而 MQ7221A-X 的 PIN 长度为  $3.3 \pm 0.25\text{mm}$ 。

| 标准品         |                         | 新型号          |                         |  |
|-------------|-------------------------|--------------|-------------------------|--|
| 型号          | 用于焊接 PIN 长度             | 型号           | 用于焊接 PIN 长度             |  |
| MQ7221BKG   | $4.3 \pm 0.25\text{mm}$ | MQ7221BKG-X  | $3.3 \pm 0.25\text{mm}$ |  |
| MQ7221BKG-R | $4.3 \pm 0.25\text{mm}$ | MQ7221BKG-RX | $3.3 \pm 0.25\text{mm}$ |  |
| MQ7221A     | $4.3 \pm 0.25\text{mm}$ | MQ7221A-X    | $3.3 \pm 0.25\text{mm}$ |  |
| MQ7221A-R   | $4.3 \pm 0.25\text{mm}$ | MQ7221A-RX   | $3.3 \pm 0.25\text{mm}$ |  |



上海英联电子系统有限公司  
质管部  
2017 年 1 月 11 日